

VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN



ANNUAL REPORT ON FDP/PROFESSIONAL DEVELOPMENT / ADMINISTRATIVE TRAINING PROGRAMMES (2018-2019)



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PRINCIPAL

Vignan's Institute of Management & Technology For Women
Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (Dt)-501301
Telangana State



VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

Sponsored by Lavu Educational Society, Approved by AICTE, New Delhi & Affiliated to JNTUH, Hyderabad.
Kondapur (V), Ghatkesar (M), Medchal - Malkajiri (D) - 501 301 TS INDIA
Phone: +08615 200006/07/08 Email: info@vmtw.in

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Teaching and non-teaching staff participating in Faculty development Programmes (FDP), Management Development Programmes (MDPs) professional development /administrative training programs during the A.Y 2018-19					
Sl. No	Year	Title of the FDP /MDP/ professional development / administrative training program	Dates (from-to) (DD-MM-YYYY)	No. of days	No. of Teaching / Non-Teaching Attended
1	2018-19	FDP ON SELF AWARENESS AND SETTING HIGHER GOALS IN EDUCATION	08-07-2018 to 13-07-2018	One Week	3
2	2018-19	FDP ON DEVELOPMENT AND APPLICATIONS OF NANOCOMPOSITE MATERIALS	09-07-2018 to 14-07-2018	One Week	2
3	2018-19	FIVE DAY NATIONAL LEVEL FDP ON ARTIFICIAL INTELLIGENCE	27-08-2018 to 31-08-2018	5 Days	2
4	2018-19	5 DAY FDP ON VIRTULIZATION USING VSPHERE 6.0	24-09-2018 to 28-09-2018	5 Days	1
5	2018-19	ONE WEEK FDP ON DESIGN OF EXPERIMENTS USING TAGUCHI APPROACH	12-11-2018 to 17-11-2018	One Week	2
6	2018-19	ONE WEEK FDP ON ELECTROMAGNETICS AND ITS APPLICATIONS WITH HANDS-ON SESSION USING MATLAB	26-11-2018 to 01-12-2018	One Week	2
7	2018-19	ONE WEEK FDP ON SECURITY APPLICATIONS USING BLOCKCHAIN TECHNOLOGY	10-12-2018 to 15-10-2018	One Week	2
8	2018-19	FDP ON OUTCOME BASED EDUCATION & CONITINOUS QUALITY IMPROVEMENT (CQI)	10-12-2018 to 14-12-2018	5 Days	4
9	2018-19	FIVE DAY FDP ON LOW POWER CMOS CIRCUITS USING MENTOR GRAPHICS CAD TOOLS	13-12-2018 to 17-12-2018	5 Days	3
10	2018-19	ONE WEEK FDP ON INNOVATIVE TEACHING METHODOLOGY IN SCIENCE AND TECHNOLOGY	16-12-2018 to 21-12-2018	One Week	4
11	2018-19	ONLINE FDP ON "MACHINE LEARNING AND DEEP LEARNING CONCEPTS USING PYTHON"	18-12-2018 to 23-12-2018	One Week	2
12	2018-19	ONLINE FDP ON "CMOS MIXED SIGNAL DESIGN".	07-01-2019 to 11-01-2019	One Week	2




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13	2018-19	ONE WEEK FDP ON INTERNET OF THINGS	28-01-2019 to 02-02-2019	One Week	2
14	2018-19	ONE WEEK FDP ON DATA ANALYTICS & MACHINE LEARNING USING PYTHON, RPROGRAMMING, TABLEAU AND SPSS	11-02-2019 to 16-02-2019	One Week	2
15	2018-19	ONE WEEK WORKSHOP ON INTELLECTUAL PROPERTY RIGHTS AND INNOVATION	21-02-2019 to 27-02-2019	One Week	3
16	2018-19	TWO WEEK FDP ON BLOCKCHAIN TECHNOLOGIES	17-06-2019 to 28-06-2019	Two Week	3
17	2018-19	ONE WEEK FDP ON MOBILE APPLICATION DEVELOPMENT	18-06-2019 to 22-06-2019	One Week	2
18	2018-19	ONE WEEK REFRESHER COURSE ON PROBLEM SOLVING THROUGH PROGRAMMING LANGUAGES	24-06-2018 to 28-06-2019	One Week	2
19	2018-19	A FIVE DAY WORKSHOP ON EFFECTIVE COMMUNICATION SKILLS	16-07-2018 to 20-07-2018	5 Days	30



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Brief Summary of Annual Report

Our Faculty had participated in Refresher/FDP/PDP/Administrative Training Programmes at various Colleges during the A.Y 2018-19. The distinct program list shown above .

1. Online FDP on “Machine Learning and Deep Learning Concepts Using Python”

One Week FDP on “Machine Learning and Deep Learning Concepts Using Python” conducted by Department of Computer Science and Engineering from 18-12-2018 to 23-12-2018. The resource persons for this program are Dr. P.S. Murthy, Professor in CSE, University College of Science, ANU, Guntur and Dr. Ch. Aparna, Professor in CSE, RVR & JC College of Engineering, Guntur. They have been explained the detail about the Introduction-Data Science, ML, DL & AI, Concept of Supervise & Unsupervised Machine Learning, Practical implementation of Supervised Machine Learning Algorithm, Practical implementation of Unsupervised Machine Learning Algorithm, Introduction To Deep Learning and Neural Networks and Deep dive into Neural Networks.

2. Online FDP on “CMOS Mixed Signal Design”.

Five days FDP on “CMOS Mixed Signal Design” was organised by Department of Electronics and Communication Engineering from 07-01-2019 to 11-01-2019. DR B. SeethaRamanjaneyulu, Professor, VFSTR, Dr T. Pitchaiah, Professor, VFSTR, Dr. Shaik Jakeer Hussain, Professor, VFSTR, Guntur Delivered the talk and conducted hands on sessions. The resource person has explained in detail about the recent trends on CMOS Mixed Signal Design and its concepts. The same concepts practiced with real time problems Mentor Graphics tool. Total 54 certificates were issued for this FDP.

3A Five-Day Workshop On Effective Communication Skills

Department of Basic Science & Humanities conducted workshop on – “Effective Communication Skills”, during 16-07-2018 to 20-07-2018 for Non – Teaching Faculty,

The main objective of this program is to provide deep knowledge in Communication Skills.

- To analyze the theoretical skills and strategies to be an effective communicator.
- To reflect with greater confidence and create positive impression in the classroom.
- To develop a learning culture to enrich knowledge where communication is both receptive and expressive




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15-12-2018

From:
Mrs.D.Swaroopa,
Assistant Professor (CSE)
VMTW.

To
The Principal,
VMTW,
Kondapur,Ghatkesar.

Respected Sir,

Subject: Request for approval to conduct Online FDP on “Machine Learning and Deep Learning Concepts Using Python” .

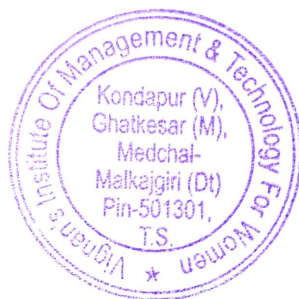
I, Mrs.D.Swaroopa, CSE , VMTW here by request your approval to conduct a One Week Online Faculty Development Program on “**Machine Learning and Deep Learning Concepts Using Python**” for all Dept’s . We would like to conduct this program from 18-12-2018 to 23-12-2018. So please grant us permission for conducting the FDP.

Thanking you Sir,

Your Sincerely

Swaroopa

Mrs.D.Swaroopa,
Assistant Professor
VMTW.



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Registration Form

Name:

Date of Birth & Age:

Gender (M/F):

Qualifications:

A). Institution:

B). Affiliating University:

Address for Communication:

Phone No (1):

Phone No (2):

Office:

Email:

Resource Person:

Dr. P.S. Murthy,

Professor in CSE,
University College of Science, ANU, Guntur.

Ch. Aparna,

Professor in CSE,
RVR & JC College of Engineering, Guntur.

Chief-patrons:

Dr. L. Rathaiah,

Chairman, Vignan Group of Institutions.

Mr. B. Shravan,

CEO, Vignan Group of Institutions

Patron:

Dr. P. Sudhakara Rao,

Principal, VMTW

Mrs. Indrani Vasireddy,

Associate Professor & HOD, CSE.

Co-ordinator:

Mrs. D. Swaroopa,

Assistant Professor, CSE.

Faculty Co-ordinator:

Mrs. A. Amara Jyothi, Assistant Professor, CSE.

Mr. Ravi Surarapu, Assistant Professor, CSE.

Mr. Nagaraju Narapogu, Assistant Professor, CSE.

Mrs. Veena Chenigaram, Assistant Professor, CSE.

For Registration:

Mr. Nitesh Vengala, Assistant Professor, CSE.

Contact No: +91 8801179588

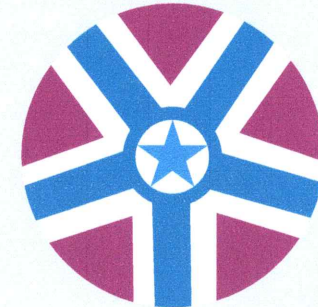
Last Date For Registration: 14th Dec, 2018

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FACULTY DEVELOPMENT PROGRAM

**“MACHINE LEARNING AND
DEEP LEARNING CONCEPTS
USING PYTHON”**

18TH TO 23RD DEC, 2018

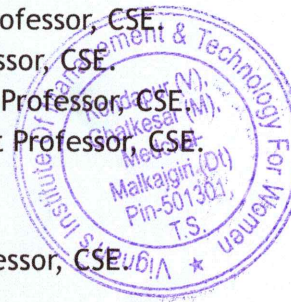
Venue:

**‘A’-Block Seminar Hall,
VMTW.**

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Organized By:

**DEPARTMENT OF
COMPUTER SCIENCE AND ENGINEERING**



About the Institution:

Vignan's Institute of Management and Technology for Women (VMTW), is the brainchild of Dr. L. Rathaiyah, Chairman, Vignan Group of Institutions, which was founded in August 2008, with four branches of Engineering.

VMTW is an exemplary institution of higher learning with a mission of pursuing excellence in education and research. The institution, with its diverse and dynamic community of about 2000 students offers a distinctive combination of some of the finest facilities for 7 different graduate, and undergraduate programs, accomplished faculty, world-class facilities with a hostel set on a sprawling area of 22 acres sylvan surroundings of mango groves and greenery.

While students at VMTW immerse themselves in academics, the college has a lot in store for them outside the classroom. Student life includes participation in sports, recreational & co-curricular, and cultural activities. In short, at VMTW students will find an academic and social environment where everyone from faculty members to peers helps shape their future.

VMTW is a home to aesthetically designed buildings with state-of-the-art computer and internet facilities, modern laboratories, workshops, seminar halls, auditoriums, and well-stocked libraries, sports, and games fields.

The Institution boasts of a strong alumni network with alumni events held every year serving as a platform for past students to give back to VMTW and share their experiences with its present fellow students. With so much to offer, it is only natural that students of VMTW get a unique opportunity to carve a niche for themselves in their chosen field of study that enables them to become well-rounded and discerning citizens, fully qualified for their chosen professions in the workplace. Institute also brings out the creativity in students by imparting engineering knowledge imbibing interpersonal skills to promote innovation, research, and entrepreneurship.

About the Department:

Department of CSE, VMTW has started B.Tech in Computer Science and Engineering in the year 2008 with an intake of 90. The scholarly ambiance in CSE Department is encouraging to the complete development of the students with well-equipped labs. Department has dedicated and professionally well-competent faculty and the many of the faculty are pursuing Ph.D. in emerging areas like Artificial Intelligence, Machine Learning, Cyber Security, Data Science and Block Chain. The main objective is to prepare the students to be successful in Computer Science Engineer to meet the industry needs, encourage lifelong skills and strengthen research.

About the Program:

The goal of this Programme is to educate and enhance knowledge on Python programming along with current research studies and challenges as well as practical experiences in the field of Deep Learning. All participants will gain understanding on enhancement from machine learning to Deep Learning. Exposure to some most practical and applicable Deep learning techniques. Hands on Python programming in conjunction with deep learning. Faculty will learn concepts of neural networks. They will be educated to implement Multilayer Perceptron, Convolutional Neural networks using python.

Objectives:

Learn the concept, syntax, semantics and gain introductory knowledge in Machine Learning principles through examples that provide useful exposure to the various concepts of Machine Learning.

- Machine Learning-Introduction
- Machine Learning-Fundamental
- Python-Basics
- Core Concepts of Python (Required for ML Practical Implementation)
- Steps of Machine Learning Implementations
- Data Collection for Machine Learning
- Concept of Supervise & Unsupervised Machine Learning

- Data Visualization for Machine Learning using Matplotlib
- Practical implementation of Supervised Machine Learning Algorithm
- Practical implementation of Unsupervised Machine Learning Algorithm

Expected Outcomes:


At the end, participants will be able to:

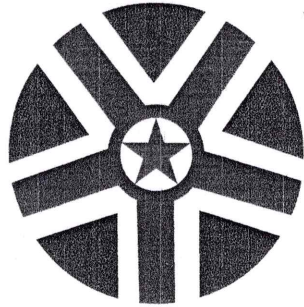
- To provide foundation of Python scripting and its principles.
- To design and implement real-world inspired Machine Learning algorithm applications.
- To teach high-value forecasts that can direct smarter decisions and smart behavior in real-time without human interference.
- To provide insights about the roles of a data scientist and enable you to analyse big data.
- To teach techniques and tools for transformation of data, understand data mining and data visualization and optimization.
- To familiarize you with different formats and make you figure out Machine Learning algorithms

Focus Areas:

At the end, participants will be able to:

- Introduction to packages in Python
- Tuples, Set, Dictionaries & Functions
- Numpy, Scipy, Pandas, EDA
- Supervised and Unsupervised Learning
- Statistics, Hypothesis Testing, Distributions
- Association Rule Mining and Recommendation System
- Linear Regression, Logistic Regression
- Decision Tree, Random Forest
- Naïve Bayes Classifier, Support Vector Machines
- Basics of Neural Network and Deep Learning
- Server Deployment using Flask


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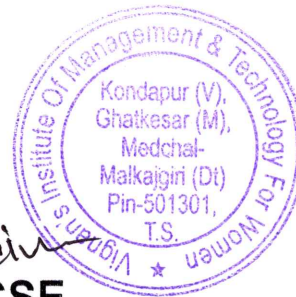
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Certificate of Participation

This is to certify that Dr./Mr./Ms. S. DILLI BABU, VMTW..... has participated in Faculty Development Program on “Machine Learning and Deep Learning Concepts Using Python” held from 18th to 23rd December, 2018, Organized by the Department of Computer Science and Engineering.

Swaroop
Co-ordinator

V. Indira
HoD, CSE



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Telangana State

Budhakar
Principal



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Email: info@vmtw.in

A Report on Machine Learning and Deep Learning Concepts Using Python

Date: 27-12-2018

Department of Computer Science & Engineering conducted **FDP on "Machine Learning and Deep Learning Concepts Using Python"**, from 18-12-2018 to 23-12-2018 for Faculty.

Speakers: Dr. P.S. Murthy, Professor in CSE, University College of Science, ANU, Guntur and Dr. Ch. Aparna, Professor in CSE, RVR & JC College of Engineering, Guntur

The main objective of this program is to inculcate the knowledge on latest technologies of **Machine Learning and Deep Learning Concepts Using Python**, to motivate Faculty.

Coordinators: Mrs.D.Swaroop, Assistant Professor, from CSE Department acted as Coordinators for this FDP.

Total 50 Number of members had participated in the FDP. This FDP session had provided an excellent opportunity to learn about Machine Learning and Deep Learning Concepts Using Python to all the participants.

We would like to thank our honourable Chairman Sri Dr. L Rathaiahgaru , CEO Sri B. Shraavan garu, Principal Dr.P.Sudhakar Raogaru and Smt. Indrani garu, HoD of CSE for their support for successful completion of one week FDP.

The Faculty participants felt very happy for attending the program and most of them given excellent in feedback.




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03-01-2019

From:
Mr.J.Sunil Kumar.
Assistant Professor (ECE)
VMTW.

To
The Principal,
VMTW,
Kondapur, Ghatkesar.

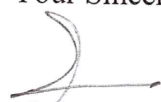
Respected Sir,

Subject: Request for approval to conduct One Week Faculty Development Program on “CMOS Mixed Signal Design” .

I, Mr. J.Sunil Kumar, Assistant Professor, ECE , VMTW here by request your approval to conduct a One Week Faculty Development Program on “**CMOS Mixed Signal Design**” for all Dept's . We would like to conduct this program from 07-01-2019 to 11-01-2019. So please grant us permission for conducting the FDP.

Thanking you Sir,

Your Sincerely


Mr. J. Sunil Kumar,
Assistant Professor
ECE Dept.




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Registration Form

Name:

Date of Birth & Age:

Gender (M/F):

Qualifications:

A). Institution:

B). Affiliating University:

Address for Communication:

Phone No (1):

Phone No (2):

Office:

Email:

Resource Person:

Dr. B. Seetha Ramananeyulu,
Professor, VFSTR,

Dr. T. Pitchaiah,
Professor, VFSTR,

Dr. Shaik Jakeer Hussain,
Professor, VFSTR,

Chief-patrons:

Dr. L. Rathaiah,
Chairman,
Vignan Group of Institutions.

Mr. B. Shravan,
CEO,
Vignan Group of Institutions

Patron:

Dr. P. Sudhakara Rao,
Principal, VMTW

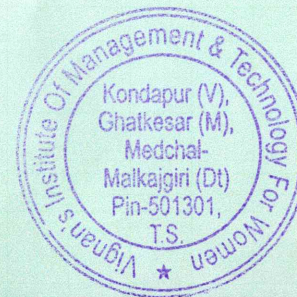
Mrs. A. Narmada,
Associate Professor & HOD, ECE.

Co-ordinator:

Mr. P. Hari Krishna,
Associate Professor, ECE.

Faculty Co-ordinator:

Mr. J. Sunil Kumar,
Assistant Professor, ECE.
Contact: +91 8374717668

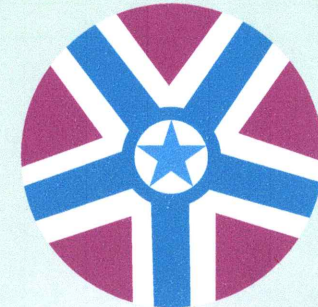


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FACULTY DEVELOPMENT PROGRAM

“CMOS MIXED SIGNAL DESIGN”

7TH TO 11TH JAN, 2019

Venue:

**'A'-Block Seminar Hall,
VMTW.**


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Telangana State

Organized By:

**DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING**

Last Date For Registration: 4th Jan, 2019

About the Institution:

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While students at VMTW immerse themselves in academics, the college has a lot in store for them outside the classroom. Student life includes participation in sports, recreational & co-curricular, and cultural activities. In short, at VMTW students will find an academic and social environment where everyone from faculty members to peers helps shape their future.

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About the Department:

Department of Electronics and Communication Engineering was started since the inception of Vignan's Institute of Management and Technology for Women during 2008 with an initial intake of 90. The strength was enhanced to 120 during 2010. The Department had added Post graduate programme in VLSI during the year 2011 and Embedded Systems during 2014 with an intake of 18 each.

The Department has state-of-art laboratories equipped with advanced and well maintained equipment, continuously updated application software packages, more than adequate computing systems with 24x7, 30 MBPS internet facility.

About the Program:

5G network is the fifth-generation cellular network technology. It is more efficient compared to the first, second, third and fourth generation technology networks for they easily get congested with information and slow down the desired speed by the users. It suffices to say that the 5G network surmounts all the challenges faced by the networks before it since the beginning of the new millennium in 2000. The 5G technology has both its opportunities and attendant challenges.

Artificial intelligence is the trending technology of the future. Machine learning is an application of Artificial intelligence that provides systems the ability to automatically learn and improve from experience without being explicitly programmed. With the expansion of volumes and complexity of data AI and machine learning are used for its processing and analyzing. Industries apply AI and ML to improve performance and propel product development.

This program focuses on exploring various research Opportunities and challenges in the field of mixed signal IC design. Mixed-signal ICs are integrated circuits that contain both analog and digital circuitry on one chip. An analog signal is a continuous time-varying signal, and a digital

signal is a noncontinuous signal that takes on only a finite number of values. Mixed signal ICs make use of both types of signals. Mixed-signal ICs are used in a wide variety of applications and application-specific integrated circuits (ASICs). The primary goal of the FDP is to bring awareness on CADENCE for mixed signal IC designing and to promote research and development activities and learn the advanced topics used in designing of VLSI.

Course Objectives:

- Overview of IC Design
- Aspects of IC Design
- Introduction to device characteristics
- Biasing styles Single-Stage Amplifiers
- Cascode Amplifier Gain Boosting Techniques
- Differential amplifiers and Current Mirrors
- Frequency Response of amplifiers and Noise
- Op-Amp Design and Compensation
- Mixed Signal Design/ RF IC Design

Expected Outcomes:

- Mixed-Signal and RF Design Platforms
- Mixed Signal and RF Verification
- CMOS Data Converters -- Analog-to-Digital Converter (ADC)/Digital-to-Analog Converter (DAC)
- Analog Layout Generation EDA Tool
- Analog Front-End Circuit (AFE)
- Analog/Mixed-Signal IC Design


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Department of Electronics and Communication Engineering

CMOS Mixed Signal Design

Program Schedule:

The Department of ECE is going to organize a One week FDP on "CMOS Mixed Signal Design" during 07-01-2019 to 11-01-2019. The following is the detailed schedule of the program.

Day	Name of the Topic
Day1	Overview & Aspects of IC Design
Day 2	Introduction to device characteristics, Digital, Analog and Mixed signal IC design Concepts
Day 3	Differential amplifiers and Current Mirror
Day 4	Cascode Amplifier, Gain Boosting Techniques
Day 5	Mixed Signal Design/ RF IC Design & Hands-on session.

Resource Person:

The resource persons for the one week FDP are :

1. DR B. SeethaRamanjaneyulu, Professor, VFSTR,
2. Dr T. Pitchaiah, Professor, VFSTR,
3. Dr.ShaikJakeer Hussain, Professor, VFSTR, Guntur

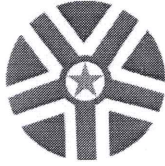
Coordinator



HOD

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Department of Electronics & Communication Engineering

Faculty Development Program Report on

CMOS Mixed Signal Design

During 07-01-2019 to 11-01-2019

The main objective of this program is to explore the VLSI technology design in the field of Mixed Signal IC as “**CMOS Mixed Signal Design**”.

Total 54 Number of members had participated in the FDP. This FDP session had provided awareness in technological advancements in the area of VLSI Design.

Topics Covered:

The program was started on 07-01-2019, where the speaker has focused on various VLSI design aspects and IC industry requirements and specifications with the required technologies and the rapid growth in technological advancements.

Also mentioned the pros and cons of each technology and focussed much on digital, analog and mixed signal IC design & its uses in broad area applications.

This program focuses on exploring various research Opportunities and challenges in the field of mixed signal IC design. Mixed-signal ICs are integrated circuits that contain both analog and digital circuitry on one chip. An analog signal is a continuous time-varying signal, and a digital signal is a non continuous signal that takes on only a finite number of values. Mixed signal ICs make use of both types of signals. Mixed-signal ICs are used in a wide variety of applications and application-specific integrated circuits (ASICs). The primary goal of the FDP is to bring awareness on CADENCE for mixed signal IC designing and to promote research and development activities and learn the advanced topics used in designing of VLSI.

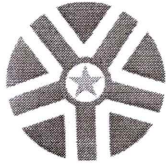
Design & development:

Typically, mixed-signal chips perform some whole function or sub-function in a larger assembly, such as the radio subsystem of a cell phone.




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A mixed-signal IC minimizes off-chip interconnects between digital and analog functionality in the system—typically reducing size and weight due to minimized packaging and a smaller module substrate—and therefore increases the reliability of the system.

Because of the use of both digital signal processing and analog circuitry, mixed-signal ICs are usually designed for a very specific purpose. Their design requires a high level of expertise and careful use of computer aided design (CAD) tools.

There are several particular challenges of mixed-signal circuit manufacturing:

- CMOS technology is usually optimal for digital performance, while bipolar junction transistors are usually optimal for analog performance.
- The advent of technologies like high performance CMOS, BiCMOS, CMOS SOI, and SiGe have removed many of these former compromises.
- Testing functional operation of mixed-signal ICs remains complex, expensive, and often is a "one-off" implementation task.
- Systematic design methods of analog and mixed-signal circuits are far more primitive than digital circuits.

Applications:

- Used in mobile phones, modern radio and telecommunication systems.
- Systems with on-chip standardized digital interfaces (including I2C, UART, SPI, or CAN).
- Voice-related signal processing, aerospace and space electronics, the Internet of things (IoT).
- Mixed-signal circuits or systems are typically cost-effective solutions, such as for building modern consumer electronics and in industrial, medical, measurement, and space applications.
- Examples of mixed-signal integrated circuits include data converters using delta-sigma modulation, analog-to-digital converters and digital-to-analog converters using error detection and correction, and digital radio chips.
- Digitally controlled sound chips are also mixed-signal circuits. With the advent of cellular and network technology, this category now includes cellular telephone, software radio, and LAN and WAN router integrated circuits.

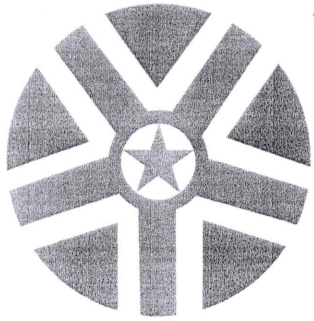

Mr. J. Sunil Kumar

Coordinator




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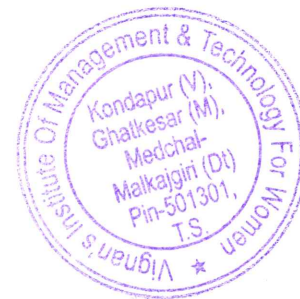
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Certificate of Participation

This is to certify that Dr./Mr./Ms. K. ASHOK REDDY, VMTW has participated in Faculty Development Program on **“CMOS MIXED SIGNAL DESIGN”** held from **7 TO 11 JAN, 2019**, Organized by the Department of Electronics and Communication Engineering.

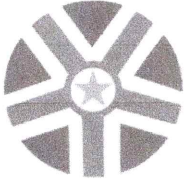

Co-ordinator


HoD, ECE




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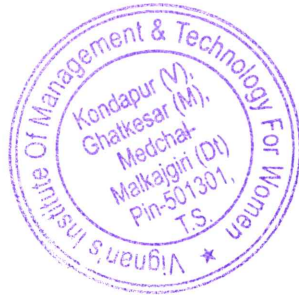
CIRCULAR

This is to inform to all the HOD's, Staff and Non-Teaching Staff that, we are going to conduct a Workshop on **Effective Communication Skills** for all Non-Teaching faculty during 16-07-2018 to 20-07-2018 by **Mrs. Suzhan Shalini**. The workshop will be coordinated by Md. Khaleel, Assistant Professor, from H&S Department. All the Non-Teaching staff are hereby informed to attend the session without fail.

Thanking you Sir,

Your Sincerely

Md. Khaleel,
Assistant Professors, BSH
VMTW.



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Registration Form

Name:

Date of Birth & Age:

Gender (M/F):

Qualifications:

A). Institution:

.....

B). Affiliating University:

.....

Address for Communication:

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.....

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.....

Phone No (1):

Phone No (2):

Office:

Email:

Source Person:
Mrs. Suzan Shalini Vurum,
Assistant Professor,
Department of BS&H.

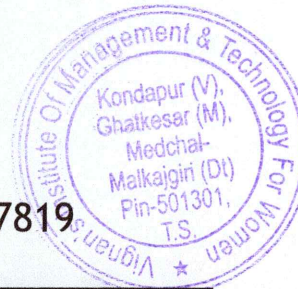
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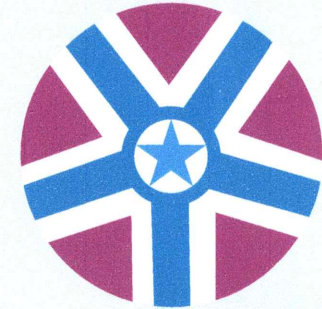
Mrs. Indrani Vasireddy,
Associate Professor &
Head of the Department,
Department of CSE.

Co-ordinator:
Mr. Md. Khaleel Akthar,
Assistant Professor,
Department of BS&H.
Contact No: +91 70132 07819



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A FIVE-DAY WORKSHOP ON
**"EFFECTIVE
COMMUNICATION SKILLS"**
16TH TO 20TH JULY, 2018

Venue:
'A'-Block Seminar Hall,
VMTW,
Principal
Vignan's Institute of Management & Technology For Women,
Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (DI)-501301
Telangana, State

Organized by:
**DEPARTMENT OF
COMPUTER SCIENCE AND ENGINEERING**

Last Date For Registration: 14th July, 2018

About the Institution:

Vignan's Institute of Management and Technology for Women (VMTW), is the brainchild of Dr. L. Rathaiah, Chairman, Vignan Group of Institutions, which was founded in August 2008, with four branches of Engineering.

VMTW is an exemplary institution of higher learning with a mission of pursuing excellence in education and research. The institution, with its diverse and dynamic community of about 2000 students offers a distinctive combination of some of the finest facilities for 7 different graduate, and undergraduate programs, accomplished faculty, world-class facilities with a hostel set on a sprawling area of 22 acres sylvan surroundings of mango groves and greenery.

While students at VMTW immerse themselves in academics, the college has a lot in store for them outside the classroom. Student life includes participation in sports, recreational & co-curricular, and cultural activities. In short, at VMTW students will find an academic and social environment where everyone from faculty members to peers helps shape their future.

VMTW is a home to aesthetically designed buildings with state-of-the-art computer and internet facilities, modern laboratories, workshops, seminar halls, auditoriums, and well-stocked libraries, sports, and games fields.

The Institution boasts of a strong alumni network with alumni events held every year serving as a platform for past students to give back to VMTW and share their experiences with its present fellow students. With so much to offer, it is only natural that students of VMTW get a unique opportunity to carve a niche for themselves in their chosen field of study that enables them to become well-rounded and discerning citizens, fully qualified for their chosen professions in the workplace. Institute also brings out the creativity in students by imparting engineering knowledge imbining interpersonal skills to promote innovation, research, and entrepreneurship.

About the Department:

Department of CSE, VMTW has started B.Tech in Computer Science and Engineering in the year 2008 with an intake of 90. The scholarly ambiance in CSE Department is encouraging to the complete development of the students with well-equipped labs. Department has dedicated and professionally well competent faculty and the many of the faculty are pursuing Ph.D. in emerging areas like Artificial Intelligence, Machine Learning, Cyber Security, Data Science and Block Chain. The main objective is to prepare the students to be successful in Computer Science Engineer to meet the industry needs, encourage lifelong skills and strengthen research.

About the Program:

The aim of technical education is human resource development in specific professional areas of which communication is an important part. The acquisition of this facility would accelerate the process and undoubtedly enrich the language education. Grammar is to a language what management is to an organization. In writing a composition or an oral presentation, the primary requirement is to ensure that the sentences you generate are acceptable from the grammatical and usage point of view. Grammar and usage together constitute a body of rules and practices. It is necessary to learn these rules and practices for acquiring a command over its spoken and written forms. This program illustrates the application of grammar at various levels through examples. The linguistic proficiency of the learner is bound to improve, if he goes through them carefully.

Objectives:

- To develop a clear perception on the basic concepts of grammatical accuracy.
- To prompt the learner to acquire proficiency in writing various forms of communication in flawless and acceptable English.
- Understand the basic difficulty in writing clearly, simply and correctly.
- Identify the different purposes of writing.
- Know the essential principles of effective written communication.
- Plan your written communication by paying special attention to the needs and expectations of your readers.

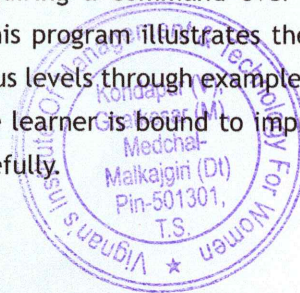
Expected Outcomes:

At the end, participants will be able to:

- To analyze the theoretical skills and strategies to be an effective communicator.
- To reflect with greater confidence and create positive impression in the classroom.
- To develop a learning culture to enrich knowledge where communication is both receptive and expressive.
- To improve the art of communication by precision in thought and expression.


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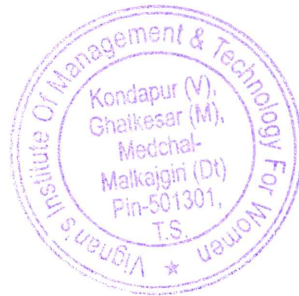
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Programme Schedule of the Workshop Effective Communication Skills

The following is the schedule for the Workshop:

S.No	Date	FN 10:00am – 12:30pm	12:30pm – 1:30pm	AN 1:30pm – 3:00pm
1	16-07-2018	Inaugural and Introduction to the workshop	Lunch Break	Develop a clear perception on the basic concepts of grammatical accuracy
2	17-07-2018	Difficulty in writing clearly, simply and correctly		Essential principles of effective written communication
3	18-07-2018	Acquire proficiency in writing various forms of communication		Common Mistakes in Communication
4	19-07-2018	Significance of Effective Communication		Group Discussion
5	20-07-2018	Self-Assessment		Valedictory




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Department of Basic Science & Humanities A Report on "Effective Communication Skills"

Date: 28-07-2018

Department of Basic Science & Humanities conducted workshop on – "Effective Communication Skills", during 16-07-2018 to 20-07-2018 for Non – Teaching Faculty.

Speaker: Mrs. Suzhan Shalini, as the resource person for this five day workshop session.

The main objective of this program is to provide deep knowledge in Communication Skills.

- To analyze the theoretical skills and strategies to be an effective communicator.
- To reflect with greater confidence and create positive impression in the classroom.
- To develop a learning culture to enrich knowledge where communication is both receptive and expressive

Coordinators: Md. Khaleel, Assistant Professor, from H&S Department acted as Coordinator for this workshop.

Total 30 Number of members had participated in the workshop. This workshop session had provided an excellent opportunity to learn about **Effective Communication Skills** to all the participants.

Date & Time: The workshop was conducted from 16-07-2018 to 20-07-2018 from 10:00 AM– 3:00 PM

We would like to thank our honorable Chairman Sri Dr. L Rathaiah, Sri B. Shravan, CEO, Dr. P. Sudhakar Rao, Principal and Mr. T. Srinivasulu, HOD-BSH for their support for successful completion of five day workshop.

The entire non-teaching Faculty participants felt very happy for attending the program and most of them given excellent in feedback.



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Topics Covered in workshop:

She has focused on the issues like many of us communicate with people every day, whether in person or on the countless digital platforms available to us. But how much of our communication actually reaches the intended audience or person the way we hoped. Effective communication requires us to be clear and complete in what we are trying to express.

The purpose of effective communication:

Effective communication is the process of exchanging ideas, thoughts, opinions, knowledge, and data so that the message is received and understood with clarity and purpose. When we communicate effectively, both the sender and receiver feel satisfied.

She also mentioned for communication to be effective, it must be **clear, correct, complete, concise and compassionate.**

Benefits of Effective communication:

In the workplace, effective communication can help you:

- Manage employees and build teams.
- Grow your organization more rapidly and retain employees.
- Benefit from enhanced creativity and innovation.
- Build strong relationships and attract more opportunities for you or your organization.

In personal life, effective communication can lead to:

- Improved social, emotional, and mental health.
- Deeper existing connections.
- New bonds based on trust and transparency.
- Better problem-solving and conflict resolution skills.

How to improve communication skills:

- Practice active listening.
- Make your message as clear as possible
- Use the right medium or platform.

The five day workshop has covered various fields where **“to develop a clear perception on the basic concepts of grammatical accuracy”**, figure out **“Difficulty in writing clearly, simply and correctly”**, made the participants **“Essential principles of effective written communication”**, finally **“proficiency in writing various forms of communication”**.



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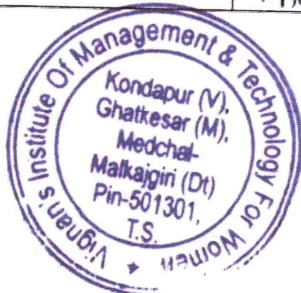
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Attendance Sheet on workshop: EFFECTIVE COMMUNICATION SKILLS

S. No	Name of the Non-Teaching Faculty	Department	16-07-2018	17-07-2018	18-07-2018	19-07-2018	20-07-2018
1	R Srilatha	CSE	Srilatha	Srilatha	Srilatha	Srilatha	Srilatha
2	Mrs.V.Srilatha	CSE	Srilatha	Srilatha	Srilatha	Srilatha	Srilatha
3	Mrs.A.Shalini	CSE	Shalini	Shalini	Shalini	Shalini	Shalini
4	Mrs.S.Minisha	CSE	S	S	S	S	S
5	K.Janardhan	CSE	R	R	R	R	R
6	R.Mounika	CSE	Mounika	Mounika	Mounika	Mounika	Mounika
7	P. Srinivasa Murthy	ECE	P.S. Murthy	P.S. Murthy	P.S. Murthy	P.S. Murthy	P.S. Murthy
8	Mr. G. Rajkumar	ECE	Rajkumar	Rajkumar	Rajkumar	Rajkumar	Rajkumar
9	Mr. T. Chethan Reddy	ECE	C	C	C	C	C
10	Mrs.P.Chandana	ECE	C	C	C	C	C
11	Mrs.K.Anitha	BSH	K	K	K	K	K
12	Mrs.K.Swaroopaa	BSH	Swaroopaa	Swaroopaa	Swaroopaa	Swaroopaa	Swaroopaa
13	Mr.T.Kala Chary	BSH	T	T	T	T	T
14	A Chandra Sekhar	Admin	A	A	A	A	A
15	D LaxmiPrasanna	Admin	L	L	L	L	L
16	D Ramani	Admin	Ramani	Ramani	Ramani	Ramani	Ramani
17	K Sudhakar Reddy	Admin	M	M	M	M	M
18	K. Yadagiri Reddy	Admin	K	K	K	K	K

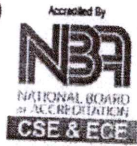



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19	Mrs.K.Swaroop	<u>Swaroop</u>	<u>Swaroop</u>	<u>Swaroop</u>	<u>Swaroop</u>	<u>Swaroop</u>	<u>Swaroop</u>
20	Mrs.T.DivyaJyothi	<u>Divya</u>	<u>Divya</u>	<u>Divya</u>	<u>Divya</u>	<u>Divya</u>	<u>Divya</u>
21	Mr.T.Kala Chary	<u>ABSH</u>	<u>ABSH</u>	<u>ABSH</u>	<u>ABSH</u>	<u>ABSH</u>	<u>ABSH</u>
22	Mr. G. Rajkumar	<u>Rajkumar</u>	<u>Rajkumar</u>	<u>Rajkumar</u>	<u>Rajkumar</u>	<u>Rajkumar</u>	<u>Rajkumar</u>
23	Mr. R. Venkateswarlu	<u>ECE</u>	<u>VE</u>	<u>VE</u>	<u>VE</u>	<u>VE</u>	<u>VE</u>
24	Mr. T. Chethan Reddy	<u>ECE</u>	<u>Chethan</u>	<u>Chethan</u>	<u>Chethan</u>	<u>Chethan</u>	<u>Chethan</u>
25	Mrs. L. Jogeswari	<u>ECE</u>	<u>Jog</u>	<u>Jog</u>	<u>Jog</u>	<u>Jog</u>	<u>Jog</u>
26	Mrs.P.Chandana	<u>ECE</u>	<u>Chandana</u>	<u>Chandana</u>	<u>Chandana</u>	<u>Chandana</u>	<u>Chandana</u>
27	Mrs.R.Mounika	<u>ECE</u>	<u>Mounika</u>	<u>Mounika</u>	<u>Mounika</u>	<u>Mounika</u>	<u>Mounika</u>
28	P. Srinivasa Murthy	<u>ECE</u>	<u>P.S. Murthy</u>	<u>P.S. Murthy</u>	<u>P.S. Murthy</u>	<u>P.S. Murthy</u>	<u>P.S. Murthy</u>
29	B Raju	<u>BSH</u>	<u>Raju</u>	<u>Raju</u>	<u>Raju</u>	<u>Raju</u>	<u>Raju</u>
30	Ms.N.SaiPriya	<u>CSE</u>	<u>SaiPriya</u>	<u>SaiPriya</u>	<u>SaiPriya</u>	<u>SaiPriya</u>	<u>SaiPriya</u>
31	Mr.M.Swaroop Reddy	<u>CSE</u>	<u>Swaroop Reddy</u>	<u>Swaroop Reddy</u>	<u>Swaroop Reddy</u>	<u>Swaroop Reddy</u>	<u>Swaroop Reddy</u>
32	Mrs.K.Varshitha	<u>CSE</u>	<u>Varshitha</u>	<u>Varshitha</u>	<u>Varshitha</u>	<u>Varshitha</u>	<u>Varshitha</u>
33	Ms.Swetha Reddy	<u>CSE</u>	<u>Swetha</u>	<u>Swetha</u>	<u>Swetha</u>	<u>Swetha</u>	<u>Swetha</u>
34	Ms.K.Shirisha	<u>CSE</u>	<u>Shirisha</u>	<u>Shirisha</u>	<u>Shirisha</u>	<u>Shirisha</u>	<u>Shirisha</u>
35	ChVenkanna	<u>CSE</u>	<u>ChVenkanna</u>	<u>ChVenkanna</u>	<u>ChVenkanna</u>	<u>ChVenkanna</u>	<u>ChVenkanna</u>
36	CH. Anjaiah	<u>CSE</u>	<u>CH. Anjaiah</u>	<u>CH. Anjaiah</u>	<u>CH. Anjaiah</u>	<u>CH. Anjaiah</u>	<u>CH. Anjaiah</u>

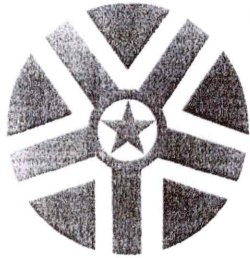
Chethan
COORDINATOR

POD
HOD-CSE



Principals
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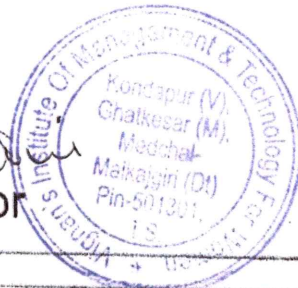
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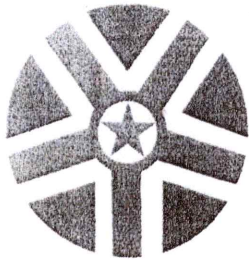
This is to certify that Mr./Ms./Mrs. S. Talouder Reddy, Sr. Asst.
of Vignan's Institute of Management and Technology for Women has participated in
a Workshop on "EFFECTIVE COMMUNICATION SKILLS" held on 16th to 20th July
2018, Organized by Vignan's Institute of Management and Technology for Women.


Co-ordinator




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2018, Organized by Vignan's Institute of Management and Technology for Women.


Co-ordinator




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