



VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

Sponsored by Lavu Educational Society, Approved by AICTE, New Delhi & Affiliated to JNTUH, Hyderabad.
Kondapur (V), Ghatkesar (M), Medchal - Malkajgiri (D) - 501 301 Phone: +91 96529 10002/3



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VMTW/ECE/WS/CIR/2021-22/1/2

Date: 09-11-2021

CIRCULAR

This is to inform all the III B.Tech ECE Students that there will be a Workshop on entitled "Analog VLSI Chip Design" in Association with **Deeksha Technologies** by Mr. **P. Hari Prasad** from **11-11-2021 to 12-11-2021** at B-Block Seminar Hall. All students are invited to take part in this Workshop. The participating students will be honored with certificates of participation.

During the Workshop you will be encouraged to interact with the speaker and the other participants through the case study discussions.


HOD

Copy to:

The Principal, for kind information

IQAC

Dept. File

Circular to III ECE Class Room




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Vignan's Institute of Management & Technology For Women
Kondapur(V),Ghatkesar(M),Medchal-Malkajgiri(Dt)-501301
Telangana State



<hariprased.peta@gmail.com>

Request to conduct a workshop in our college

Dept. of ECE VMTW <ece@vmtw.in>

10:00 AM To: <hariprased.peta@gmail.com>

Mon November, 8th, 2021 at

Respected sir,

I am Mr. P.Harikrishna, HOD , Dept of ECE , Vignn's Institute Of Management And Technology For Women. Ghatkesar, Hyderabad, Telangana. We are planning to conduct a two day workshop on "Analog VLSI Chip Design" on 11th and 12th November 2021 to enhance the knowledge of the students. I request to send approval for conducting workshop

Thanking you

Thanks Regards
P.Harikrishna
Assoc. Prof & HOD,
Dept. of ECE, VMTW
Contact: 7093246048



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DEEKSHA TECHNOLOGIES <hariprased.peta@gmail.com>

Regarding work shop approval

DEEKSHA TECHNOLOGIES <hariprased.peta@gmail.com>

09, November, 2021 at 11:02 AM

To: Dept. of ECE VMTW <ece@vmtw.in>

Sir/Madam,

Sub: approved for organizing workshop

Hence further enhancement of students knowledge we have approved the organization to conduct a two day workshop on "Analog VLSI Chip Design".

With Regards,

DEEKSHA TECHNOLOGIES
hariprased.peta@gmail.com



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REGISTRATION FORM

Name:

Date of Birth & Age:

Gender (M/F):

Qualifications:

..... Institution:

..... Affiliating University:

Address for Communication:

Phone Nos.

Office:

Personal:

Email:

LAST DATE FOR REGISTRATION: 27TH APRIL, 2022

Resource Person:
Mr. P. Hari Prasad,
Deeksha Technologies,
Hyderabad.

Chief-patrons:
Dr. L. Rathaiah,
Chairman,
Vignan Group of Institutions.

Mr. B. Shravan,
Chief Executive Officer,
Vignan Group of Institutions

Patron:
Dr. G. Apparao Naidu,
Principal, VMTW

Mr. P. Harikrishna,
Associate Professor & Head of the Department,
Dept. of ECE.

Co-ordinator:
Mr. Pullaiah Thanam
Associate Professor, Dept. of ECE,
Contact No: +91 96425 64980

Student Co-ordinator:
Ms. Tankala Harika
Contact No: +91 95503 92403

Ms. Nattuva Bhargavi
Contact No: +91 80965 33999



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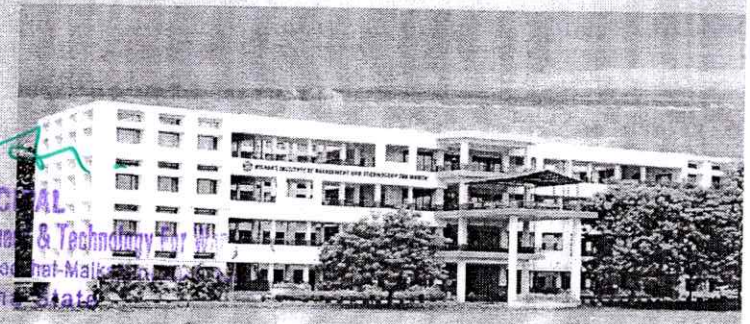


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A TWO DAY WORKSHOP ON
“ANALOG VLSI
CHIP DESIGN”
11TH & 12TH NOVEMBER, 2021

VENUE:
‘B’-BLOCK SEMINAR HALL, VMTW

ORGANIZED BY:
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



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Vignan's Institute of Management & Technology
Kondapur(V), Ghatkesar(M), Medchal-Malkajgiri(D)
Telangana State

ABOUT THE INSTITUTION

Vignan's Institute of Management and Technology for Women (VMTW), is the brainchild of Dr. L. Rathaiah, Chairman, Vignan Group of Institutions, which was founded in August 2008, with four branches of Engineering.

VMTW is an exemplary institution of higher learning with a mission of pursuing excellence in education and research. The institution, with its diverse and dynamic community of about 2000 students offers a distinctive combination of some of the finest facilities for 7 different graduate, and undergraduate programs, accomplished faculty, world-class facilities with a hostel set on a sprawling area of 22 acres sylvan surroundings of mango groves and greenery.

While students at VMTW immerse themselves in academics, the college has a lot in store for them outside the classroom. Student life includes participation in sports, recreational & co-curricular, and cultural activities. In short, at VMTW students will find an academic and social environment where everyone from faculty members to peers helps shape their future.

VMTW is a home to aesthetically designed buildings with state-of-the-art computer and internet facilities, modern laboratories, workshops, seminar halls, auditoriums, and well-stocked libraries, sports, and games fields.

The Institution boasts of a strong alumni network with alumni events held every year serving as a platform for past students to give back to VMTW and share their experiences with their present fellow students. With so much to offer, it is only natural that students of VMTW get a unique opportunity to carve a niche for themselves in their chosen field of study. It enables them to become well-rounded and discerning professionals, fully qualified for their chosen professions in the workplace. Institute also brings out the creativity in students by imparting engineering knowledge imbuing interpersonal skills to promote innovation, research, and entrepreneurship.

ABOUT THE DEPARTMENT

Department of Electronics and Communication Engineering was started since the inception of Vignan's Institute of Management and Technology for Women during 2008 with an initial intake of 90. The strength was enhanced to 120 during 2010. The Department had added Post graduate programme in VLSI during the year 2011 and Embedded Systems during 2014 with an intake of 18 each.

The Department has state-of-art laboratories equipped with advanced and well maintained equipment, continuously updated application software packages, more than adequate computing systems with 24x7, 30 MBPS internet facility.

ABOUT THE COURSE

The goal of the workshop is to introduce the students to the main principles of the MOS transistor implementation, the basic VLSI analog design flow, and the Cadence analog design environment. The students will learn how to draw schematics, run different types of analog simulations and how to draw and verify layout.

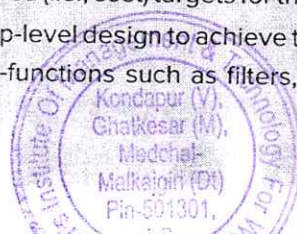
COURSE OBJECTIVES


Analog design in the context of integrated circuit (IC) design is a discipline that focuses on the creation of circuits that operate in and are optimized for continuous time-domain behavior. Typical objectives of analog design include.

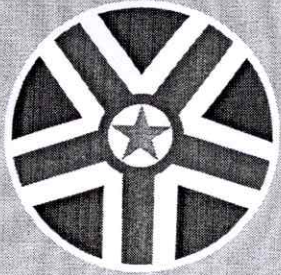
EXPECTED OUTCOMES

At the end of Workshop, participants will be able to

- Develop a high-level specification for the design. What functions will it perform? What are the performance, power, and area (i.e., cost) targets for the design?
- Develop a top-level design to achieve the required results using macro-functions such as filters, comparators, and amplifiers.

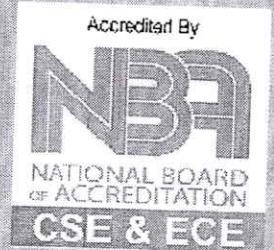



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Telangana State



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WORKSHOP ON

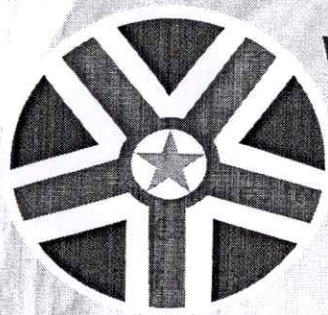
ANALOG VLSI CHIP DESIGN

11TH & 12TH NOVEMBER, 2021

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



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Kondapur (V), Ghatkesar (M), Medchal - Malkajgiri (D) - 501 301

Accredited By



CERTIFICATE OF PARTICIPATION

This certificate is presented to

.....

for her participation in a Two Day Workshop on
"Analog VLSI Chip Design" held from 11-11-2021 to 12-11-2021,
Organized by the Department of Electronics and
Communication Engineering.

Coordinator



HOD, ECE


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T.S.



Vignan's Institute Of Management and Technology For Women

Work shop on "Analog VLSI Chip Design"

III B.Tech II sem Students List-2021-22

Branch :ECE-A



S. No	Hall Ticket No	Student Name	Sign of the student-11/11/21	Sign of the student-12/11/21
1	19UP1A0401	ARKALA SNEHA REDDY	Sneha	Sneha
2	19UP1A0402	BANDI RASAGNA	Rasagna	Rasagna
3	19UP1A0403	BASWA VIGNATHA	Vignatha	Vignatha
4	19UP1A0404	BELLI PAVANI	Pavani	Pavani
5	19UP1A0405	B V SHIVA KEERTHIKA	Keerthi	Keerthi
6	19UP1A0406	DIWAKARLA SAI LAKSHMI SRAVYA	Sul	Sul
7	19UP1A0407	GAJULA ABHIGNA	Abhigna	Abhigna
8	19UP1A0408	GADDAM LIKHITHA	Likhitha	Likhitha
9	19UP1A0409	GIRUGUBOTHALA SAI DEEPTHI	Said cepti	Said cepti
10	19UP1A0410	GOLLA PRIYANKA	Priyanka	Priyanka
11	19UP1A0411	GOURISHETTI SWETHA	Sul	Sul
12	19UP1A0412	KOPPELA NISHITHA	Nishitha	Nishitha
13	19UP1A0413	KALAVA CHARITHA	Charitha	Charitha
14	19UP1A0414	KALVA MAHATHEE REDDY	Mahathee	Mahathee
15	19UP1A0415	KANDE LAHARI	Lahari	Lahari
16	19UP1A0416	KANDULA MEGHANA	Meghana	Meghana
17	19UP1A0417	KONAKANCHI TEJASWINI	Tejaswini	Tejaswini
18	19UP1A0418	KUNCHETTI PRAVALIKA	Pravalika	Pravalika
19	19UP1A0419	LINGAMPALLY ABHINAYA	Abhinaya	Abhinaya
20	19UP1A0420	MANNE SRAVANI	Sravani	Sravani
21	19UP1A0421	NAKKA LAVANYA	Lavanya	Lavanya
22	19UP1A0422	NEELAM MOHANA PRIYA	AB	AB
23	19UP1A0423	PUSHADAPU LAVANYA	Lavanya	Lavanya
24	19UP1A0424	PALLE PAVANI	Pavani	Pavani
25	19UP1A0425	PARIMI PRASANNA LAXMI	Laxmi	Laxmi



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26	19UP1A0426	P SINDHU	Sindhu	Sindhu
27	19UP1A0427	RAJPUTH MAHESHWARI SINGH	AB	AB
28	19UP1A0428	ROWTHU PALLAVI	Pallavi	Pallavi
29	19UP1A0429	SYED TABASSUM	Tabassum	Tabassum
30	19UP1A0430	SAKSHI JAYASREE BHAVANI	AB	AB
31	19UP1A0431	SANGOJU SATHWIKA	Sathwika	Sathwika
32	19UP1A0432	SEETHAMMAGARI ANITHA	Anitha	Anitha
33	19UP1A0433	SIVVA AKSHAYA	Akshaya	Akshaya
34	19UP1A0434	SRIPADA JITYA LAKSHMI YESASWINI	Lakshmi	Lakshmi
35	19UP1A0435	SUGGUNA LIKHITA	Likhita	Likhita
36	19UP1A0436	TANKALA HARIKA	Harika	Harika
37	19UP1A0437	THOOMU SREYA REDDY	Sreya	Sreya
38	19UP1A0438	UPPULA SHUVALEELA	Shuvaleela	Shuvaleela
39	19UP1A0439	VANAM ASHVITHA REDDY	AB	AB
40	19UP1A0440	VANKUDOTH SANGITHA	Sangitha	Sangitha
41	19UP1A0441	V LAXMI VASUNDHARA	Vasundhara	Vasundhara
42	19UP1A0442	YARASURI GAYATHRI MANASA	Manasa	Manasa
43	20UP5A0401	AKKI PRATHIMA BHARATHI	AB	AB
44	20UP5A0402	EDUGU RAJITHA	Rajitha	Rajitha
45	20UP5A0403	GODAVARTHI ANUSHA	Anusha	Anusha
46	20UP5A0404	GOKA ANUSHA	Anusha	Anusha
47	20UP5A0405	GOPI VARSHA	Varsha	Varsha
48	20UP5A0406	JUPALLY NAVYA SRI	Navyasri	Navyasri
49	20UP5A0407	KOLA CHETHANA	Chethana	Chethana
50	20UP5A0408	KOTTHUR DEVI	Devi	Devi
51	20UP5A0409	MUPPANENI SOWMIKA	Sowmya	Sowmya

Sellip
COORDINATOR

[Signature]
ECE, HOD

Head of the Department

Electronics and Communication Engineering
Vignans Institute of Management & Technology For Women
Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (DT)-501301



[Signature]
Vignans Institute of Management & Technology For Women
Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (DT)-501301
Telangana



Vignans Institute Of Management and Technology For Women
Work shop on "Analog VLSI Chip Design"

III B.Tech II sem Students List-2021-22

Branch :ECE-B



S. No	Hall Ticket No	Student Name	Sign of the student- 11/11/21	Sign of the student- 12/11/21
1	19UP1A0443	ANNU PRAGATHI	Pragathi	Pragathi
2	19UP1A0444	ANUMOJU KAVYA	Kavya	Kavya
3	19UP1A0446	BANALA SNITHIKA	Snithika	Snithika
4	19UP1A0447	BHIMAVARAPU SIVA NAGA SAI SARANYA	Saranya	Saranya
5	19UP1A0448	BOGALA ANUSHA	Anusha	Anusha
6	19UP1A0449	BOKKA HARSHITHA	Harshitha	Harshitha
7	19UP1A0450	BOKKA VARSHITHA	Varshitha	Varshitha
8	19UP1A0451	BOLLEPALLY JASHANVI	Jashanvi	Jashanvi
9	19UP1A0452	CHIMATA MADHAVI	Madhavi	Madhavi
10	19UP1A0453	DASI VENKATA SAHITHAYA	AB	AB
11	19UP1A0454	DUGYALA VAISHNAVI	AB	AB
12	19UP1A0455	G.SHIRISHA	Shirisha	Shirisha
13	19UP1A0456	GABALE NISHITHA	AB	AB
14	19UP1A0457	GOURISHETTI HARSHA VARDHINI	Vardhini	Vardhini
15	19UP1A0458	GUMAMALA SNEHA REDDY	AB	AB
16	19UP1A0459	HALDODDI DIVYA	Divya	Divya
17	19UP1A0460	JANJIRALA ANUSHA	Anusha	Anusha
18	19UP1A0461	KARKURI SOWMYA	AB	AB
19	19UP1A0462	KALYANAM MOUNIKA	Mounika	Mounika
20	19UP1A0463	KOMMINENI JASMITHA	AB	AB
21	19UP1A0464	KONDERU SRAVYA	Sravya	Sravya
22	19UP1A0465	LAKAMGARI PRASANNA LAXMI REDDY	AB	AB
23	19UP1A0466	GADDAM LIKHITHA	Likhitha	Likhitha
24	19UP1A0467	LIMBURE SANTHOSHI	AB	AB
25	19UP1A0468	MALGIREDDY YOGITHA REDDY	Yogitha	Yogitha
26	19UP1A0469	MARVALA DEEPIKA	Deepika	Deepika



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Tel. Andhra State

27	19UP1A0470	MEDA BHUVANESWARI	AB	AB
28	19UP1A0471	PADAKULA UMA	Uma	Uma
29	19UP1A0472	RAMIDI DEEKSHITHA	Deekshitha	Deekshitha
30	19UP1A0473	RAVULA HIRANMAI	Hiranmai	Hiranmai
31	19UP1A0474	SHANIGALA CHANDANA	Chandana	Chandana
32	19UP1A0475	MORAPALLY SIRI CHANDANA REDDY	AB	AB
33	19UP1A0476	SUROJU SRIVAISHNAVI	Srivaishnavi	Srivaishnavi
34	19UP1A0477	TELUKUNTLA SREYA REDDY	Sreya	Sreya
35	19UP1A0478	THAMATAM AKSHITHA	AB	AB
36	19UP1A0479	TURPU SHRAVANI	Shravani	Shravani
37	19UP1A0480	VANGETI BANNY	Banny	Banny
38	19UP1A0481	VENNAMANENI ANOUSHIKA	AB	AB
39	19UP1A0482	YEDULLA NAGALAXMI	Nagalaxmi	Nagalaxmi
40	20UP5A0410	NAGARAM ANUSHA	Anusha	Anusha
41	20UP5A0411	NAKKA YAMINI	AB	AB
42	20UP5A0412	NATTUVA BHARGAVI	Bhargavi	Bhargavi
43	20UP5A0413	PALASA CHANDANA	AB	AB
44	20UP5A0414	SANKURI SRAVANI	Sravani	Sravani
45	20UP5A0415	T ANUSHA	Anusha	Anusha
46	20UP5A0416	VADDABOINA NAMRATHA	AB	AB
47	20UP5A0417	VADLA SHIVANI	Shivani	Shivani
48	20UP5A0418	VINDHYA DARSHINI	Darshini	Darshini
49	20UP5A0419	YAMJALA SHAMILI	Shamili	Shamili

[Signature]
COORDINATOR

[Signature]
ECE, HOD

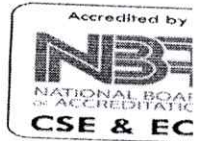


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Telangana State



**VIGNAN'S INSTITUTE OF MANAGEMENT AND
TECHNOLOGY FOR WOMEN**

Kondapur (V), Ghatkesar (M), Medchal. Dist – 501 301.



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 15-11-2021

Conclusion report on “Analog VLSI Chip Design”

Sub: conclusion Report of “Analog VLSI Chip Design” conducted on 11th & 12th November ,2021—reg.

The workshop was aimed to provide knowledge about Analog VLSI Chip Design. Mr. P. Hari Prasad, DEEKSHA TECHNOLOGIES HYD, executed 2-days Workshop on “Analog VLSI Chip Design”. After attending this workshop students have expressed about their capability of Designing Analog VLSI Chip and applications in any Electronics Project. This workshop will be helpful for students for circuit development of their final year project. Total 80 students have participated in this workshop. The workshop was an informative and interesting. All the students of ECE had a practical exposure about Analog VLSI Chip Design.

On behalf of the students I request you to arrange more workshops for students to improve the knowledge of students and enhance the career opportunities.


Mr. Pullaiah Thanam

Associate Professor, ECE.

VMTW ,Ghatkesar, HYD.




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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VMTW/ECE/WS/CIR/2019-20/1/1

Date: 07-08-2019

CIRCULAR

This is to inform all the IV ECE Students that there will be a Workshop on entitled "LOW POWER CMOS POWER AMPLIFIER" in Association with **Deeksha Technologies** by Mr. **P. Hari Prasad** from 13-08-2019 to 14-08-2019 at B-Block Seminar Hall. All students are invited to take part in this Workshop. The participating students will be honored with certificates of participation.

During the Workshop you will be encouraged to interact with the speaker and the other participants through the case study discussions.

HOD

Copy to:

The Principal, for kind information

IQAC

Dept. File

Circular to IV ECE Class Rooms



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Telangana State



<hariprasad.peta@gmail.com>

Request to conduct a workshop in our college

Dept. of ECE VMTW <ece@vmtw.in>

To: <hariprasad.peta@gmail.com>

Thu Aug, 1st, 2019 at 10:44 AM

Respected sir,

I am Vijaykumar Rewnath Urkude, , HOD , Dept of ECE , Vignan's Institute Of Management And Technology For Women. Ghatkesar, Hyderabad, Telangana. We are planning to conduct a two day workshop on "**Low Power CMOS Power amplifier**" on 13th and 14th August 2019 to enhance the knowledge of the students. I request to send approval for conducting workshop

Thanking you

Thanks Regards
Vijaykumar Rewnath Urkude
Assoc. Prof & HOD,
Dept. of ECE, VMTW
Contact: 92937 93986




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Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (D), Pin-501301, T.S.



Hariprasad <hariprasd.peta@gmail.com>

Regarding work shop approval

Hariprasad <hariprasd.peta@gmail.com>

09, Aug, 2019 at 11:02 AM

To: Dept. of ECE VMTW <ece@vmtw.in>

Sir/Madam,

Sub: approved for organizing workshop

Hence further enhancement of students knowledge we have approved the organization to conduct a two day workshop on "Low Power CMOS Power amplifier".

With Regards,
Deeksha Technologies,
9396671541,




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Telangana State

REGISTRATION FORM

Name:

Date of Birth & Age:

Gender (M/F):

Qualifications:

Institution:

Affiliating University:

Address for Communication:

.....

.....

Phone Nos.

Office:

Personal:

Email:

START DATE FOR REGISTRATION: 11TH AUGUST, 2019

Resource Person:

Mr. P. Hari Prasad,
Deeksha Technologies,
Hyderabad.

Chief-patrons:

Dr. L. Rathaiah,
Chairman,
Vignan Group of Institutions.

Mr. B. Shravan,
Chief Executive Officer,
Vignan Group of Institutions

Patron:

Dr. P. Sudhakara Rao,
Principal, VMTW

Mr. Vijaykumar Rewnath Urkude
Associate Professor &
Head of the Department, ECE.

Co-ordinator:

Mr. J. Sunil Kumar
Assistant Professor,
Dept. of ECE,
Contact No: +91 83747 17668

Student Co-ordinator:

Ms. T. Anusha
Contact No: +91 93473 98739

Ms. Nattuva Bhargavi
Contact No: +91 80965 33999

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INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

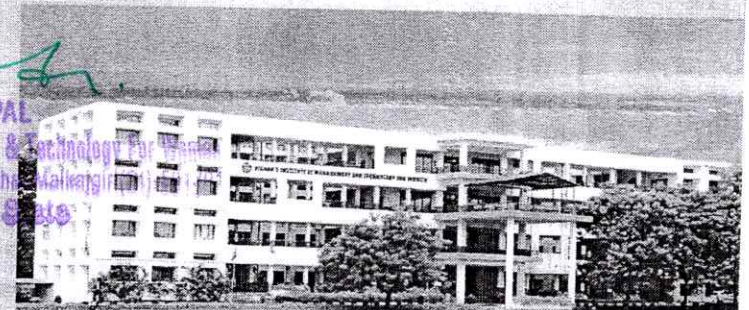


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Kondapur (V), Ghatkesar (M), Medchal - Malkajgiri (D) - 501301

A TWO DAY WORKSHOP ON
“LOW POWER CMOS
POWER AMPLIFIER”
13TH & 14TH AUGUST, 2019

VENUE:
‘B’-BLOCK SEMINAR HALL, VMTW

ORGANIZED BY:
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING




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Vignan's Institute of Management & Technology for Women
Kondapur(V), Ghatkesar(M), Medchal - Malkajgiri(D)
Telangana State

ABOUT THE DEPARTMENT

Department of Electronics and Communication Engineering was started since the inception of Vignan's Institute of Management and Technology for Women during 2008 with an initial intake of 90. The strength was enhanced to 120 during 2010. The Department had added Post graduate programme in VLSI during the year 2011 and Embedded Systems during 2014 with an intake of 18 each.

The Department has state-of-art laboratories equipped with advanced and well maintained equipment, continuously updated application software packages, more than adequate computing systems with 24x7, 30 MBPS internet facility.

ABOUT THE COURSE

Designing power amplifiers with low power consumption, high efficiency and integration is an important topic with significant impact on communication and circuit research area.

COURSE OBJECTIVES

The Internet of Things (IoT) promises the tremendous potential to improve resource management, increase productivity, and enable new business models across business and consumer markets. The basic concept of IoT is to construct an information network architecture that connects many 'things' in the world together.

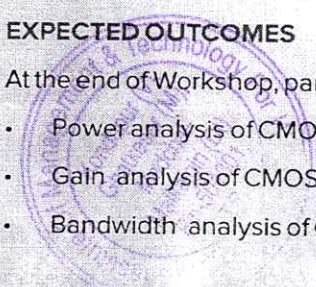
EXPECTED OUTCOMES

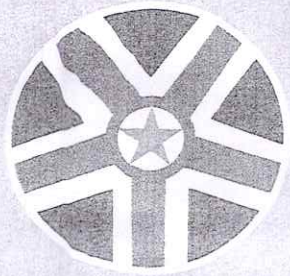
At the end of Workshop, participants will be able to

- Power analysis of CMOS Power amplifier.
- Gain analysis of CMOS Power amplifier.
- Bandwidth analysis of CMOS Power amplifier.

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Kondapur (V), Ghatkesar (M), Medchal - Malkajgiri (D) - 501 301 Phone: +91 96529 10002/3



WORKSHOP ON

LOW POWER CMOS POWER AMPLIFIER

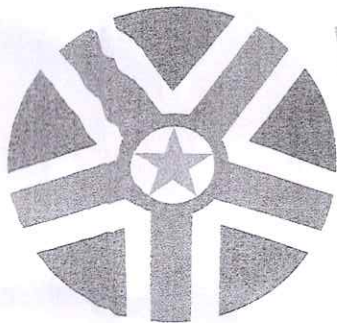
13TH & 14TH AUGUST, 2019

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



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Accredited By



Certificate of Participation

This certificate is presented to

.....

for her participation in a Two Day Workshop on

“Low Power CMOS Power Amplifier” held from 13-08-2019 to 14-08-2019

Organized by the Department of Electronics and Communication Engineering.

Coordinator



HoD, ECE

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Telangana State

Principal



Vignani's Institute Of Management and Technology For Women
 Work shop on "low Power CMOS Power amplifier"
 IV B.Tech II sem Students List-2019-20
 Branch :ECE-A




S. No	Hall Ticket No	Student Name	Sign of the student- 13/08/19	Sign of the student- 14/08/19
1	16UP1A0401	ALLIAMPALLI RISHITHA	Rishitha	Rishitha
2	16UP1A0402	ANNAPUREDDY DEEPTHI	Deepti	Deepti
3	16UP1A0403	APPALAREDDY TEJASHWINI	Tejashwini	Tejashwini
4	16UP1A0404	BAKI SOWMYA	Sowmya	Sowmya
5	16UP1A0405	BALLIKONDA RUTHVIKA	Ruthvi	Ruthvi
6	16UP1A0407	DACHEPALLI NAGA SHARANYA	Sharanya	Sharanya
7	16UP1A0408	DONTHU ALEKHYA	Alekha	Alekha
8	16UP1A0409	DORAM MEGHANA	Meghana	Meghana
9	16UP1A0410	GAJALA SHANTHI SRI	Shanthi	Shanthi
10	16UP1A0412	GARSHA RAJITHA	Rajitha	Rajitha
11	16UP1A0413	GOURIBATLA SRILEKHA	Srilekha	Srilekha
12	16UP1A0414	GUTHA SUMAREDDY	Suma	Suma
13	16UP1A0415	JAGTAP SRIVIDYA	Srividya	Srividya
14	16UP1A0416	JANUMPALLY SANGEETHA	Sangeetha	Sangeetha
15	16UP1A0417	K NEELA VENKATA SRIYA	Sriya	Sriya
16	16UP1A0418	KAGITHA DURGA VARA PRASANNA SHIVA POOJA	Pooja	Pooja
17	16UP1A0419	KAKI AKANKSHA	Akanksha	Akanksha
18	16UP1A0420	KANUKUNTLA AMULYA	Amulya	Amulya
19	16UP1A0422	KEESARI AKSHITHA	Akshitha	Akshitha
20	16UP1A0423	KOLAGANI KEERTHI	Keerthi	Keerthi
21	16UP1A0424	KONTHAM MANASWINI	Manaswini	Manaswini
22	16UP1A0425	KORATUR YESASVINI RAJA RAJESWARI	Rajeswari	Rajeswari
23	16UP1A0426	KOTI SRI SAI LALITHA SARANYA	Lalitha	Lalitha
24	16UP1A0427	LAKUMARAPU SAI SREEJA	Sreeja	Sreeja
25	16UP1A0428	LINGAPURAM SRAVANI	Sravani	Sravani
26	16UP1A0430	MAHESHWARAM CHANDRIKA	Chandrika	Chandrika
27	16UP1A0431	MALLOJALA PRANAVI	Pranavi	Pranavi
28	16UP1A0432	MARRI RAJESHWARI	Rajeshwari	Rajeshwari
29	16UP1A0434	MUTYALA SINDHUJA	Sindhu	Sindhu
30	16UP1A0435	N NISHA REDDY	Nisha	Nisha
31	16UP1A0436	NANDINI KANIKE	Nandini	Nandini
32	16UP1A0437	NERALLAPALLI RAVALIKA	Ravali	Ravali
33	16UP1A0438	P NARMADA	Narmada	Narmada
34	16UP1A0439	PELIMELLI HARIKA	Hari	Hari
35	16UP1A0440	PENDYALA SHARANYA	Sharanya	Sharanya
36	16UP1A0441	PIGILAM LIKITHA	Likitha	Likitha
37	16UP1A0442	PULLURI MOUNIKA	Mounika	Mounika
38	16UP1A0443	RAMAGIRI VISHALI LAXMI	Vishali	Vishali
39	16UP1A0445	SAVA SRAVANI	Sravani	Sravani
40	16UP1A0446	SHIKA MANIDEEPA	Manideepa	Manideepa
41	16UP1A0447	UDATA KAVERI KOMRAIYA	Kaveri	Kaveri
42	16UP1A0448	VALISETTY SUSHMA	Sushma	Sushma



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CO ORDINATOR


HOD ECE
Department of Electronics and Communication Engineering
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38	16UP1A0443	RAMAGIRI VISHALI LAXMI	Mounika	mounika
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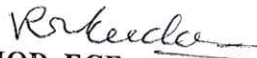


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 Vignans Institute of Management & Technology For Women
 Kondapur(V), Shakkarapeta, Medak District, Hyderabad-500084
 Telangana State

41	16UP1A0447	UDATA KAVERI KOMRAIYA	Kavari Sushma	Kavari suhu
42	16UP1A0448	VALISETTY SUSHMA		

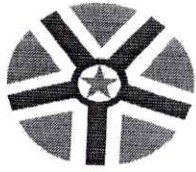

CO ORDINATOR




HOD, ECE
Head of the Department
Electronics and Communication Engineering
Vignans Institute of Management & Technology
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TECHNOLOGY FOR WOMEN**

Kondapur (V), Ghatkesar (M), Medchal. Dist – 501 301.



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 18-08-2019

Conclusion report on “low Power CMOS Power amplifier”

Sub: conclusion Report of “low Power CMOS Power amplifier”

Conducted on 13& 14 August, 19—reg.

The workshop was aimed to provide knowledge about application design using IOT and cloud services.

Mr. P.Hariprasad **low Power CMOS Power amplifier**, HYD, executed 2-days Workshop on “Application **low Power CMOS Power amplifier**” and applications workshop. After attending this workshop students have expressed about their capability of Designing Special purpose IOT Devices Design and applications in any Electronics Project. This workshop will be helpful for students for circuit development of their final year project. Total 80 students have participated in this workshop. The workshop was an informative and interesting. All the students of ECE had a practical exposure about IOT Devices Design and its applications.

On behalf of the students I request you to arrange more workshops for students to improve the knowledge of students and enhance the career opportunities

Mr.Sunilkumar .J

Assistant Professor, ECE.

VMTW ,Ghatkesar,HYD.



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