



VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

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Kondapur (V), Ghatkesar (M), Medchal - Malkajiri (D) - 501 301 Phone: +91 96529 10002/3



INSTITUTIONAL PROGRAM BROCHURE, CIRCULAR, SYALLBUS, REPORT -2019-20

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DEPARTMENT OF BASIC SCIENCE AND HUMANITIES

VMTW/BSH/AOC/CIR/2019-20/II/01

Date: 17.01.2020

CIRCULAR

We are delighted to introduce an add-on course titled "FUNDAMENTAL CONCEPTS OF COMPUTER TROUBLESHOOTING," exclusively tailored for our esteemed B. Tech. IT students. We are pleased to inform you that **Dr. S. Ranga Swamy**, Associate Professor, Department of CSE, VMTW, Hyderabad have been identified by the department advisory committee as the esteemed resource person for this course. We are delighted to share that **Dr. S. Ranga Swamy** have graciously accepted our invitation to conduct the classes, providing you with a comprehensive and enriching learning experience. The course is scheduled to run for 32 days from **20th JAN 2020 to 11th MAR 2020**. Participating students will be awarded Certificates of Course Completion. Throughout the duration, you will have the opportunity to engage with the speaker and other participants through case study discussions, fostering interactive learning. The Course is organizing by department of IT. If you are interested in joining this course, kindly fill out the registration form and secure your spot for this exciting learning opportunity.


HOD

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Head of the Department:
Basic Sciences and Humanities
Vignan's Institute of Management and Technology for Women
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Telangana State



ABOUT THE COURSE:

The course on Fundamentals Concepts of Computer Troubleshooting provides a comprehensive understanding of the basic principles and techniques involved in troubleshooting computer systems. It aims to equip students with the necessary skills to diagnose and resolve common hardware and software issues that may arise in computer systems.

COURSE OBJECTIVES:

The objective of the course "Fundamental Concepts of Computer Troubleshooting" is to provide students with a comprehensive understanding of the fundamental concepts related to computers and equip them with troubleshooting skills to effectively identify and resolve common computer issues.

EXPECTED OUTCOMES:

1. Understand the Basic Concepts of Computer Systems
 - Demonstrate knowledge of the essential components and functions of a computer system.
 - Explain the roles of hardware and software in computer operations.
2. Identify and Troubleshoot Hardware Issues
 - Diagnose common hardware problems such as faulty components, connectivity issues, or hardware conflicts.
 - Apply appropriate troubleshooting techniques to resolve hardware issues effectively.
3. Identify and Troubleshoot Software Issues
 - Identify common software problems, such as application crashes, software conflicts, or compatibility issues.

- Apply appropriate troubleshooting techniques to resolve software issues effectively.

4. Implement Computer Maintenance and Optimization Techniques
 - Demonstrate knowledge of routine computer maintenance tasks to ensure optimal system performance.
 - Apply strategies for system optimization, including disk cleanup, software updates, and driver installations.
5. Develop Data Backup and Recovery Strategies
 - Understand the importance of data backup and the potential risks of data loss.
 - Implement data backup and recovery strategies to protect important files and ensure data availability.

Resource Person:

Dr. S. Ranga Swamy,

Associate Professor,
Department of CSE.

Co-ordinator:

Mr. K. Bharath Reddy,

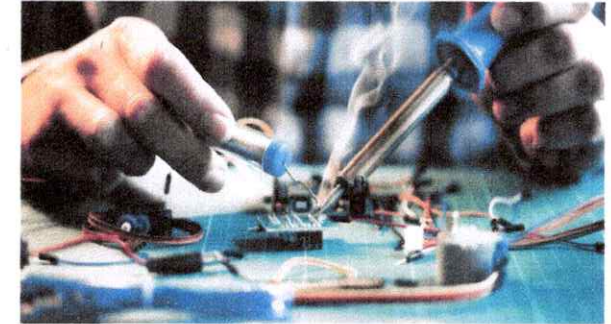
Assistant Professor,
Department of IT.

Contact No: +91 89854 41753

LAST DATE FOR REGISTRATION: 19TH JAN, 2020

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ADD-ON COURSE ON

**"FUNDAMENTAL
CONCEPTS OF
COMPUTER
TROUBLESHOOTING"**

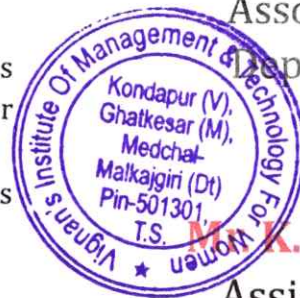
20TH JAN TO 11TH MAR, 2020

Duration of the Course : 32 Hrs

Venue:
**Room No. AFF-2,
VMTW.**

Organized by:

**DEPARTMENT OF
INFORMATION TECHNOLOGY**




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DEPARTMENT OF INFORMATION TECHNOLOGY

ADD ON COURSE SYLLABUS: FUNDAMENTAL CONCEPTS OF COMPUTER TROUBLESHOOTING

Academic Year: 2019-20

B. Tech. IT – I - II SEM

SYLLABUS

Course Overview:

The course on "Fundamental Concepts of Computer Troubleshooting" is designed to equip participants with the essential knowledge and skills required to diagnose and troubleshoot common computer hardware and software issues. Participants will learn fundamental concepts of computer troubleshooting, including problem identification, analysis, and resolution techniques. The course will cover hardware components, operating systems, software conflicts, network connectivity, and basic troubleshooting tools. Participants will also develop effective problem-solving strategies and communication skills to assist users in resolving computer-related issues.

COURSE OBJECTIVES:

1. Understand the fundamental concepts of computer troubleshooting.
2. Identify and diagnose common hardware and software problems.
3. Utilize troubleshooting techniques to resolve computer-related issues.
4. Develop effective problem-solving strategies and critical thinking skills.
5. Enhance communication skills to effectively assist users in troubleshooting.

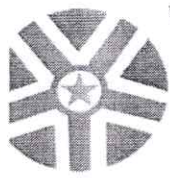
COURSE OUTCOME:

1. Understanding of Domains
2. Foundational Knowledge
3. Interdisciplinary Perspective
4. Application of Domain Knowledge
5. Awareness of Industry Trends

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Course Syllabus:

Unit 1: Introduction to Computer Troubleshooting, Overview of computer troubleshooting, Troubleshooting methodologies and strategies , Essential tools for computer troubleshooting, Safety precautions during troubleshooting,

Unit 2: Hardware Troubleshooting, Common hardware problems and their symptoms, Troubleshooting computer startup issues, Diagnosing and resolving hardware component failures Troubleshooting peripheral devices.

Unit 3: Software Troubleshooting, Identifying software conflicts and compatibility issues, Troubleshooting operating system errors and crashes, Resolving software installation and update problems, Troubleshooting software application errors.

Unit 4: Network Connectivity Troubleshooting, Understanding network connectivity issues, Diagnosing and resolving network connection problems, Troubleshooting internet connectivity issues, Dealing with wireless network problems

Unit 5: Data and Security Troubleshooting, Troubleshooting data backup and recovery issues, Identifying and resolving common data loss scenarios, Addressing computer security-related problems, Dealing with malware infections and security breaches

Some reference textbooks:

1. "CompTIA A+ Certification All-in-One Exam Guide" by Mike Meyers
2. "Troubleshooting and Maintaining Your PC All-in-One For Dummies" by Dan Gookin
3. "PC Troubleshooting Pocket Guide" by Jean Andrews


COORDINATOR


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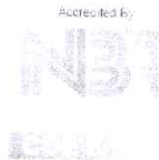
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DEPARTMENT OF INFORMATION TECHNOLOGY

ADD ON COURSE NAME: FUNDAMENTAL CONCEPTS OF COMPUTER TROUBLESHOOTING

Academic Year: 2019-20

Date: 17.01.2020

B. Tech. IT- II - I SEM

DAY WISE SCHEDULE SHEET

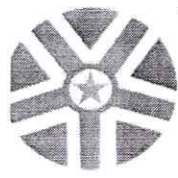
Venue: A-Block - AFF-2 (Classroom)

Sl. No.	DAY	Topic	Timings	Duration
1	20.01.2020	Introduction to Computer Troubleshooting	11.00 a.m. to 12.00 a.m.	01 hour
2	21.01.2020	Troubleshooting Methodologies and Strategies	11.00 a.m. to 12.00 a.m.	01 hour
3	22.01.2020	Essential Tools for Computer Troubleshooting	11.00 a.m. to 12.00 a.m.	01 hour
4	23.01.2020	Safety Precautions during Troubleshooting	11.00 a.m. to 12.00 a.m.	01 hour
5	24.01.2020	Common Hardware Problems and Symptoms	11.00 a.m. to 12.00 a.m.	01 hour
6	27.01.2020	Troubleshooting Computer Startup Issues	11.00 a.m. to 12.00 a.m.	01 hour
7	28.01.2020	Diagnosing and Resolving Hardware Component Failures	11.00 a.m. to 12.00 a.m.	01 hour
8	29.01.2020	Troubleshooting Peripheral Devices (e.g., Printers, Scanners)	11.00 a.m. to 12.00 a.m.	01 hour
9	30.01.2020	Identifying Software Conflicts and Compatibility Issues	11.00 a.m. to 12.00 a.m.	01 hour
10	31.01.2020	Troubleshooting Operating System Errors and Crashes	11.00 a.m. to 12.00 a.m.	01 hour
11	03.02.2020	Resolving Software Installation and Update Problems	11.00 a.m. to 12.00 a.m.	01 hour
12	04.02.2020	Troubleshooting Software Application Errors	11.00 a.m. to 12.00 a.m.	01 hour
13	05.02.2020	Understanding Network Connectivity Issues	11.00 a.m. to 12.00 a.m.	01 hour




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14	06.02.2020	Diagnosing and Resolving Network Connection	11.00 a.m. to 12.00 a.m	01 hour
15	07.02.2020	Troubleshooting Internet Connectivity Issues	11.00 a.m. to 12.00 a.m	01 hour
16	10.02.2020	Dealing with Wireless Network Problems	11.00 a.m. to 12.00 a.m	01 hour
17	11.02.2020	Troubleshooting Data Backup and Recovery Issues	11.00 a.m. to 12.00 a.m	01 hour
18	13.02.2020	Identifying and Resolving Common Data Loss Scenarios	11.00 a.m. to 12.00 a.m	01 hour
19	14.02.2020	Addressing Computer Security-related Problems	11.00 a.m. to 12.00 a.m	01 hour
20	17.02.2020	Dealing with Malware Infections and Security Breaches	11.00 a.m. to 12.00 a.m	01 hour
21	18.02.2020	Developing Effective Problem-Solving Skills	11.00 a.m. to 12.00 a.m	01 hour
22	19.02.2020	Enhancing Communication Skills for Technical Support	11.00 a.m. to 12.00 a.m	01 hour
23	20.02.2020	Documenting and Reporting Troubleshooting Procedures	11.00 a.m. to 12.00 a.m	01 hour
24	24.02.2020	Providing Exceptional Customer Service and User Support	11.00 a.m. to 12.00 a.m	01 hour
25	25.02.2020	Troubleshooting BIOS and UEFI Issues	11.00 a.m. to 12.00 a.m	01 hour
26	26.02.2020	Resolving Blue Screen of Death (BSOD) Errors	11.00 a.m. to 12.00 a.m	01 hour
27	27.02.2020	Troubleshooting Network Printing Issues	11.00 a.m. to 12.00 a.m	01 hour
28	28.02.2020	Dealing with Slow Performance and System Hangs	11.00 a.m. to 12.00 a.m	01 hour
29	02.03.2020	Troubleshooting Email and Internet Browser Problems	11.00 a.m. to 12.00 a.m	01 hour
30	09.03.2020	Resolving Hardware Driver Compatibility Issues	11.00 a.m. to 12.00 a.m	01 hour
31	10.03.2020	Troubleshooting Audio and Video Playback Issues	11.00 a.m. to 12.00 a.m	01 hour
32	11.03.2020	Diagnosing and Resolving Hardware Overheating Problems	11.00 a.m. to 12.00 a.m	01 hour
Total – 32 hours				


COORDINATOR


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DEPARTMENT OF INFORMATION TECHNOLOGY

PROGRAM REPORT

Add-on Course: FUNDAMENTAL CONCEPTS OF COMPUTER TROUBLESHOOTING

Course Duration: 20th January 2020 to 11th March 2020 (32days)

Time : 11.00 a.m. to 12.00 a.m. (1 hour)

Resource Person: Dr. S. Ranga Swamy

Course Coordinator: Mr. K. Bharath Reddy

Number of Participants: 34

Introduction:

We are pleased to present the conclusion report for the add-on course titled "Fundamental Concepts of Computer Troubleshooting," which was conducted from 20th JAN 2020 to 11th March 2020. The course aimed to provide students with a comprehensive understanding of the fundamental concepts related to computers and equip them with troubleshooting skills to effectively identify and resolve common computer issues.

Course Delivery and Activities:

The course was delivered through a series of interactive lectures, hands-on practical sessions, and real-world case studies. Dr. S. Ranga Swamy, an esteemed resource person with extensive expertise in computer troubleshooting, facilitated engaging learning experiences and shared valuable insights. Mrs. K. Bharath Reddy, the course coordinator, ensured smooth organization and coordination of course activities.



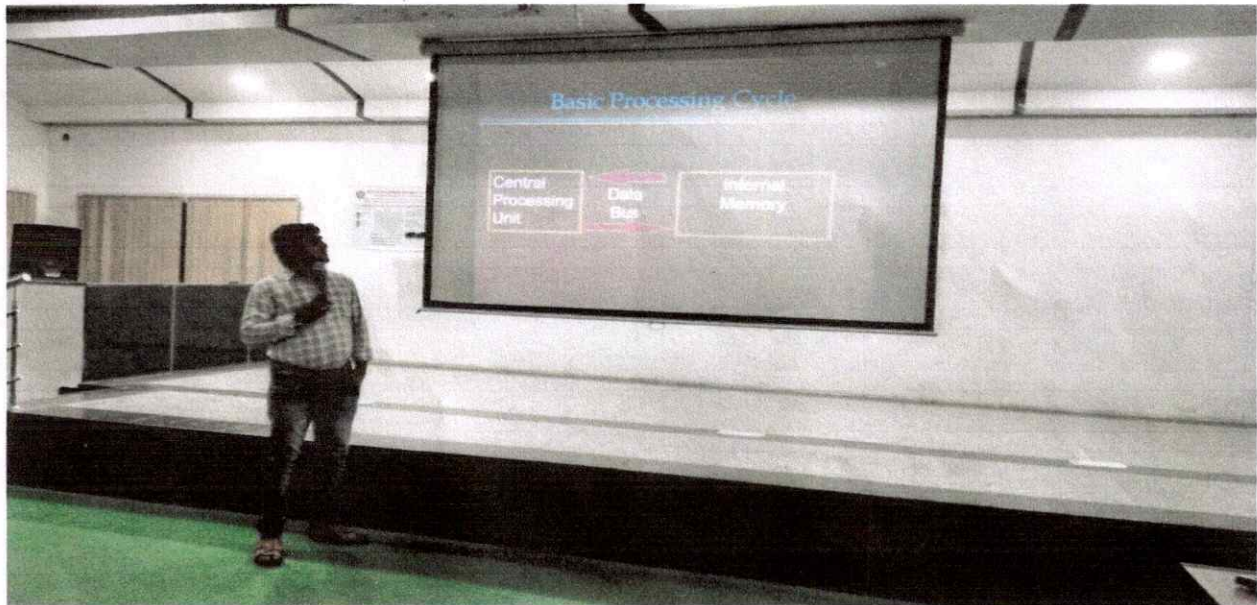
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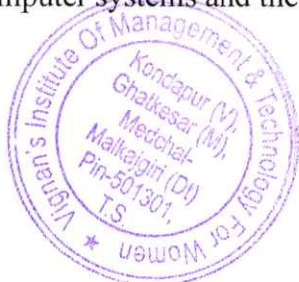


Course Completion:

We are delighted to announce that all 34 IT B. Tech 1st Year 2nd Semester students who participated in the course have successfully completed the requirements and have been awarded the course completion certificate. We congratulate all the participants for their active participation, dedication, and commitment throughout the course.

Achievements and Learning Outcomes:

1. Students gained a comprehensive understanding of fundamental concepts related to computer systems and their components.



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2. They developed the ability to identify and troubleshoot common hardware and software issues effectively.
3. Participants acquired knowledge and techniques for computer maintenance and optimization to ensure optimal performance.
4. The course enhanced critical thinking and problem-solving abilities in the context of computer troubleshooting.

COURSE OUTCOMES:

Course Outcomes (CO)	Program Outcomes (PO)	Strength of Mapping
CO1: Understand the fundamental concepts	PO1: Apply knowledge of mathematics, science	5
CO2: Identify and diagnose common hardware	PO2: Identify, analyze, and solve complex engineering	5
CO3: Utilize troubleshooting techniques to resolve	PO3: Design and conduct experiments, as well as to analyze	3
CO4: Develop effective problem-solving strategies	PO4: Design and develop solutions for complex engineering	5
CO5: Enhance communication skills to effectively	PO5: Use modern engineering tools, software, and equipment	5

We express our sincere gratitude to Dr. S. Ranga Swamy for his invaluable contributions as the resource person.

Conclusion:

The add-on course on Fundamental Concepts of Computer Troubleshooting was a resounding success, providing students with a comprehensive understanding of computer systems and equipping them with troubleshooting skills. We are confident that the knowledge and skills gained during this course will prove beneficial in their academic pursuits and future careers.


COORDINATOR




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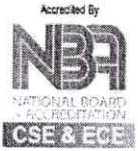

HOD

Head of the Department
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DEPARTMENT OF BASIC SCIENCE AND HUMANITIES

VMTW/BSH/AOC/2019-20/II/02

Date: 28-01-2020

CIRCULAR

This is to inform all I-II B.Tech CSE students that there is going to be an addon course on "Joy of Python" that will be held from 03-02-2020 to 08-02-2020 by Mrs.B.Geetha, Asst. Professor, Dept of CSE, VMTW. All students are welcome to participate in this Addon course. Interested students can sign in the registration form and communicate to the program coordinator Mr.Vishnu Vardhan Rao, Assistant Professor, Dept of CSE, VMTW.

T.V.P.
HOD

Head of the Department
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COURSE MODULES:

Module 1:

Python Basics, Objects- Python Objects, Standard Types, Other Built-in Types, Internal Types, Standard Type Operators, Numbers, operators, list, set, tuple.

Module 2:

FILES: File Objects, File Built-in Function [open()], File Built-in Methods, File Built-in Attributes, Standard Files, Command-line Arguments, Exceptions in Python, Detecting and Handling Exceptions.

Module 3:

Regular Expressions: Introduction, Special Symbols and Characters, Multithreaded Programming: Introduction, Threads and Processes, Threading Module.

Module 4:

GUI Programming: Introduction, Tkinter and Python Programming, Brief Tour of Other GUIs
WEB Programming: Introduction, Web Surfing with Python, Creating Simple Web Clients.

Module 5:

Database Programming: Introduction, Python Database Application Programmer's Interface (DB-API), Object Relational Managers (ORMs).

COURSE OBJECTIVES:

1. To Learn Syntax and Semantics and create Functions in Python.
2. To know how to Handle Strings and Files in Python.

3. To Understand Lists, Dictionaries and Regular expressions in Python.
4. To Implement Object Oriented Programming concepts in Python.
5. To Build Web Services and introduction to Network and Database Programming in Python

EXPECTED OUTCOMES:

At the end of the course, students will have the ability to:

- Understand why Python is a useful scripting language for developers.
- Learn how to use lists, tuples, and dictionaries in Python programs.
- Learn how to identify Python object types.
- Define the structure and components of a Python program.
- Acquire programming skills in core Python.



Resource Person:
Mrs. B. Geetha,
Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (Dt)-501301
Telangana State

Assistant Professor,
Department of CSE.

Co-ordinator:

Mr. M. Vishnuvardhana Rao,
Assistant Professor,
Department of CSE.

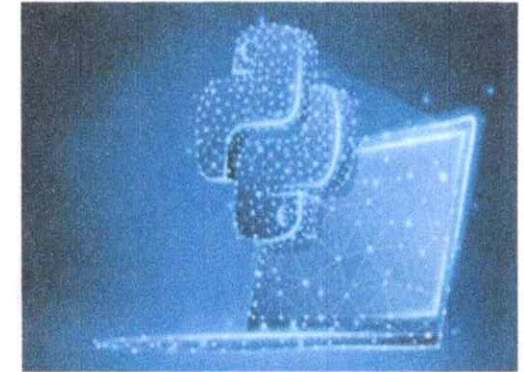
Contact No: +91 8639936639

LAST DATE FOR REGISTRATION: 1ST FEB, 2020

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ADD-ON COURSE ON

“JOY OF PYTHON”

3RD TO 8TH FEB, 2020

Duration of the Course : 36 Hrs

Venue:

‘A’-Block Seminar Hall,
VMTW.

Organized by:

DEPARTMENT OF
COMPUTER SCIENCE AND ENGINEERING





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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

ADDON COURSE SYLLABUS

ADDON COURSE: Joy of Python

Course objectives:

1. To Learn Syntax and Semantics and create Functions in Python.
2. To know how to Handle Strings and Files in Python.
3. To Understand Lists, Dictionaries and Regular expressions in Python.
4. To Implement Object Oriented Programming concepts in Python.
5. To Build Web Services and introduction to Network and Database Programming in Python.

Module 1:

Python Basics, Objects- Python Objects, Standard Types, Other Built-in Types, Internal Types, Standard Type Operators, Numbers, operators, list, set, tuple.

Module 2:

FILES: File Objects, File Built-in Function [open()], File Built-in Methods, File Built-in Attributes, Standard Files, Command-line Arguments, Exceptions in Python, Detecting and Handling Exceptions.

Module 3:

Regular Expressions: Introduction, Special Symbols and Characters, Multithreaded Programming: Introduction, Threads and Processes, Threading Module.

Module 4:

GUI Programming: Introduction, Tkinter and Python Programming, Brief Tour of Other GUIs
WEB Programming: Introduction, Web Surfing with Python, Creating Simple Web Clients.

Module 5:

Database Programming: Introduction, Python Database Application Programmer's Interface (DB-API), Object Relational Managers (ORMs).

Text Book

1. Think Python, Allen Downey, Green Tea Press.

Muller
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N. Indira
HOD

Head of the Department
Computer Science and Engineering
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DAY WISE SCHEDULE SHEET

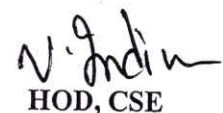
Addon Course: Joy of Python

Room No: A Block Seminar Hall-ASF-5


DAY	Topic	Timings	Duration
03-02-2020	Python Basics, Objects- Python Objects, Standard Types, Other Built-in Types, Internal Types, Standard Type Operators, Numbers, operators, list, set, tuple.	09:00 am to 03:30 pm	6 hrs
04-02-2020	FILES: File Objects, File Built-in Function [open()], File Built-in Methods, File Built-in Attributes, Standard Files, Command-line Arguments, Exceptions in Python, Detecting and Handling Exceptions.	09:00 am to 03:30 pm	6 hrs
05-02-2020	Regular Expressions: Introduction, Special Symbols and Characters, Multithreaded Programming: Introduction, Threads and Processes, Threading Module.	09:00 am to 03:30 pm	6 hrs
06-02-2020	GUI Programming: Introduction, Tkinter and Python Programming, Brief Tour of Other GUIs	09:00 am to 03:30 pm	6 hrs
07-02-2020	WEB Programming: Introduction, Web Surfing with Python, Creating Simple Web Clients.	09:00 am to 03:30 pm	6 hrs
08-02-2020	Database Programming: Introduction, Python Database Application Programmer's Interface (DB-API), Object Relational Managers (ORMs).	09:00 am to 03:30 pm	6 hrs
Total 36 hours			


COORDINATOR




HOD, CSE

Head of the Department
Computer Science and Engineering
Vignan : Institute of Management and Technology for Women
Kondapur (V), Ghatkesar (M), R.R. Dist-501 301.


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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Date: 10-02-2020

Addon course Report

Name of the Add on Course: Joy of Python

Day/Duration: 03-02-2020 to 08-02-2020 6 (Days) – 36 Hrs

Time: 09:00am to 03:30pm

Resource Person: Mrs.B.Geetha, Asst. Professor, Dept of CSE, VMTW.

Name of the Coordinator: Mr.Vishnu Vardhan Rao, Asst. Professor, Dept of CSE, VMTW.

Number of the Participants: 120

Topics covered: The following topics covered in this program

- ❖ Python Objects, Standard Types, Internal Types, Standard Type Operators, Numbers, operators, list, set, tuple.
- ❖ File Built-in Methods, File Built-in Attributes, Standard Files, Command-line Arguments.
- ❖ Symbols and Characters, Multithreaded Programming: Introduction, Threads and Processes.
- ❖ Brief Tour of Other GUIs WEB Programming: Introduction, Web Surfing with Python.
- ❖ Python Database Application Programmer's Interface (DB-API), Object Relational Managers

Course Outcomes

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Understand why Python is a useful scripting language for developers.	PO1	4
CO2	Learn how to use lists, tuples, and dictionaries in Python programs.	PO2	4
CO3	Learn how to identify Python object types	PO1	4
CO4	Define the structure and components of a Python program.	PO3	3



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CO5	Acquire programming skills in core Python.	PO2	3
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Addon course sessions by Mrs.B.Geetha, Asst. Professor, Dept of CSE, VMTW


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Vignan's Institute of Management and Technology for Women
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DEPARTMENT OF BASIC SCIENCE AND HUMANITIES

VMTW/BSH/AOC/2019-20/II/03

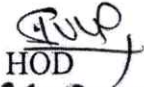
Date: 04-02-2020

CIRCULAR

This is to inform all the I B. Tech ECE students that, the Department is organizing an Add on course on "Design of PCB". Department advisory committee has been identified Mr.M.KARTHIKPAL, Assistant Professor in ECE Dept. for the course., He will be available 6 days to complete the course, which will take place from 10-02-2020 TO 15-02-2020. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions, hence those who are interested fill the registration form.

Copy to

- The Principal, for kind information
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- Notice board
- I.B.Tech ECE students
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Head of the Department
Basic Sciences and Humanities
Vignans Institute of Management and Technology for Women,
Kondapur (V), Ghatkesar (M), R R Dist-501 301.




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Telangana State

COURSE MODULES:

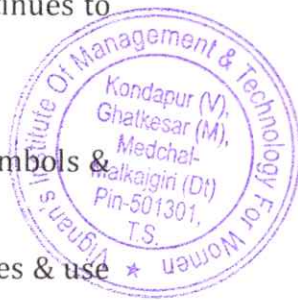
This is a basic course for designing PCB using the software. PCB (Printed Circuit Board) designing is an integral part of each electronics product and this program is designed to make students capable to design their own projects PCB up to industrial grade.

This skill is called PCB design and it's easier than many think. In this guide, I'll lay out all the steps for you. And once you've gone through these steps one time, the second time everything is much easier. Without Printed Circuit Boards, our devices would not be able to function. They are essential in connecting different components and allowing for communication between them. Additionally, PCBs help protect these components from damage and interference.

PCB designing is a growing field with many opportunities for skilled talent in terms of career opportunities. With rising advancements in technology, communications and computing, the need for professional PCB designers continues to grow.

COURSE OBJECTIVES:

- Identifying Electronic Components Symbols & Footprints
- Constructing your Component libraries & use them effectively
- Schematic creation & interpretation
- Effective use of design rules & interfacing between schematic & PCB



- Component placement & routing techniques for various technologies

EXPECTED OUTCOMES:

At the end of the course, students will have the ability to:

- Students are able to design a schematic of their circuit. Students are able to design PCB layout of their design. Students are capable to produce PCB of their own circuit.
- The course is intended to give the students the necessary knowledge and of PCB design steps, starting from a simple schematic, through creating new components, and all the way to down a final PCB layout.
- Recognize the technologies used in electronic industry through the practical experience gained in the course.

Resource Person:

Mr. M. Karthikpal,

Assistant Professor,
Department of ECE.

Co-ordinator:

Mr. G. Ganesh Reddy,

Assistant Professor,
Department of ECE.

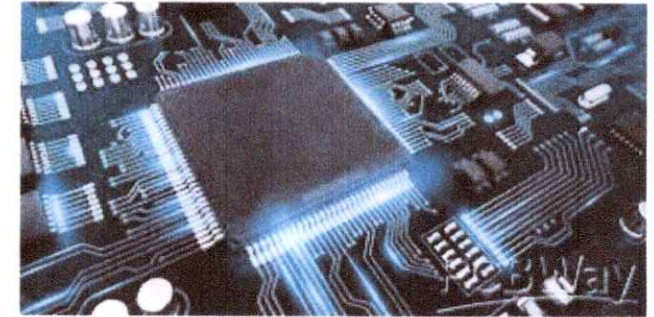
Contact No: +91 9951407307

LAST DATE FOR REGISTRATION: 6TH FEB, 2020

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ADD-ON COURSE ON

“DESIGN OF PCB”

10TH TO 15TH FEB, 2020

Duration of the Course : 36 Hrs

Venue:

Room No-BFF-8,
VMTW.

Organized by:

**DEPARTMENT OF ELECTRONICS
AND COMMUNICATION ENGINEERING**



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Academic Year: 2019-20

Date: 04-02-2020

ADD ON COURSE SYLLABUS: Design of PCB

B.Tech I-II SEM

VENUE: BFF-8

SECTIONS: ECE-A&B

COURSE OBJECTIVES:

- Identifying Electronic Components Symbols & Footprints
- Constructing your Component libraries & use them effectively
- Schematic creation & interpretation
- Effective use of design rules & interfacing between schematic & PCB
- Component placement & routing techniques for various technologies

Module I:

Introduction to Printed circuit board: fundamental of electronic components, basic electronic circuits, Basics of printed circuit board designing: Layout planning, general rules and parameters, ground conductor considerations, thermal issues, check and inspection of artwork.

Module II:

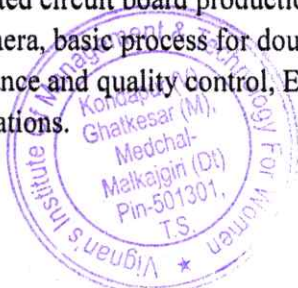
Design rules for PCB: Design rules for Digital circuit PCBs, Analog circuit PCBs, high frequency and fast pulse applications, Power electronic applications, Microwave applications, Module III Static CMOS Logic Elements

Module III:

Introduction to Electronic design automation(EDA) tools for PCB designing: Brief Introduction of various simulators, SPICE and PSpice Environment, Selecting the Components Footprints as per design, Making New Footprints, Assigning Footprint to components, Net listing, PCB Layout Designing, Auto routing and manual routing. Assigning specific text (silkscreen) to design, Creating report of design, creating manufacturing data (GERBER) for design

Module IV

Introduction printed circuit board production techniques Photo printing, film master production, reprographic camera, basic process for double sided PCBs photo resists, Screen printing process, plating, relative performance and quality control, Etching machines, Solders alloys, fluxes, soldering techniques, Mechanical operations.




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Module V

PCB Technology Trends: Multilayer PCBs, Multiwire PCB, Flexible PCBs, Surface mount PCBs, Reflow soldering, Introduction to High-Density Interconnection (HDI) Technology.

Text Books:

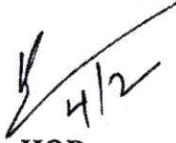
1. Printed circuit board design, fabrication assembly and testing By R. S. Khandpur, Tata McGraw Hill 2006

Reference Books:

1. Printed circuit Board Design and technology, Walter C. Bosshart
2. Printed Circuits Handbook, Sixth Edition, by Clyde F. Coombs, Jr, Happy T. Holden, Publisher: McGraw-Hill Education Year: 2016
3. Complete PCB Design Using OrCAD Capture and PCB Editor, Kraig Mitzner Bob Doe Alexander Akulin Anton Suponin Dirk Müller, 2nd Edition 2009.
4. Introduction to System-on-Package, Rao R Tummala & Madhavan Swaminathan, McGraw Hill, 2008.


RESOURCE PERSON


COORDINATOR


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Head of the Department
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Academic Year: 2019-20

Date: 04-02-2020

ADD ON COURSE NAME: Design of PCB

B.Tech I-II SEM

VENUE: BFF-8

SECTIONS: ECE-A&B

DAY-WISE SCHEDULE

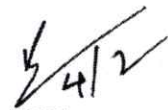
S.NO	DAY	TOPIC	TIMINGS	DURATION
1.	10-02-2020	Introduction to Printed circuit board: fundamental of electronic components, basic electronic circuits, Basics of printed circuit board designing: Layout planning, general rules and parameters, ground conductor considerations, thermal issues, check and inspection of artwork	9AM to 4 PM	6 hours
2.	11-02-2020	Design rules for PCB: Design rules for Digital circuit PCBs, Analog circuit PCBs, high frequency and fast pulse applications, Power electronic applications, Microwave applications, Module III Static CMOS Logic Elements	9AM to 4 PM	6 hours
3.	12-02-2020	Introduction to Electronic design automation (EDA) tools for PCB designing: Brief Introduction of various simulators, SPICE and PSpice Environment, Selecting the Components Footprints as per design, Making New Footprints	9AM to 4 PM	6 hours
4.	13-02-2020	Assigning Footprint to components, Net listing, PCB Layout Designing, Auto routing and manual routing. Assigning specific text (silkscreen) to design, Creating report of design, creating manufacturing data (GERBER) for design	9AM to 4 PM	6 hours




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5.	14-02-2020	Introduction printed circuit board production techniques Photo printing, film master production, reprographic camera, basic process for double sided PCBs photo resists, Screen printing process, plating, relative performance and quality control, Etching machines, Solders alloys, fluxes, soldering techniques, Mechanical operations, fluxes, soldering techniques, Mechanical operations	9AM to 4 PM	6 hours
6.	15-02-2020	PCB Technology Trends: Multilayer PCBs. Multiwire PCB, Flexible PCBs, Surface mount PCBs, Reflow soldering, Introduction to High-Density Interconnection (HDI) Technology. Feedback Taken & Test Conducted	9AM to 4 PM	6 hours
Total 36 hours				


COORDINATOR


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 17-02-2020

PROGRAM REPORT

Name of the Add on Course: Design of PCB

Day/Duration: 6 Days/36 Hours

Time: 9AM to 4PM

Resource Person: Mr.M.KARTHIKPAL, Assistant Professor in ECE Dept, VMTW, Hyderabad

Name of the Coordinator: Mr. G. Ganesh Reddy, Assistant Professor Dept. of ECE, VMTW, Hyderabad.

Number of the Participants: 80

Topics covered: The following topics covered in this program

1. Introduction to PCB designing concepts
2. Component introduction and their categories
3. Introduction to Development Tools
4. Detailed description and practical of PCB designing
5. Lab practice and designing concepts

Course Outcomes:

COs	At the end of the course, students will have the ability to:	POs Mapped	Strength of mapping
CO1	Able to design a schematic of their circuit	PO2,P04	2
CO2	Able to design PCB layout of their design	PO3,PO5	3
CO3	Able produce PCB of their own circuit	PO3,PO5	3
CO4	Recognize the technologies used in electronic industry through the practical experience gained in the course.	PO1,PO4	2
CO5	Design steps, starting from a simple schematic, through creating new components, and all the way to down a final PCB layout.	PO5,PO3	2




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Assessment Procedure: The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode.

Summary:

Department of ECE has organized add on course on “Design of PCB” for the year 2019-20, was provided to I BTech ECE students from 10-02-2020 to 15-02-2020. PCB designing is a growing field with many opportunities for skilled talent in terms of career opportunities. With rising advancements in technology, communications and computing, the need for professional PCB designers continues to grow. But the syllabus did not cover this language in the academics. Considering this gap, we offered the course as an add-on to the I year students. The program was coordinated by Mr. G. Ganesh Reddy, Assistant Professor Dept. of ECE and conducted by Mr.M.KARTHIKPAL, Assistant Professor in ECE Dept, VMTW. 80 students registered registered out of which 78 students did complete their course successfully completing the end course examination and all were issued certificates.



Ganesh Reddy
COORDINATOR

M. Karthikpal
HOD

Head of the Department
Electronics and Communication Engineering
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M. Karthikpal
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CS

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VMTW/ECE/AOC/CIR/2019-20/I/01

Date: 07-08-2019

CIRCULAR

This is to inform all the IV B. Tech ECE students that there will be an Add on course on "Latest developments in microwave antenna design and its Applications" in association with True Chip PVT LTD., from 26-08-2019 to 12-11-2019. All the students are invited to take part in this course. The participating students will be honored with certificates of participation. During the course you will be encouraged to interact with the speaker and the other participants through the case study and discussions.

Rskudra
7/8
HOD

Head of the Department
Electronics and Communication Engineering
Vignan's Institute of Management & Technology For Women
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IV.B.Tech students

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Telangana State

ABOUT THE COURSE

This course is devoted to address the need to enhance the knowledge about the Antenna design and the latest development pertaining to RF and Microwave circuits. This Program is designed for all engineering disciplines. This niche program particularly aims to impart these quintessential skills to the participants. Antenna engineering is a fast-growing field of communication concerned with the study and design of different type of antennas for various applications. The participants will be trained with hands-on approach to have an in-depth insight into the domain of communication and expose them to Feasibility & future scope.

COURSE OBJECTIVES

- Microwave devices and circuits
- Satellite communications
- Microstrip & CPW antennas
- Implantable Antennas
- MW antennas by using SIW technology
- Reconfigurable planar antenna design and simulation methodology
- Wearable UWB Antennas for body area network
- Metamaterial
- Millimetre wave and sub millimetre-wave technologies
- Smart & MIMO antenna design
- Antenna parameters and design considerations
- MM Wave/THz Integrated Circuits
- EMI/EMC

EXPECTED OUTCOMES

At the end of Workshop, participants will be able to

- Have an in-depth knowledge on Microwave devices and circuits & Satellite communications.
- Have an in-depth knowledge on Micro strip & CPW antennas, Implantable Antennas.
- Have an in-depth knowledge on Reconfigurable planar antenna design and simulation methodology.
- Working with Wearable UWB Antennas for body area network, Millimeter wave and sub millimeter-wave technologies.
- Working with Smart & MIMO antenna design, Antenna parameters and design considerations.

Resource Person:

Mr. M. Achyuth Reddy,

Technical Manager,
Truechip, Noida.

Coordinator:

Dr. J. Anil Kumar,

Assistant Professor,
Department of ECE,

Contact No: +91 83747 17668

LAST DATE FOR REGISTRATION: 24TH AUG, 2019

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ADD-ON COURSE ON "LATEST DEVELOPMENT IN MICROWAVE ANTENNA DESIGN AND ITS APPLICATIONS" 26TH AUG TO 12TH NOV, 2019

In collaboration with:



Duration of the Course : 35 Hrs

PRINCIPAL

Vignan's Institute of Management & Technology For Women
Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (D),
Telangana State

Venue:

B-BLOCK SEMINAR HALL, VMTW

Organized by:

DEPARTMENT OF ELECTRONICS AND
COMMUNICATION ENGINEERING



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

LATEST DEVELOPMENTS IN MICROWAVE ANTENNA DESIGN AND ITS APPLICATIONS

Academic Year: 2018-19

B.Tech IV-I SEM

SYLLABUS

SECTION: ECE-A&B

COURSE OBJECTIVES

1. To understand the concept of Reconfigurable planner antenna design and simulation Methodology
2. To analyze the characteristics of Smart & MIMO antenna design
3. To understand the concepts of Micro strip & CPW antennas
4. To analyze the characteristics of Microwave devices and circuits

Module – I: Antenna Theory: Antennas, Radiation concept, Types of Antennas, Antenna parameters, Friis Transmission equation.

Module – II: Aperture Antenna: Introduction, Pyramidal Horns- Design Procedure, Conical and Corrugated Horns, Aperture Corrugated Horns, Reflected Antennas- Parameters, Analysis of front-fed parabolic reflector, Feed methods and feed types, Cassegrain Reflector Horns.

Module – III: Micro strip Radiators: Introduction, Rectangular Microstrip Antenna analysis and Design, Circular Micro strip Antenna Analysis and Design,

Module – IV : Micro strip Slot Antennas: Wave guide fed slots, Radiation mechanism, Micro strip slot antennas, Introduction to rectangular slot antennas, narrow, wide, tapered and circularly polarized slot antennas, Annular slot antennas, Comparison of microstrip slot antennas with patch antennas.

Module – V : Micro Strip Antenna Arrays: Introduction, Micro strip array antennas, Characteristics of fixed beam linear antenna arrays, Linear micro strip arrays, Characteristics of planar arrays, Microstrip planar arrays, Microstrip scanned array antennas, Phase scanned microstrip arrays, Time delay scanning, Electronic feed switching, Frequency scanned microstrip arrays, Advantage and disadvantages of phased array antennas.




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REFERENCE BOOKS:

1. Ramesh Garg, Prakash Bhatia, Inder Bahl, Apisak Ittipiboon, "Microstrip Antenna Design Hand Book", Artech House Inc., 2001.
2. Samuel Silver, "Microwave Antenna – Theory and design", IEE Press, 1984.
3. James. J R. Hall, P S. Wood. C, "Micro strip Antenna-Theory and Design", Peter Peregrinus Ltd., 1981.


COORDINATOR


RESOURCE PERSON


HOD
Head of the Department
Electronics and Communication Engineering
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE NAME: Latest Developments in Microwave Antenna Design and Its Applications

Academic Year: 2019-20

Date: 21-08-2019

B.Tech IV-I SEM

DAY WISE SCHEDULE SHEET

SECTION: ECE-A&B

Room number: BFF6

S.no	DAY	Topic	Timings	Duration
1.	26-08-19	Antenna Theory	04:00pm to 05:00pm	1 hour
2.	27-08-19	Antennas Introduction	04:00pm to 05:00pm	1 hour
3.	28-08-19	Radiation concept	04:00pm to 05:00pm	1 hour
4.	29-08-19	Types of Antennas	04:00pm to 05:00pm	1 hour
5.	30-08-19	Antenna parameters	04:00pm to 05:00pm	1 hour
6.	31-08-19	Friis Transmission equation	04:00pm to 05:00pm	1 hour
7.	01-09-19	Aperture Antenna	04:00pm to 05:00pm	1 hour
8.	02-09-19	Aperture Antenna : Introduction	04:00pm to 05:00pm	1 hour
9.	03-09-19	Pyramidal Horns	04:00pm to 05:00pm	1 hour
10.	04-09-19	Design Procedure	04:00pm to 05:00pm	1 hour
11.	05-09-19	Conical and Corrugated Horns	04:00pm to 05:00pm	1 hour
12.	06-09-19	Aperture Corrugated Horns	04:00pm to 05:00pm	1 hour
13.	07-09-19	Reflected Antennas	04:00pm to 05:00pm	1 hour
14.	09-09-19	Reflected Antennas :Parameters	04:00pm to 05:00pm	1 hour
15.	11-09-19	Analysis of front-fed parabolic	04:00pm to 05:00pm	1 hour
16.	13-09-19	Feed methods and feed types	04:00pm to 05:00pm	1 hour
17.	16-09-19	Cassegrain Reflector Horns.	04:00pm to 05:00pm	1 hour
18.	17-09-19	Microstrip Radiators	04:00pm to 05:00pm	1 hour
19.	18-09-19	Introduction	04:00pm to 05:00pm	1 hour
20.	19-09-19	Rectangular Antenna Design	04:00pm to 05:00pm	1 hour
21.	20-09-19	Circular μ strip Antenna Analysis and Design	04:00pm to 05:00pm	1 hour
22.	23-09-19	Microstrip Slot Antennas	04:00pm to 05:00pm	1 hour
23.	24-09-19	Wave guide fed slots	04:00pm to 05:00pm	1 hour
24.	25-09-19	Radiation mechanism	04:00pm to 05:00pm	1 hour
25.	26-09-19	Micro strip slot antennas	04:00pm to 05:00pm	1 hour
26.	27-09-19	Introduction to rectangular slot	04:00pm to 05:00pm	1 hour
27.	01-10-19	Narrow, wide	04:00pm to 05:00pm	1 hour
28.	03-10-19	Tapered and circularly polarized slot	04:00pm to 05:00pm	1 hour
29.	04-10-19	Annular slot antennas	04:00pm to 05:00pm	1 hour
30.	01-11-19	Comparison of microstrip slot	04:00pm to 05:00pm	1 hour
31.	04-11-19	Micro Strip Antenna Arrays	04:00pm to 05:00pm	1 hour




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32.	05-11-19	Characteristics of fixed beam linear	04:00pm to 05:00pm	1 hour
33.	06-11-19	Time delay scanning	04:00pm to 05:00pm	1 hour
34.	12-11-19	Advantage and disadvantages of	04:00pm to 05:00pm	1 hour
35.	12-11-19	Test conducted & feedback Taken	04:00pm to 05:00pm	1 hour
Total 35 hours				


COORDINATOR


ECE

Head of the Department
Electronics and Communication Engineering
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 14-11-2019

PROGRAM REPORT

Name of the Add on Course: Latest developments in microwave antenna design and its Applications.

Day/Duration: 35 Hours.

Time: 4:00PM to 5:00PM.

Resource Person: Mr. M.Achyuth Reddy, Sr. Engineer, True Chip PVT, LTD.

Name of the Coordinator: Mr. J. Sunil Kumar, Assistant Professor, VMTW.

Number of the Participants: 83.

Topics covered: The following topics covered in this program

- Smart & MIMO antenna design
- Antenna parameters and design considerations
- MM Wave/THz Integrated Circuits.
- Have an in-depth knowledge on Microwave devices and circuits & Satellite communications.
- Have an in-depth knowledge on Micro strip & CPW antennas, Implantable Antennas.

Course Outcomes:

COs	At the end of the course, students will have the ability to:	POs Mapped	Strength of mapping
CO1	Understand Working with Smart & MIMO antenna design.	PO3,PO4	3
CO2	Understand the characteristics Micro wave device VTC	PO2,PO3	4
CO3	Working with Wearable UWB Antennas for body area network	PO2,PO4	2
CO4	Analyze the characteristics of Micro strip & CPW antennas.	PO2,PO3	2
CO5	Able to understand the characteristics Decimation in Frequency algorithms	P03,PO1	2




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Latest developments in microwave antenna design and its Applications: Add on course explaining by

Mr. M.AchyuthReddy, Sr. Engineer, True Chip PVT, LTD,

By having such a great event students are able to Work with Smart & MIMO antenna design, & understood the characteristics of Micro strip & CPW antennas, Decimation in Frequency algorithms. They have understood the Micro wave device VTC, and are able to Work with Wearable UWB Antennas for body area network.so students want to have such kind of events further more and are thankful for the department.

Assessment Procedure: The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode.


Coordinator


ECE, HOD

Head of the Department
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VMTW/ECE/AOC/CIR/2019-20/I/02

Date: 07-10-2019

CIRCULAR

This is to inform all the II B. Tech ECE students that, the Department organizing an Add on course on “Overview of MATLAB and its Applications for Engineers”, Department advisory committee has been identified Mr. P. Harikrishna, Associate Professor in ECE Dept. for the course, He will be available 30 days to complete the course, which will take place from 11-10-2019 to 30-11-2019. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions hence those who are interested fill the registration form.

HOD

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ABOUT THE COURSE

Nowadays programming knowledge has become very essential for engineering professionals as well as scientists and researchers to develop simulation models, perform analysis, optimization & decision-making. Knowledge of using modeling packages like AutoCAD, Pro/E, SolidWorks, CATIA, MS-Excel, etc. is not sufficient. Engineering professionals, scientists, and researchers are also expected to know, how to control the package by programming and customizing it as per the requirement. Many times, data exchange between different software packages has become a necessity to utilize expertise of different software packages and requires a neutral software tool like MATLAB/SCILAB to take the input from software, perform computations, and output the results to another software package. MATLAB and SCILAB are excellent tools for the visualization and manipulation of engineering data as well as performing various engineering computations. Software modules can also be developed using the GUI of MATLAB. It also provides various toolboxes to perform specialized computations.

COURSE OBJECTIVES

It is a hands-on experience-oriented programming skill development program with exhaustive application-specific programming exercises. The course will be beneficial to beginners of MATLAB/SCILAB as well as practitioners of different domains. Teaching methodology will be adopted in such a manner so that all can cope even those who do not possess any prior knowledge of programming. Apart from three days of workshop, one day of pre-workshop will also be organized for those who don't have prior knowledge of MATLAB/SCILAB, on January 09, 2014. Case studies of

advanced application of MATLAB will be presented by highly motivated researchers/faculty and will demonstrate their recent research work. Course content will cover a basic introduction to MATLAB/SCILAB, the use of various commands, logical operators, functions, arrays, loops, etc. printing, plotting visualization using MATLAB, data input/output in various formats, generating executable files and stand-alone applications, building Graphical user interface (GUI), introduction to Simulink etc., with exhaustive sample programs and exercises.

EXPECTED OUTCOMES

At the end of Workshop, participants will be able to

- Work with use of various commands.
- Work with programs with the use of Operators, functions, arrays, loops, plotting
- Generating Executable files and stand-alone applications.
- Building Graphical user interface (GUI)
- Introduction to Simulink
- Working with simulink in MATLAB

Resource Person:

Mr. P. Harikrishna,

Associate Professor,

Department of ECE,

Go-ordinator:

Mr. L. Sunil Kumar,

Assistant Professor,

Department of ECE,

Contact No: +91 83747 17668



LAST DATE FOR REGISTRATION: 8TH OCT, 2019

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ADD-ON COURSE ON

"OVERVIEW OF MATLAB AND ITS APPLICATIONS FOR ENGINEERS"

11TH OCT TO 30TH NOV, 2019

Duration of the Course : 30 Hrs

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Telangana State

Venue:

'B'-BLOCK SEMINAR HALL, VMTW

Organized by:

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Academic Year: 2019-20

Date: 07-10-2019

ADD ON COURSE SYLLABUS: Overview of MATLAB and its Applications for Engineers

B.Tech II-I SEM VENUE: B: BLOCK SEMINAR HALL

SECTION: ECE-A&B

COURSE OBJECTIVES:

1. To provide background and fundamentals of MATLAB tool for the analysis and processing Of signals and to generate various continuous and discrete time signals
2. To provide an overview of signal transmission through linear systems, convolution and Correlation of signals and sampling
3. To understand the concept and importance of Fourier and Z-Transforms
4. To perform operations on signals and sequences find the simulation results.
5. To Solve, Simulate and Analyse basic Electrical and Electronics Circuits and Applications

Module I: MATLAB Basics

The MATLAB Environment •– Creating Variables, Some Useful MATLAB Functions Operations with Variables, Standard Mat Lab windows, Mat Lab as {best} calculator. • Naming, Arrays – Checking

Module II: Operations with Variables

Arrays • Column and rows: creation and indexing - Matrix- Arrays- Size and length, Multiplication, Division, Writing script files

Module III Logical variables and operators

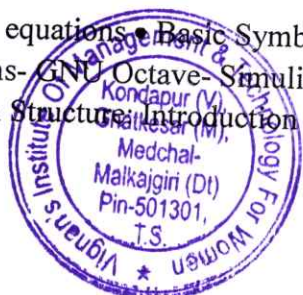
Files and File Management – Import/Export • Basic 2D, 3D plots • Graphic handling- Numbers- Strings

Module IV Data and data flow in MATLAB

Conditional Statements, Loops • MATLAB Programs – Programming and Debugging. • Applications of MATLAB Programming- Data import- Data output- Plotting- Graphics

Module V Modifying properties of graphics objects

Algebraic equations • Basic Symbolic Calculus and Differential equations • Numerical Techniques and Transforms- GNU Octave- Simulink, Graphing, Exercises: Graphing Functions Using MATLAB, Repetition Structures, Introduction to Loops



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Reference Books:

1. "A Guide to MATLAB - for Beginners and Experienced Users", 2nd Ed., Brian R. Hunt, Ronald L. Lipsman, Jonathan M. Rosenberg, Cambridge University Press, (2006).
2. "Essentials of MATLAB Programming", 2nd Ed., Stephen J. Chapman, Cengage Learning, (2009).
3. "MATLAB Demystified", David McMahon, The McGraw-Hill Companies, (2007).


RESOURCE PERSON


COORDINATOR


HOD



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE NAME: Overview of MATLAB and its Applications for Engineers

Academic Year: 2019-20

Date: 07-10-2019

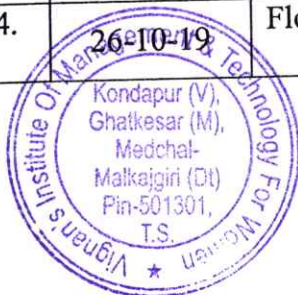
B.Tech II-I SEM

DAY WISE SCHEDULE SHEET

SECTION: ECE-A&B

Room number: B-BLOCK SEMINAR HALL

S.NO	DAY	TOPIC	TIMINGS	DURATION
1.	11-10-19	Mat Lab Introduction	04:00pm to 05:00pm	1 hour
2.	12-10-19	Standard Mat Lab windows	04:00pm to 05:00pm	1 hour
3.	14-10-19	Operations with Variables	04:00pm to 05:00pm	1 hour
4.	15-10-19	Naming, Checking existence	04:00pm to 05:00pm	1 hour
5.	16-10-19	Checking existence	04:00pm to 05:00pm	1 hour
6.	17-10-19	Clearing, Operations	04:00pm to 05:00pm	1 hour
7.	18-10-19	Clearing, Operations	04:00pm to 05:00pm	1 hour
8.	19-10-19	Arrays	04:00pm to 05:00pm	1 hour
9.	21-10-19	Column and rows: creation and indexing	04:00pm to 05:00pm	1 hour
10.	22-10-19	Size and length	04:00pm to 05:00pm	1 hour
11.	23-10-19	Multiplication, Division, Power	04:00pm to 05:00pm	1 hour
12.	24-10-19	Writing script files	04:00pm to 05:00pm	1 hour
13.	25-10-19	Logical variables and operators	04:00pm to 05:00pm	1 hour
14.	26-10-19	Flow control	04:00pm to 05:00pm	1 hour



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15.	28-10-19	Flow control	04:00pm to 05:00pm	1 hour
16.	29-10-19	Writing functions	04:00pm to 05:00pm	1 hour
17.	30-10-19	Loop Operators	04:00pm to 05:00pm	1 hour
18.	01-11-19	Input/output arguments	04:00pm to 05:00pm	1 hour
19.	02-11-19	Function visibility, path	04:00pm to 05:00pm	1 hour
20.	04-11-19	Mat Lab startup	04:00pm to 05:00pm	1 hour
21.	12-11-19	File Input-Output a. Mat Lab files	04:00pm to 05:00pm	1 hour
22.	13-11-19	Binary files d. Mixed text-binary files	04:00pm to 05:00pm	1 hour
23.	14-11-19	Communication with external devices ,Serial port ,Parallel	04:00pm to 05:00pm	1 hour
24.	15-11-19	Sound port d. Video port	04:00pm to 05:00pm	1 hour
25.	18-11-19	Polynomial fit a. 1D & 2D fits Data windowing	04:00pm to 05:00pm	1 hour
26.	22-11-19	Arbitrary function fit a. Error function b. Fixing Parameters	04:00pm to 05:00pm	1 hour
27.	25-11-19	Goodness of fit a. 2 criteria b. Error of parameters	04:00pm to 05:00pm	1 hour
28.	26-11-19	1. Pre-defined dialogs 2. Handle graphics a. Graphics	04:00pm to 05:00pm	1 hour
29.	28-11-19	Modifying properties of graphics objects	04:00pm to 05:00pm	1 hour
30.	30-11-19	Test conducted & Feedback taken	04:00pm to 05:00pm	1 hour
Total 30 hours				


 COORDINATOR




 HOD

Head of the Department
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 03-12-2019

PROGRAM REPORT

Name of the Add on Course: Overview of MATLAB and its Applications for Engineers

Day/Duration: 30 Hours

Time: 4pm to 5pm

Resource Person: Mr. P. Harikrishna, Associate professor Dept. of ECE, VMTW, Hyderabad

Name of the Coordinator: Mr. J. Sunil Kumar, Assistant Professor, Dept. of ECE, VMTW, Hyderabad

Number of the Participants: 110

Topics covered: The following topics covered in this program

1. Creating Variables, Some Useful MATLAB Functions Operations
2. Creation and indexing - Matrix- Arrays- Size and length
3. Graphic handling- Numbers- Strings
4. MATLAB Programming- Data import- Data output- Plotting
5. Graphing Functions Using MATLAB

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Understand the main features and importance of the MATLAB/ SCI LAB mathematical programming	PO2,PO3	2
CO2	Apply working knowledge of MATLAB/ SCI LAB package to simulate and solve Electrical, Electronic circuits	PO2,PO3	3
CO3	Solve, Simulate and Analyse various DC circuits	PO3,PO4	3
CO4	Apply the concepts of convolution and correlation for continuous time signals	PO2,PO4	2
CO5	Solve, Simulate and Analyse various Analog and Digital Electronics circuits.	PO3,PO4	3

Assessment Procedure: The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode.



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Summary:

Department of ECE has organized add on course on "Overview of MATLAB and its Applications for Engineers" for the year 2019-20, was provided to II BTech students from 11-10-2019 to 30-11-2019. MATLAB is a programming platform designed specifically for engineers and scientists to analyze and design systems and products that transform our world, a matrix-based language allowing the most natural expression of computational mathematics. Able to program scripts and functions using the MATLAB development environment, we offered the course as an add-on to the II year students. The 110 students registered and out of which 105 students did complete their course successfully completing the end course examination and all were issued certificates.



Rohunda
HOD

Head of the Department
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VMTW/ECE/AOC/CIR/2019-20/II/01

Date: 18-01-2020

CIRCULAR

This is to inform all the III B. Tech ECE students that, the Department is organizing an Add on course on “**Contemporary tendencies in VLSI chip Design**”. Department advisory committee has been identified Mr.T. Pullaiah, Associate Professor in ECE Dept. for the course., He will be available 33 days to complete the course, which will take place from 24-01-2020 TO 04-03-2020. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions, hence those who are interested fill the registration form.


HOD

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III.B.Tech students

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ABOUT THE COURSE

VLSI Stands for Very-large-scale integration (VLSI), which creates an integrated circuit (IC) by combining millions of MOS (Metal Oxide Silicon transistor) transistors over a single chip. The microprocessor and memory chips are VLSI (Very-large-scale integration) devices. Before the intro of VLSI technology, most ICs had a limited set of roles they could perform. IC (Integrated Circuits) are used in various types of electronic devices. Our mobile phone consists of IC. IC is a circuit where all or some of the circuit elements are inseparably linked and electrically interconnected, so it's considered indivisible for construction and commerce. Before VLSI, IC size used to be much larger than it is now. Because of VLSI, designers can now add millions of transistors and other components on a tiny chip.

COURSE OBJECTIVES

- Overview of IC Design
- Aspects of IC Design
- Introduction and Device Characteristics
- Analog IC Design
- Digital IC Design
- Semicustom IC Design/PLD
- Introduction to cadence EDA
- Op Amp Design and Compensation
- VLSI Signal Processing
- VLSI Communication System Design
- Mixed Signal Design/RF IC Design
- FDGA/VHDL
- Introduction to DSP VLSI
- Architectural transformation techniques (Retiming, Folding)

- Architectural transformation techniques (Unfolding)
- Numerical Strength reduction techniques

EXPECTED OUTCOMES

At the end of Workshop, participants will be able to

- Model digital systems in VHDL and SystemC at different levels of abstraction.
- Partition a digital system into different subsystems.
- Simulate and verify a design.
- Transfer a design from a version possible to simulate to a version possible to synthesize.
- Use computer-aided design tools to synthesize, map, place, routing, and download the digital designs on the FPGA board.
- Apply knowledge of mathematics, science, and engineering to design and analysis of analog integrated circuits.
- Identify, formulate, and solve engineering problems in the area of analog integrated circuits.
- Use the techniques, skills, and modern programming tools such as Mentor Graphics, necessary for engineering practice.

Resource Person:

Mr. T. Pullaiah,

Associate Professor,
Department of ECE.

Co-ordinator:

Mr. E. Nagaraju,

Assistant Professor,
Department of ECE,

Contact No: +91 83747 17668

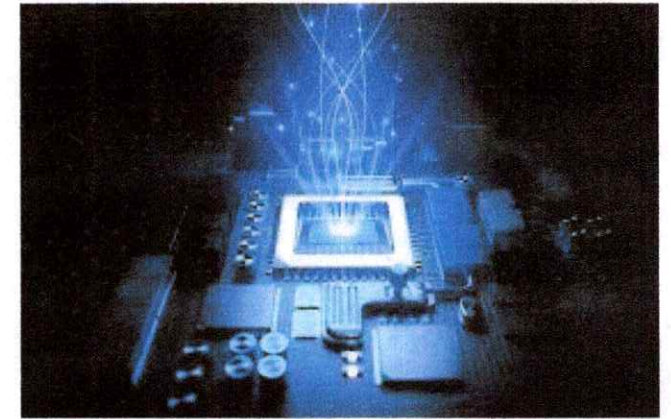


LAST DATE FOR REGISTRATION: 20TH JAN, 2020

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VALUE ADDED COURSE ON "CONTEMPORARY TENDENCIES IN VLSI CHIP DESIGN"

24TH JAN TO 4TH MAR, 2020

Duration of the Course : 33 Hrs

PRINCIPAL
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Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (Dt)-501301
Telangana State

Venue:

B-BLOCK SEMINAR HALL, VMTW

Organized by:

**DEPARTMENT OF ELECTRONICS AND
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Academic Year: 2019-20

Date: 20-01-2020

ADD ON COURSE SYLLABUS: Contemporary tendencies in VLSI chip Design

B.Tech III-II SEM VENUE: B- BLOCK SEMINAR HALL SECTION: ECE-A&B

COURSE OBJECTIVES:

1. To provide extended knowledge of digital logic circuits in the form of state model approach.
2. To provide an overview of system design approach using programmable logic devices.
3. To provide and understand of fault models and test methods.
4. To get exposed to the various architectural features of CPLDS and FPGAS
5. To enable the students to efficiently analyze and design circuits

Module I: Introduction to VLSI

Full-Voltage Scaling, Constant-Voltage Scaling, MOSFET Capacitances, Geometric Scaling Theory, Properties of MOSFETs, Challenges of MOSFET Scaling.

Module II: Challenges of MOSFET Scaling

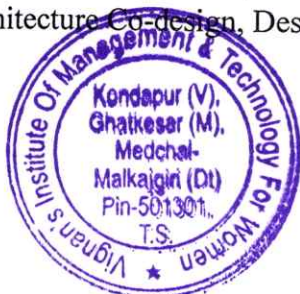
Layout of CMOS Logic Circuits: CMOS fabrication processing steps, Layout of logic circuits, Switching Properties of MOSFETs, Static and dynamic characteristics of Pass Transistors, CMOS Inverter: Basic Circuit and DC Operation.

Module III Static CMOS Logic Elements

CMOS Inverter: Basic Circuit and DC Operation, DC Characteristics, Noise Margins, Layout, considerations, Inverter Switching Characteristics, Transient Effects on the VTC, Static CMOS Logic Elements: CMOS NAND Gate, CMOS NOR Gate, Complex Logic Functions, EDA tools, architecture design and test optimization with system integration issues, Overview of SOC Design Process.

Module IV SOC Architecture Design

Techniques to improve a specific design metric, ASIC Design flow and EDA tools, CMOS Inverter: Basic Circuit and DC Operation, DC Characteristics, , Layout, Platforms and SoC Design, Function Architecture Co-design, Designing Communication Networks.



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Module V Exploration, Software design in SoCs

SOC Design and Test Optimization: Design methodologies for SoC, Noise and signal integrity Analysis, System Integration issues for SoC, SoC Test Scheduling and Integration, SoC Test Resource partition.

Reference Books:

1. Wolf, W., Modern VLSI Design: System-on-chip Design, Prentice Hall (2002) 3rd ed.
2. Nekoogar, F. and Nekoogar, F., From ASICs to SOCs: A Practical Approach, Prentice Hall (2003).
3. Uyemura, J.P., Modern VLSI Design – SOC Design, Prentice Hall (2001).
4. Rajsuman, R., System-on-a-chip: Design and Test, Artech House (2000).


RESOURCE PERSON


COORDINATOR


HOD

Head of the Department
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE NAME: Contemporary tendencies in VLSI chip Design

Academic Year: 2019-20

Date: 18-01-2020

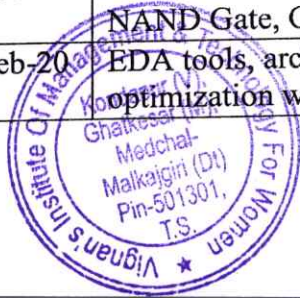
B.Tech III-II SEM

DAY WISE SCHEDULE SHEET

SECTION: ECE-A&B

Room number: B-BLOCK SEMINAR HALL

S.NO	DAY	TOPIC	TIMINGS	DURATION
1.	24-Jan-20	Introduction: Basic MOSFET Characteristics – Threshold Voltage	04:00pm to 05:00pm	1 hour
2.	25-Jan-20	Body Bias concept, Current Voltage Characteristics	04:00pm to 05:00pm	1 hour
3.	27-Jan-20	Square-Law Model, MOSFET Modeling	04:00pm to 05:00pm	1 hour
4.	28-Jan-20	Drain-Source Resistance	04:00pm to 05:00pm	1 hour
5.	29-Jan-20	MOSFET Capacitances, Geometric Scaling Theory	04:00pm to 05:00pm	1 hour
6.	30-Jan-20	Full-Voltage Scaling, Constant-Voltage Scaling	04:00pm to 05:00pm	1 hour
7.	1-Feb-20	Properties of MOSFETs	04:00pm to 05:00pm	1 hour
8.	3-Feb-20	Challenges of MOSFET Scaling	04:00pm to 05:00pm	1 hour
9.	4-Feb-20	Layout of CMOS Logic Circuits: CMOS fabrication processing steps	04:00pm to 05:00pm	1 hour
10.	5-Feb-20	Design Rules, Stickdiagram, Layout of logic circuits, latch-up	04:00pm to 05:00pm	1 hour
11.	6-Feb-20	Switching Properties of MOSFETs	04:00pm to 05:00pm	1 hour
12.	7-Feb-20	Static and dynamic characteristics of Pass Transistors	04:00pm to 05:00pm	1 hour
13.	10-Feb-20	CMOS Inverter: Basic Circuit and DC Operation	04:00pm to 05:00pm	1 hour
14.	11-Feb-20	DC Characteristics, Noise Margins, Layout considerations,	04:00pm to 05:00pm	1 hour
15.	12-Feb-20	Inverter Switching Characteristics, Transient Effects on the VTC	04:00pm to 05:00pm	1 hour
16.	13-Feb-20	Static CMOS Logic Elements: CMOS NAND Gate, CMOS NOR	04:00pm to 05:00pm	1 hour
17.	14-Feb-20	EDA tools, architecture design and test optimization with system integration	04:00pm to 05:00pm	1 hour



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18.	15-Feb-20	Overview of SOC Design Process: Introduction, Top-down SoC design	04:00pm to 05:00pm	1 hour
19.	17-Feb-20	Techniques to improve a specific design metric, ASIC Design flow and EDA	04:00pm to 05:00pm	1 hour
20.	18-Feb-20	Techniques to improve a specific design metric, ASIC Design flow and EDA	04:00pm to 05:00pm	1 hour
21.	19-Feb-20	SOC Architecture Design: Introduction, Front-end chip design	04:00pm to 05:00pm	1 hour
22.	20-Feb-20	Back-end chip design, Integration	04:00pm to 05:00pm	1 hour
23.	21-Feb-20	Platforms and SoC Design, Function Architecture Co-design	04:00pm to 05:00pm	1 hour
24.	22-Feb-20	Designing Communication Networks,	04:00pm to 05:00pm	1 hour
25.	24-Feb-20	System Level Power Estimation and Modeling	04:00pm to 05:00pm	1 hour
26.	25-Feb-20	Transaction Level Modeling	04:00pm to 05:00pm	1 hour
27.	26-Feb-20	Design Space, Exploration	04:00pm to 05:00pm	1 hour
28.	27-Feb-20	Software design in SoCs	04:00pm to 05:00pm	1 hour
29.	28-Feb-20	SOC Design and Test Optimization: Design methodologies for SoC	04:00pm to 05:00pm	1 hour
30.	29-Feb-20	Noise and signal integrity, analysis	04:00pm to 05:00pm	1 hour
31.	2-Mar-20	System Integration issues for SoC	04:00pm to 05:00pm	1 hour
32.	3-Mar-20	System Integration issues for SoC, SoC Test Scheduling and Test Integration	04:00pm to 05:00pm	1 hour
33.	4-Mar-20	Test conducted & Feedback taken	04:00pm to 05:00pm	1 hour
Total 33 hours				


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 07-03-2020

PROGRAM REPORT

Name of the Add on Course: Contemporary tendencies in VLSI chip Design

Day/Duration: 33 Hours

Time: 4pm to 5pm

Resource Person: Mr. T. Pullaiah, Associate professor Dept. of ECE, VMTW, Hyderabad

Name of the Coordinator: Mr. E. Nagaraju, Assistant Professor Dept. of ECE, VMTW, Hyderabad.

Number of the Participants: 95

Topics covered: The following topics covered in this program

1. Challenges of MOSFET Scaling
2. Static and dynamic characteristics of Pass Transistors
3. Static CMOS Logic Elements
4. Layout, Platforms and SoC Design
5. SoC Test Scheduling and Integration

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Explain the unit fabrication process steps	PO2,P04	2
CO2	Explain the functions of packaging	PO3,PO5	3
CO3	Analyze process yield and yield measurement for a process	PO3,PO5	3
CO4	Propose the process flow for novel MOS devices	PO1,PO4	2
CO5	Design memories with efficient architectures to improve access times, power consumption.	PO5,PO3	2

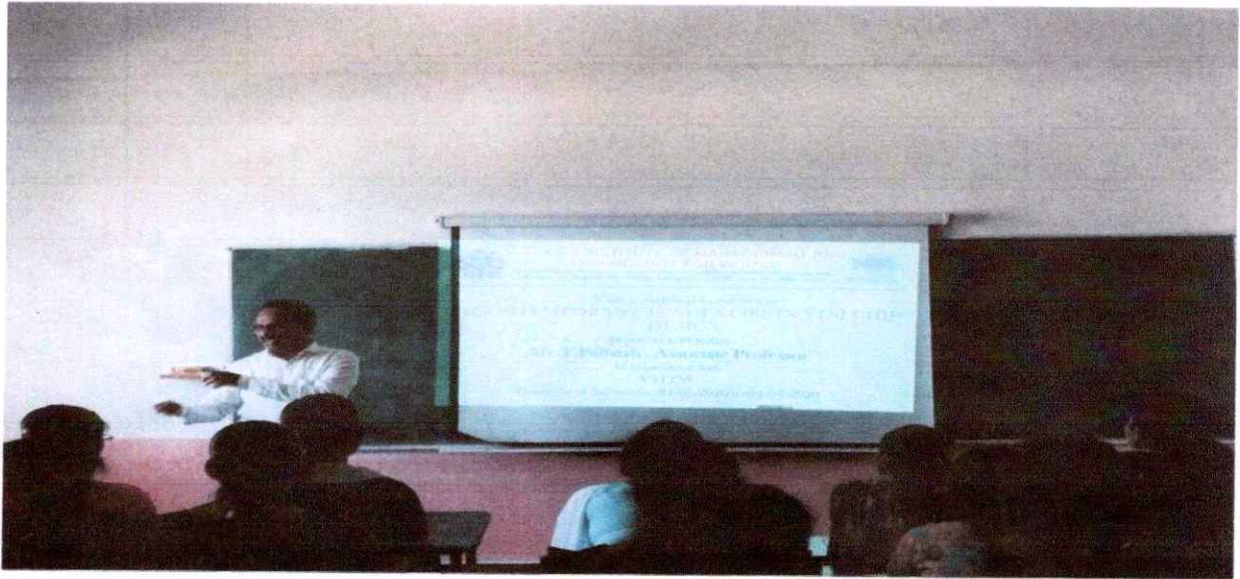
Assessment Procedure: The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode.




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Summary:

Department of ECE has organized add on course on “Contemporary tendencies in VLSI chip Design” for the year 2019-20, was provided to III BTech students from 24-01-2020 to 04-03-2020. VLSI affords IC designers the ability to design utilizing less space. Typically, electronic circuits incorporate a CPU, RAM, ROM, and other peripherals on a single PCBA. However, very large-scale integration (VLSI) technology affords an IC designer the ability to add all of these into one chip. But the syllabus did not cover this language in the academics. Considering this gap, we offered the course as an add-on to the III year students. The program was coordinated by Mr. E. Nagaraju, Assistant Professor, Dept. of ECE and conducted by Mr. T. Pullaiah, Associate professor Dept. of ECE, VMTW. 95 students registered out of which 91 students did complete their course successfully completing the end course examination and all were issued certificates.



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VMTW/ECE/AOC/CIR/2019-20/II/04

Date: 08-12-2019.

CIRCULAR

This is to inform all the III B. Tech. ECE students, NPTEL/Swayam Course is enabled for the January-April 2020 session. The department advisory committee has identified "Programming in Java" as an Add-on course. So, the students are instructed to Enroll in the above course, this helps to enhance the technical and academic skills and will be awarded with a course completion certificate based on the performance index, which is beneficial for future endowers.

For the registration of the course visit https://swayam.gov.in/nc_details/NPTEL

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

VMTW/CSE/AOC/2019-20/I/01

Date: 08-07-2019

CIRCULAR

This is to announce all the IV-I B. Tech CSE students that there'll be an Addon course on "Web development using Bootstrap" by Mrs.D.Swaroopaa, Asst.Professor, Dept of CSE, VMTW from 15-07-2019 to 20-07-2019. All students are invited to require part during this course. The participating students are going to be honored with certificates of completion. So those that have an interest can check in registration form.

V. Andir
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ABOUT THE COURSE:

Bootstrap is the most popular Frontend framework being used online today. Bootstrap helps you create amazing fully responsive websites quickly. Bootstrap is a free and open-source front-end web framework for designing websites and web applications. This course will help you learn to use Bootstrap within your web projects for more rapid development. Explore how you can use CSS classes, within your HTML to bring Bootstrap functionality. Learning about adding Bootstrap typography, forms, buttons, navigation and other interface components, as well as optional JavaScript extensions to your web projects. Bootstrap is all about front-end development only and it delivers. Bootstrap is designed for everyone, everywhere. Once you start using it you will see why it is the most popular HTML, CSS, and JS framework for developing responsive, mobile-first projects on the web. Bootstrap makes front-end web development faster and easier. Bootstrap is ready for all skill levels and ready for anyone. Your websites and projects of all sizes will work across multiple screen sizes perfectly with Bootstrap. Web Development course is designed with industry-standard topics, practical software development training with prescribed tools & latest technologies.

COURSE OBJECTIVES:

This course teaches the fundamentals of using a component CSS framework like Bootstrap to create websites and applications more efficiently. You'll learn about the benefits of reusability in design, how to work with grids for responsive design, and how to integrate third-party components. By the end of the course, you'll be able to build mobile-first websites, design responsive layouts, customize Bootstrap, optimize designs for different devices, and manipulate media objects and buttons on webpages.

EXPECTED OUTCOMES:

Bootstrap training, students will be featured with skills in the concepts to build and experiment websites. Get

knowledge of bootstrap built-in components design grids, fluid grids, and responsive layout. Understand global Bootstrap CSS classes for images, typography, tables, grids, forms, buttons, and more. Understand the reusable bootstrap components including icons, dropdowns, alerts navbars, breadcrumbs, popovers, and many more. Utilize the bootstrap javascript Plugins to develop modern web pages. Customize Bootstrap's elements with fewer variables and jQuery plugins to build our version. This course will help students in building stunning responsive and mobile sites using Bootstrap 4, CSS3, HTML5, jQuery, and JavaScript. Bootstrap course is instructor-led, but extremely practical in orientation. By the end of the course, students should have a small, but richly featured site, exploiting many of the most popular features of Bootstrap. This course allows you to learn current tools and latest version to build modern mobile friendly and responsive websites. After the completion of bootstrap training you can become a successful web designer.



Resource Person:
Mrs. Swaroopa Duli,
Assistant Professor,
Department of CSE.



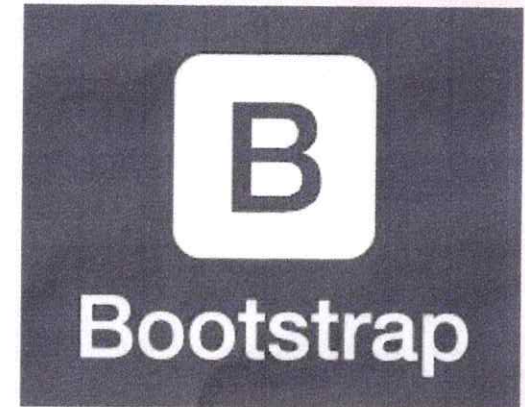
Co-ordinator:

Mrs. Deepthisri Dakuri,
Assistant Professor,
Department of CSE
Contact No: +91 8074777560

LAST DATE FOR REGISTRATION: 12TH JULY, 2019

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ADD-ON COURSE ON
**"WEB DEVELOPMENT
USING BOOT STRAP"**
15TH TO 20TH JULY, 2019

Duration of the Course : 36 Hrs

Venue:
'A'-Block Seminar Hall,
VMTW.


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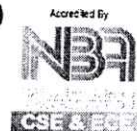
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Addon Course Syllabus: Web development using Bootstrap

Course objectives:

1. To introduce the fundamentals of Internet, and the principles of web design.
2. To construct basic websites using HTML and Cascading Style Sheets.
3. To build dynamic web pages with validation using Java Script objects and by applying different event handling mechanisms.
4. To develop modern interactive web applications using PHP, XML and MySQL.
5. To use the most popular Bootstrap components

Module 1: Introduction, Bootstrap File Structure, Default Grid System, Fluid Grid System, Responsive Design.

Module 2: Typography, Tables, Forms, Button, Images, Icons.

Module 3: Dropdown Menus, Button Groups, Button with Dropdowns, Navigation Elements, Navbar, Breadcrumbs, Pagination, Labels, Badges, Typographic Elements, Thumbnails, Alerts, Progress Bars, Media Object

Module 4: Model, Dropdown, Scrollspy, Toggleable Tabs, Tooltips, Popover, Alerts, Buttons, Collapse, Carousel, Typeahead.

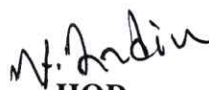
Module 5: Customizing Bootstrap, Built with Bootstrap.

Text Books

1. Responsive Web Development Illustrated by Jake Spurlock (Author).
2. Web development for Beginners in HTML + Web design with CSS + Javascript basics for Beginners by Andy Vickler (Author).


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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DAY WISE SCHEDULE SHEET

Addon Course Name: Web development using Bootstrap

Room No: A Block Seminar Hall-ASF-5

DAY	Topic	Timings	Duration
15-07-2019	Introduction, Bootstrap File Structure, Default Grid System, Fluid Grid System, Responsive Design.	09:00 am to 03:30 pm	6 hrs
16-07-2019	Typography, Tables, Forms, Button, Images, Icons.	09:00 am to 03:30 pm	6 hrs
17-07-2019	Dropdown Menus, Button Groups, Button with Dropdowns, Navigation Elements, Navbar, Breadcrumbs, Pagination, Labels, Badges, Typographic Elements, Thumbnails, Alerts, Progress Bars, Media Object.	09:00 am to 03:30 pm	6 hrs
18-07-2019	Model, Dropdown, Scrollspy, Toggleable Tabs,	09:00 am to 03:30 pm	6 hrs
19-07-2019	Customizing Bootstrap, Built with Bootstrap.	09:00 am to 03:30 pm	6 hrs
20-07-2019	Tooltips, Popover, Alerts, Buttons, Collapse, Carousel, Typeahead, Lab Practice.	09:00 am to 03:30 pm	6 hrs
Total 36 hours			


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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Date: 22-07-2019

Addon course Report

Name of the Add on Course: Web development using Bootstrap

Day/Duration: 15-07-2019 to 20-07-2019 6(Days) - 36Hrs

Time: 09:00am to 03:30pm

Name of the Coordinator: Mrs,Deepthi sri Assistant Professor, Dept of CSE, VMTW.

Number of the Participants: 85

Topics covered: The following topics covered in this program

- Introduction, Bootstrap File Structure, Responsive Design.
- Typography, Tables, Forms, Button, Images, Icons.
- Dropdown Menus, Button Groups, Button with Dropdowns, Navigation Elements, Navbar, Breadcrumbs, Pagination, Labels, Badges,
- Model, Dropdown, Scrollspy, Toggleable Tabs, Tooltips, Popover, Alerts, Buttons, Collapse, Carousel, Typeahead.
- Customizing Bootstrap, Built with Bootstrap.

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Effectively manage website projects using available resources.	PO1,PO9	3
CO2	Apply employability skills including fundamental skills, personal management skills, and teamwork skills.	PO1,PO5	4
CO3	Demonstrate communication skills, service management skills, and presentation skills.	PO2,PO3	3
CO4	Integrate in existing web projects for fast development	PO5	4
CO5	Know the benefits of reusability in design and development	PO1	4




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Resource Person: Mrs.D.Swaroopa, Assistant Professor, Dept of CSE, VMTW.



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v. Indira
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

VMTW/CSE/AOC/2019-20/II/02

Date: 03-01-2020

CIRCULAR

This is to inform all the III-II B. Tech CSE students that there will be an Add on course on "Advanced Database programming" from 6/1/2020 to 11/1/2020 by Mr.K.Bharath Reddy, Assistant Professor, Dept of CSE, VMTW. All students are invited to take part in this course. The participating students will be honored with certificates of participation. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions. So those who are interested can sign in registration form.

V. Jindin
HOD

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- III-II B.Tech Students
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ABOUT THE COURSE:

This course teaches SQL for implementing and maintaining databases, covering data design, manipulation, and programming business logic. It explores modern database systems, their internal functioning, data structures, and algorithms for efficient processing. Topics include database architecture, data models, indexing, transaction processing, recovery, parallel architectures, cloud databases, and ML in databases. Valuable for data science industry and research in data management. Includes hands-on programming with database systems beyond SQL queries.

COURSE OBJECTIVES:

This course focuses on fundamental concepts of database analysis and design, emphasizing the importance of these aspects in implementing database applications. It covers drawing ER-Diagrams, transaction processing, concurrency control, and introduces new data querying and modeling techniques. The course explores advanced database systems like PostGIS, MongoDB, and SciDB, comparing them to relational databases. Students will design and implement real-world applications using these systems to showcase their capabilities. The course also delves into core theories related to operating systems, concurrency, and transactions. Understanding database internals is valuable for both users and administrators of DBMS.

EXPECTED OUTCOMES:

Understand the basic principles of database management systems. Draw Entity-Relationship diagrams to represent simple database application scenarios to write SQL queries for a given context in relational database. Discuss normalization techniques with simple examples. Describe transaction processing

and concurrency control concepts. On completion of the course, the student will be able to describe how database management systems function internally. Interpret and comparatively criticize database systems architectures. Implement major components of a database management system and analyze their performance. Analyze and compare the fundamental query evaluation and concurrency control algorithms. Identify strengths and weaknesses of query evaluation plans. Optimize query evaluation plans. Create and Manage Constraints, Views, Indexes, and Other Specialized Objects. Specify primary and foreign keys and use indexes. Create Database Instance. Create and Manage Queries and update data. Develop stored procedures and triggers. Use advanced techniques for program control and data manipulation. Develop applications with embedded SQL and ODBC.



Resource Person:
Mr. K. Bharath Reddy,
Assistant Professor,
Department of CSE.

Co-ordinator:
Mr. V. Nitesh,
Assistant Professor,
Department of CSE.

Contact No: +91 9949242938

LAST DATE FOR REGISTRATION: 4TH JAN, 2020

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ADD-ON COURSE ON
**“ADVANCED DATABASES
PROGRAMMING”**
6TH TO 11TH JAN, 2020

Duration of the Course : 36 Hrs

Venue:

**'A'-Block Seminar Hall,
VMTW.**

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Addon Course Syllabus: Advanced Database programming

Course objectives:

1. To develop conceptual understanding of database management system
2. To understand how a real world problem can be mapped to schemas
3. To solve different industry level problems & to learn its applications
4. To Create and Mange Queries and update data.
5. To Develop stored procedures and triggers.

Module 1: Formal review of relational database and FDs Implication, Closure, its correctness.

Module 2: 3NF and BCNF, Decomposition and synthesis approaches, Review of SQL99, Basics of query processing, external sorting, file scans.

Module 3: Processing of joins, materialized vs. pipelined processing, query transformation rules, DB transactions, ACID properties, interleaved executions, schedules, serialisability.

Module 4: Correctness of interleaved execution, Locking and management of locks, 2PL, deadlocks, multiple level granularity, CC on B+ trees, Optimistic CC.

Module 5: T/O based techniques, Multiversion approaches, Comparison of CC methods, dynamic databases, Failure classification, recovery algorithm, XML and relational databases.

Text Book

1. Advanced Database Management System by Rini Chakrabarti (Author), Shilbhadra Dasgupta (Author), Subhash K. Shinde (Author).


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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Add On Course Name: Advanced Data Base Programming

DAY WISE SCHEDULE SHEET

Room No: A Block Seminar Hall-ASF-5

DAY	Topic	Timings	Duration
06-01-2020	Formal review of relational database and FDs Implication, Closure, its correctness.	09:00 am to 03:30 pm	6 hrs
07-01-2020	3NF and BCNF, Decomposition and synthesis approaches, Review of SQL99, Basics of query processing, external sorting, file scans.	09:00 am to 03:30 pm	6 hrs
08-01-2020	Processing of joins, materialized vs. pipelined processing, query transformation rules, DB transactions, ACID properties, interleaved executions, schedules, serialisability.	09:00 am to 03:30 pm	6 hrs
09-01-2020	Correctness of interleaved execution, Locking and management of locks, 2PL, deadlocks, multiple level granularity, CC on B+ trees, Optimistic CC.	09:00 am to 03:30 pm	6 hrs
10-01-2020	T/O based techniques, Multiversion approaches, Comparison of CC methods	09:00 am to 03:30 pm	6 hrs
11-01-2020	Dynamic databases, Failure classification, recovery algorithm, XML and relational databases.	09:00 am to 03:30 pm	6 hrs
Total 36 hours			


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Head of The Department
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Date: 20-01-2020

Addon course Report

Name of the Add on Course: Advanced Data Base Programming

Day/Duration: 06-01-2020 to 11-01-2020 6(Days) - 36Hrs

Time: 09:00am to 03:30pm

Name of the Coordinator: Mr.V.Nitesh, Assistant Professor, Dept of CSE, VMTW.

Number of the Participants: 89

Topics covered: The following topics covered in this program

- Formal review of relational database and FDs Implication, Closure, its correctness.
- 3NF and BCNF, Decomposition and synthesis approaches.
- Processing of joins, materialized vs. pipelined processing, query transformation rules, DB transactions, ACID properties, Correctness of interleaved execution, Locking and management of locks, 2PL, deadlocks, multiple level granularity, CC on B+ trees.
- Multiversion approaches, Comparison of CC methods, dynamic databases, Failure classification, recovery algorithm, XML and relational databases.

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Ability to define a problem at the view level & ability to understand the physical structure of the database to handle data.	PO1,PO9	3
CO2	Students would be able to implement the logic by using tools like ERD	PO1,PO5	4
CO3	Ability to normalize the database & understand the internal data structure	PO2,PO3	3
CO4	Understand the transaction system & extract data efficiently.	PO5	4
CO5	Develop applications with embedded SQL and ODBC.	PO1	4




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The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode.

Resource Person: Mr.K.Bharath Reddy, Assistant Professor, Dept of CSE, VMTW.



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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

VMTW/CSE/AOC/2019-20/II/03

06-01-2020

CIRCULAR

This is to inform all the II-II B. Tech CSE students that there will be an Add on course on "NodeJS for server side scripting" from 20-01-2020 to 25-01-2020. All students are invited to take part in this course. The participating students will be honored with certificates of participation. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions.

V. Andlu
HOD

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ABOUT THE COURSE:

NodeJS is an open source, cross-platform framework running on Mac OSX, Linux and also Windows. It is a JavaScript, Server-side environment that runs in JavaScript utilize a Google. And it also runs on various platforms such as Windows, Linux, Unix, Mac OS X, etc. NodeJS uses JavaScript on the server NodeJS is an open-source, cross-platform JavaScript run-time atmosphere which implements JavaScript code external of a browser. In fact, JavaScript is primarily used for client-side scripting, in which scripts printed in JavaScript are rooted in a web page HTML and run client-side by a JavaScript engine in the user's web browser. This technology will make a big evolution. This course is focusing on huge essential concepts of NodeJS, and it provides the hands-on experience in building HTTP server with NodeJS working file system, multi-processing, events and streams in NodeJS. This training helps to enhance the applications using ExpressJS. In this immersive learning Node.js program, you will understand various Node.js concepts with hands-on experience in building everything from command-line tools to web servers and more.

COURSE OBJECTIVES:

The objective of this course is to teach students how to build web applications using the Express.js framework, with focus on industry-practices like functional programming, object-oriented design, programming style guides, security, and version control. Understand how asynchronous event-based development works with this Node.js course and write code real-time, write command-line scripts that perform tasks such as reading files from a hard disk and copying files from one directory to another and learn to set up a MongoDB database and learn the basics of working with document-based databases in Node. Amp up your skills with Express, understand rendering, middleware, routing, authentication and more, and gear up to protect Express apps against malicious attacks. Learn why server-side JavaScript is useful, Installing Node.js. Learn how Node.js is architected to allow high scalability with asynchronous code. Create basic web applications with Node.js. Automate tasks with Gulp. Build an HTTP server using the core modules in Node.js. Use

stream I/O to efficiently serve the web pages. Create modules to organize the server to test the reliability of the application with unit tests. Convert the application to an MVC framework using Express.

EXPECTED OUTCOMES:

Demonstrate fundamental first-principles-based knowledge about web development and the practical chops to use them to build real-world software. They would also have learnt what it is to work in a professional software environment, helping build a strong foundation for their transition to the industry as competent professionals. Understand Node JS and REPL terminal. Experiment with Node JS Modules and Node Package Manager. Develop applications to handle events in Node JS Make use of Web Server to manage the database. Demonstrate Express Framework. Build web applications using Express.js. Manipulate data using both imperative and functional programming techniques. The Model real-world systems using object-oriented design Write HTML & CSS to create elegant web pages to Build database applications using Sequelize. Develop skills for real career growth by working on real-world problems. The fundamentals of working with persistent data include how to set up a model, and save, delete, and find documents in the database.



Resource Person:
Mr. B. Phijik,
Assistant Professor,
VITAE, Hyderabad.

Co-ordinator:
Mr. S. Dilli Babu,
Assistant Professor,
Department of CSE.

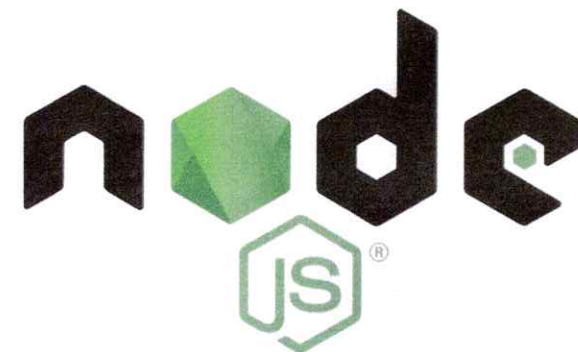
Contact No: +91 8801 179 588

LAST DATE FOR REGISTRATION: 19TH JAN, 2020

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ADD-ON COURSE ON
**"NODE JS FOR
SERVER-SIDE SCRIPTING**
20TH TO 25TH JAN, 2020

Duration of the Course : 36 Hrs

Venue:
**'A'-Block Seminar Hall,
VMTW.**



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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Addon course Syllabus

Addon Course: NodeJS for server side scripting

Course objectives:

1. To Create basic web applications with Node.js.
2. To Test the reliability of the application with unit tests.
3. To Build an HTTP server using the core modules in Node.js.
4. To Create modules to organize the server
5. To Learn how Node.js is architected to allow high scalability

Module 1: Introduction to web & internet, Introduction to server, Understanding local host Server, Starting Node.js, Node.js syntax and variables, Operators and Expressions, Conditional Branching and Looping Statements, Learning Arrays in Node.js.

Module 2: String and Math functions in Node.js, Introduction HTML Form Elements and Fields, Accessing Node.js, HTTP Data, Query Strings and Hyperlinks.

Module 3: Introduction to Functional Programming, Events and handlers Events And Process On Firing Events, Constructors, Exception Handling, Regular Expressions, Session Handling Using Cookies.

Module 4: Introduction to JSON, Understanding the power of json Building a Sample Web Application.

Module 5: Introduction to Express Frameworks, Working with Express Framework, Routing Routes, Views and Public Assets, Helpers and Modules FormValidation, FetchingData using Session.

P. U. Babu
COORDINATOR

v. Indira
HOD

Head of The Department
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DAY WISE SCHEDULE SHEET

Addon Course Name: Node Js for Server Side Scripting

Room No: A Block Seminar Hall

DAY	Topic	Timings	Duration
20-01-2020	Introduction to web & internet, Introduction to server, Understanding local host Server, Starting Node.js, Node.js syntax and variables, Operators and Expressions, Conditional Branching and Looping Statements, Learning Arrays in Node.js.	09:00 am to 03:30 pm	6 hrs
21-01-2020	String and Math functions in Node.js, Introduction HTML Form Elements and Fields, Accessing Node.js, HTTP Data, Query Strings and Hyperlinks.	09:00 am to 03:30 pm	6hrs
22-01-2020	Introduction to Functional Programming, Events and handlers Events And Process On Firing Events, Constructors, Exception Handling, Regular Expressions, Session Handling Using Cookies.	09:00 am to 03:30 pm	6hrs
23-01-2020	Introduction to JSON, Understanding the power of JSON Building a Sample Web Application.	09:00 am to 03:30 pm	6hrs
24-01-2020	Introduction to Express Frameworks, Working with Express Framework, Routing Routes,	09:00 am to 03:30 pm	6hrs
25-01-2020	Views and Public Assets, Helpers and Modules form Validation, Fetching Data using Session.	09:00 am to 03:30 pm	6hrs
Total 36 hours			

Pilli Babu
COORDINATOR

N. Indira
HOD

Head of The Department
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Date: 27-01-2020

Addon Course Report

Name of the Add on Course: Node Js for Server Side Scripting

Day/Duration: 20-01-2020 to 25-01-2020 6(Days) - 36Hrs

Time: 09:00am to 03:30pm

Resource Person: Mr. B.Phijik, Assistant Professor, Dept of CSE.VITAE Hyd.

Name of the Coordinator: Mr.S.Dilli Babu, Assistant Professor, Dept of CSE, VMTW.

Number of the Participants: 85

Topics covered: The following topics covered in this program

- Introduction to server, Understanding local host Server, Starting Node.js, Node.js syntax and variables, Operators and Expressions.
- String and Math functions in Node.js, Introduction HTML Form Elements and Fields, Accessing Node.js, HTTP Data, Query Strings and Hyperlinks.
- Events and handlers Events And Process On Firing Events, Constructors, Exception Handling.
- Understanding the power of JSON Building a Sample Web Application.
- Working with Express Framework, Routing Routes, Views and Public Assets, Helpers and Modules form Validation, Fetching Data using Session.

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Experiment with Node JS Modules and Node Package Manager.	PO4	3
CO2	Develop applications to handle events in Node JS	PO4,PO5	2
CO3	Make use of Web Server to manage database.	PO1	4
CO4	Convert the application to an MVC framework using Express	PO4	3




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CO5	Interface to a PostgreSQL and MongoDB database and a web service	PO2, PO3	3
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Mr. B. Phijik taking sessions on addon course Node Js for Server Side Scripting

Dilli Babu
COORDINATOR

V. Indira
HOD

Head of The Department
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

VMTW/CSE/AOC/2019-20/II/04

11-02-2020

CIRCULAR

It is advised to all the IV-II B. Tech CSE students that there will be an Add on course on "object-oriented and functional programming-Scala" from 17/02/2020 to 21/02/2020. All students can utilize the opportunity to enhance the skills and Certificates of completion will be presented to the students. You can register the course with coordinator.

V. Andri
HOD

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ABOUT THE COURSE:

The Scala programming language has been gaining popularity in recent years not only in the software development community but also in enterprise development as well. Its benefits include the use of the most advanced features of contemporary programming languages and extensive support of functional programming. Scala offers programmers an opportunity to improve development efficiency and create a very expressive and efficient code. Scala code is compiled to Java byte code and runs inside the JVM, which provides excellent compatibility with existing Java libraries and tools. The course covers all the content that is necessary to effectively use Scala: operators and data types, using basic data structures such as arrays, maps, and tuples, the use of object-oriented and functional approaches, working with collections, pattern matching, case classes, generics and implicit conversions. It also includes a large number of practical exercises. Knowledge of the contemporary OOP programming language is required. The training explores new design patterns and language idioms, showing how to make the most of features ranging from generics to enums, annotations to autoboxing. It explains how to achieve typical programming tasks in Scala, such as modeling business domains, breaking down complex problems into simpler problems, manipulating data, or running parallel tasks. Throughout the course, you will also learn the best practices for writing high-quality Scala code that scales to large applications, how to handle errors, how to write tests, and how to leverage a productive development environment.

COURSE OBJECTIVES:

Scala has emerged as a prominent programming language for various applications. This course teaches the principles of functional programming, including designing immutable data structures and integrating functional programming with objects and classes. You will learn to write purely functional programs using recursion, pattern matching, and higher-order functions. Scala combines functional and object-oriented programming and seamlessly interoperates with Java and JavaScript. It is used in frameworks like Apache Spark, Kafka, and Akka, powering platforms such as Twitter, Netflix, and Zalando. Through hands-on exercises, you will gain practical experience in applying functional programming techniques to tasks like domain modeling and business logic implementation. The course also emphasizes developing a solid foundation for reasoning about

functional programs, including proving invariants and symbolically tracing execution.

EXPECTED OUTCOMES:

This course teaches you how to use Scala to solve common programming challenges such as modeling business domains, simplifying complex problems, manipulating data, and running parallel programs. This informative, hands-on course combines functional programming with object-oriented programming to help you improve your programming skills. Additionally, you will learn about the standard library and common code patterns. In this you will use Scala idioms to model business domains and implement business logic. Develop a solid understanding of the language, allowing you to read and understand codebases written in Scala. Acquire best practices and standard patterns used in the real world. Work with asynchronous computations, handle failures, and manipulate recursive data structures comfortably. Throughout the course, you will learn the best practices for writing high-quality Scala code that scales to large applications, how to handle errors, how to write tests, and how to leverage a productive development environment. This comprehensive, hands-on, course aims at leveling up your programming skills by embracing both functional programming and object-oriented programming.



Resource Person:
Dr. G Soma Sekhar,
Associate Professor, CSE.
Geethanjali College of
Engineering & Technology,
Hyderabad.

Co-ordinator:

Mr. T. Srajan Kumar,
Assistant Professor,
Department of CSE.

Contact No: +91 9032150341

LAST DATE FOR REGISTRATION: 15TH FEB, 2020

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ADD-ON COURSE ON

OBJECT-ORIENTED AND FUNCTIONAL PROGRAMMING-SCALA

17TH TO 21ST FEB, 2020

Duration of the Course : 32 Hrs

Venue:

A'-Block Seminar Hall,
VMTW.

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Addon course Syllabus

Addon Course: Object-oriented and functional programming – Scala

Course objectives:

1. To understand the basic concepts and fundamentals of platform independent object oriented language.
2. To demonstrate skills in writing programs using exception handling techniques and multithreading.
3. To understand streams and efficient user interface design techniques.
4. To Design immutable data structures
5. To Combine functional programming with objects and classes

Module 1: Scala Keywords, Scala Identifiers, Data Types in Scala, Variables in Scala
Scala Console ,println, printf and readLine, Control Statements, Scala | Decision Making
Scala | Loops(while, do..while, for, nested loops), For Loop in Scala.

Module 2: Class and Object in Scala, Inner class in Scala, Inheritance in Scala, Operators in
Scala, Operators Precedence in Scala, Abstract Classes in Scala, Singleton and Companion
Objects, Generic Classes in Scala, Access Modifiers in Scala, Scala Constructors.

Module 3: Scala Type Hierarchy, Scala | Lower Bound , Scala Upper bound, Scala Variance
Scala Null, null, Nil, Nothing, None, and Unit.

Module 4: Exceptions, Scala Exception Handling, Throw Keyword in Scala, Scala Try-Catch
Exceptions, Scala Finally Exceptions, Scala Either.

Module 5: Scala Lists, Scala ListBuffer, ListSet in Scala, Scala Arrays, Multidimensional
Arrays in Scala, Scala Create Array with Range. Scala ArrayBuffer, Scala Tuple.

Text Book

1. Programming in Scala, 3rd Edition by Martin Odersky (Author), Lex Spoon (Author), Bill
Venners (Author).

T. Srajan Kumar
COORDINATOR



V. Indira
HOD

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DAY WISE SCHEDULE

ADD ON COURSE NAME: Object Oriented And Functional Programming-Scala

Room: A Block Seminar Hall

DAY	Topic	Timings	Duration
17-02-2020	Scala Keywords, Scala Identifiers, Data Types in Scala, Variables in Scala, Scala Console ,println, printf and readLine, Control Statements, Scala Decision Making Scala /Loops(while, do..while, for, nested loops), For Loop in Scala, while and do while Loop in Scala, Break statement in Scala, Scala Literals.	09:00 am to 03:30 pm	6 hrs
18-02-2020	Class and Object in Scala, Inner class in Scala, Inheritance in Scala, Operators in Scala, Operators Precedence in Scala, Abstract Classes in Scala, Singleton and Companion Objects, Generic Classes in Scala, Access Modifiers in Scala, Scala Constructors.	09:00 am to 03:30 pm	6 hrs
19-02-2020	Scala Type Hierarchy, Scala Lower Bound , Scala Upper bound, Scala Variances, Scala Null, null, Nil, Nothing, None, and Unit.	09:00 am to 03:30 pm	6 hrs
20-02-2020	Exceptions, Scala Exception Handling, Throw Keyword in Scala, Scala Try-Catch Exceptions, Scala Finally Exceptions, Scala Either.	09:00 am to 03:30 pm	7 hrs
21-02-2020	Scala Lists, Scala ListBuffer, ListSet in Scala, Scala Arrays, Multidimensional Arrays in Scala, Scala Create Array with Range, Scala ArrayBuffer, Scala Tuple.	09:00 am to 03:30 pm	7 hrs
Total 32 hours			

T. Brajan Kumar
COORDINATOR

N. Jindin
HOD

Head of The Department
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Date: 24-02-2020

Report on addon course

Name of the Add on Course: Object Oriented And Functional Programming-Scala

Day/Duration: 17-02-2020 to 21-02-2020 5(Days) - 32Hrs

Time: 09:30am to 03:30pm

Resource Person: Dr. G Soma Sekhar, Associate Professor in CSE Department for Geethanjali College of Engineering and Technology Hyderabad.

Name of the Coordinator: Mr.T.Srajan Kumar, Assistant Professor, Dept of CSE, VMTW.

Number of the Participants: 82

Topics covered: The following topics covered in this program

- Scala Keywords, Scala Identifiers, Data Types in Scala, Variables in Scala, Scala Console .println, printf and readLine, Control Statements, Scala | Decision Making Scala.
- Class and Object in Scala, Inner class in Scala, Inheritance in Scala, Operators in Scala, Operators Precedence in Scala, Abstract Classes in Scala.
- Scala Type Hierarchy, Scala | Lower Bound , Scala Upper bound, Scala Variances, Scala Null, null, Nil, Nothing, None, and Unit.
- Exceptions, Scala Exception Handling, Throw Keyword in Scala, Scala Try-Catch Exceptions.
- Scala Lists, Scala ListBuffer, ListSet in Scala, Scala Arrays, Multidimensional Arrays in Scala.

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Able to write programs in a functional style	PO1,PO2	4
CO2	Analyze functional programs	PO4,PO5	3




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Telangana State



VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

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CO3	Apply polymorphism and higher-order functions	PO1	5
CO4	Formulate the time and space complexity of programs	PO2	2
CO5	Design functional libraries and their APIs.	PO2, PO3	4



Addon course training by Dr. G Soma Sekhar

T. Srajan Kumar
COORDINATOR

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