VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

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CSE & ECE

INSTITUTIONAL PROGRAM BROCHURE, CIRCULAR, SYLLABUS, REPORT-2018-19

INDEX

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DEPARTMENT OF BASIC SCIENCE AND HUMANITIES

VMTW/BSH/AOC/CIR/2018-19/II/01

02-01-2019.

CIRCULAR

This is to inform all the I B. Tech II SEM ECE students that, the Department organizing a Value added course on **"Public Speaking and Communications"**. Department advisory committee has been identified Mrs.V.Suzan Shalini, Associate Professor in BS&H Dept. for the course. She has an efficient and fine knowledge about that add on course, hence she will available 35 hours to complete the course, which will take place from 07-01-2019 to 11-01-2019. The participating students will be honored with certificates of participation. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions. Hence those who are interested can sign in registration form.

HOD

Head of the Department
Basic Sciences and Humanitie
Vignan's Institute of Management and Technology for Women
Kondapur (V), Ghatkesar (M) R R Dist-501 301

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I.B.Tech students

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Telangana State

About the Course:

A course in public speaking and communication studies will provide the students with practical skills that are useful for a wide range of career choices. We do not narrowly train students for one specific job, instead we help students develop creative and critical ways to organize, build relationships, and solve a variety of communication problems. Business leaders and other professionals recognize the importance of developing communication skills and many times a course in communication studies will give you an edge over other job applicants in fields such as business, government, social and public services, sales, marketing, and the arts.

Course Objectives:

- Describe the elements of public speaking and their relationship to critical thinking.
- Describe the importance of listening to the speech process.
- Describe the relationship of the audience to the speaker and analyze an audience for any speech situation.
- Identify speeches for special occasions.
- Deliver an informative and persuasive speech, briefing, or other public presentation, using traditional and/or electronic visual aids.

- Describe the nature and formats for small group communication.

Expected Outcomes:

At the end of the course, students will have the ability to:

- Say things simply but effectively.
- Importance of Intonations.
- Win the battle over stage-fright.
- Understand that content is as important as communication – help you to collect your thoughts before you speak; speak from the head and the heart.
- Understand more Intricate Skills – use of the pause, a dramatic beginning, how to win an audience over.

Resource Person:

Mrs. V. Suzan Shalini,
Assistant Professor,
Dept. of BS&H.

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Telangana State

Co-ordinator:

Mr. B. Vinod Kumar ,
Assistant Professor,
Department of ECE.
Contact No: +91 7095707096

Last Date For Registration: 5th Jan, 2019

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ADD-ON COURSE ON

"PUBLIC SPEAKING AND COMMUNICATIONS"

7TH TO 11TH JAN, 2019

Duration of the Course: 35 Hrs

Venue:
Room No. BFF-2, VMTW.

Organized By:

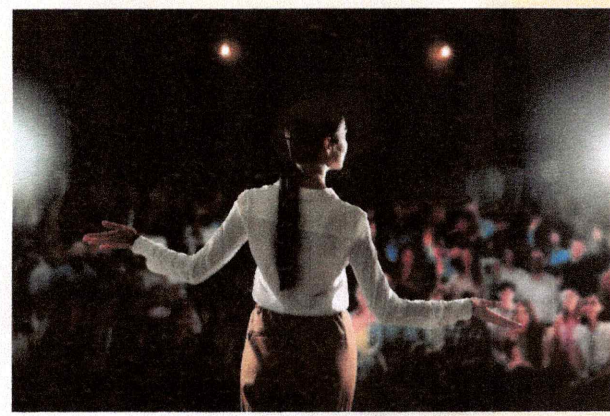## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE SYLLABUS: Public Speaking and Communications

Academic Year: 2018-19

B.Tech I-II SEM   **SYLLABUS**   **SECTION: ECE-A&B**

COURSE OBJECTIVES

- Describe the elements of public speaking and their relationship to critical thinking.
- Describe the importance of listening to the speech process.
- Describe the relationship of the audience to the speaker and analyze an audience for any speech situation.
- Identify speeches for special occasions.
- Deliver an informative and persuasive speech, briefing, or other public presentation, using traditional and/or electronic visual aids.
- Describe the nature and formats for small group communication.

Module I: Introduction to Public Speaking Benefits

Introduction to Public Speaking Benefits, History, and Theories of Communication-he Public Speaking Situation; Ethics; and Speaker; confronting your fears

Module II: Speeches

Self-Introduction Speech-A 30-second speech in which the student will have to highlight some aspects of himself/herself. The speech should be concise and polished and should include a specific brief story or example that shows how this aspect applies to you. The time limit will be strictly enforced.-. Persuasive Speech, A 5-6 minute speech designed to make the audience act, think, or feel in a very specific way.

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Module III Oral Interpretation

Helping students in pronunciation, vocal emphasis and inflections. Informative Speech A 4-5 minute speech in which the student will present information in a pleasing and interesting way that speaks directly to the needs of the audience. The subject matter can center on an object, process, event, or concept.

Module IV Spontaneous Speaking

Preparing and evaluating impromptu speeches, thought organization, delivery of impromptu speeches. Ceremonial Speech-A 1:30-2 minute ceremonial speech of the students choosing: presentation, acceptance, farewell, welcome, dedication, introduction, or nomination. The goal is to identify key values inherent in the topic and to help the audience celebrate these values.

Module V Debating Skills

Improving debating skills, understanding the skills of taking notes on unfamiliar information and teaching students how to flow a debate. Topics are centered on questions of fact, value, or policy. You will be asked to argue your particular side of the issue as convincingly as possible.

Text Book

1. Shalilesh Patel, "Public Speaking Presentation & Communication Skills", Kindle Edition (2020).
2. Dale Carnegie, "The Art of Public Speaking", Kindle Edition (2016).

Coordinator

Resource Person

HOD

Head of the Department
Electronics and Communication Engine
Vignan's Institute of Management & Technology For
Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (Dt.)-

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE NAME: Public Speaking and Communications

Academic Year: 2018-19   **Date: 04-01-2019**

B.Tech I-II SEM   **DAY WISE SCHEDULE SHEET**   **SECTION: ECE-A&B**

Room number: BFF-2

S.no	DAY	Topic	Timings	Duration
1.	07-01-2019	Introduction to Public Speaking Benefits, History, and Theories of Communication, Debating Skills.	09:00am to 03:30pm	7 hour
2.	08-01-2019	The Public Speaking Situation; Ethics; and Speaker; Confronting your fears, Presentation skills and techniques.	09:00am to 03:30pm	7 hour
3.	09-01-2019	Self-Introduction Speech, Informative Speech, Persuasive Speech, Ceremonial Speech, Horning Critical Listening Skills.	09:00am to 03:30pm	7 hour
4.	10-01-2019	Oral Interpretation, Spontaneous Speaking, the Audience, Introduction to Small Group Communication/Group Discussion.	09:00am to 03:30pm	7 hour
5.	11-01-2019	Grooming techniques, Body Language and Positive Attitude, Interview Sessions, Test conducted & Feedback Taken.	09:00am to 03:30pm	7 hour
		Total 35 hours		

Coordinator

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HOD

Head of the Department
Electronics and Communication Engineer
Vignan's Institute of Management & Technology For W
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 18-01-2019.

PROGRAM REPORT

Name of the Add on Course: Public Speaking and Communications

Day/Duration: 35 HOURS

Time: 9:00AM TO 3:30PM

Resource Person: Mrs.V.Suzan shalini Assistant Professor, VMTW, Hyderabad.

Name of the Coordinator: Mrs.D.Kavitah, Assistant Professor, VMTW

Number of the Participants: 58

Topics covered: The following topics covered in this program

- Self-Introduction Speech
- Informative Speech
- Persuasive Speech
- Ceremonial Speech
- Oral Interpretation

Course Outcomes: Work with real time projects using Architectural transformation techniques

COs	AT THE END OF THE COURSE, YOU WILL BE ABLE TO	POs Mapped	Strength of mapping
CO1	Say things simply but effectively	PO1,PO3	3
CO2	Importance of Intonations	PO1,PO3	2
CO3	Win the battle over stage-fright	PO1,PO2	2
CO4	Understand that content is as important as communication – help you to collect your thoughts before you speak; speak from the head and the heart.	PO1,PO2	3
CO5	Understand more Intricate Skills – use of the pause, a dramatic beginning, how to win an audience over	PO1,PO2	2
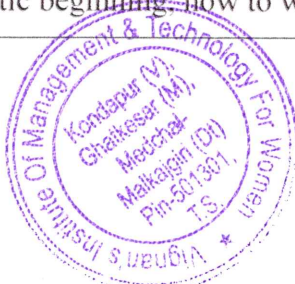
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Students attended for the course: Public Speaking and Communications

Department of ECE has organized add on course on "Public Speaking And Communication" for the year 2018-19, was provided to I BTech students from 07-01-2019 to 11-01-2019 studies will provide the students with practical skills that are useful for a wide range of career choices. We do not narrowly train students for one specific job, instead we help students develop creative and critical ways to organize, build relationships, and solve a variety of communication problems. Business leaders and other professionals recognize the importance of developing communication skills and many times a course in communication studies will give an edge over other job, applicants in fields such as business, government, social and public services, sales, marketing, and the arts. Eight participants from EEE have participated in the program. We are thankful for conducting such type of events and students would like to have such a events further. we offered the course as an add-on to the I year students. The program was coordinated by B.Vinod Kumar, Assistant Professor Dept. of ECE and conducted Mr. V. Shanlini, Assistant Professor, BSH, VMTW. The classes in their scheduled time between 9am to 3:30Pm in weekdays. 58 students registered and out of which 58 students did complete their course successfully completing the end course examination and all were issued certificates.

Assessment Procedure: The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode.

Coordinator

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HOD

Head of the Department
Electronics and Communication Engineering
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DEPARTMENT OF BASIC SCIENCE AND HUMANITIES

VMTW/BSH/AOC/2018-19/II/02   Date: 08-01-2019

CIRCULAR

This is to inform all I-II B.Tech students that addon course on "Statistics with R" will be organizing by Department of CSE from 21-01-2019 to 28-01-2019 to improve the programming skill. All students are welcome to participate in this Addon course. Interested students can sign in the registration form and communicate to the program coordinator Mr.S. Dilli Babu, Dept of CSE, VMTW accordingly.

HOD

Head of the Department
Basic Sciences and Humanitie
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I-II B.Tech Students

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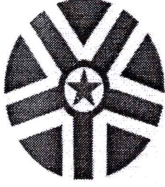
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About the Course:

Module-1: Introduction, How to run R, R Sessions and Functions, Basic Math, Variables, Data Types, Vectors, Conclusion, Advanced Data Structures, Data Frames, Lists, Matrices, Arrays, Classes.

Module-2: R Programming Structures, Control Statements, Loops, - Looping Over Nonvector Sets,- If-Else, Arithmetic and Boolean Operators and values, Default Values for Argument, Return Values, Deciding Whether to explicitly call return- Returning Complex Objects.

Module-3: Doing Math and Simulation in R, Math Function, Extended Example Calculating Probability- Cumulative Sums and Products-Minima and Maxima- Calculus.

Module-4: Graphics, Creating Graphs, The Workhorse of R Base Graphics, the plot() Function - Customizing Graphs, Saving Graphs to Files.

Module-5: Probability Distributions, Normal Distribution- Binomial Distribution- Poisson Distributions Other Distribution, Basic Statistics, Correlation and Covariance.

Course Objectives:

1. To Design the basics of statistical computing and data analysis

2. How to use R for analytical programming and to implement data structure in R

3. To Apply the R loop functions and debugging tools

4. To Develop simple Data visualization in R

To Understand How to perform error handling and Writing custom R functions

Expected Outcomes:

At the end of the course, students will have the ability to:

- Import, review, manipulate and summarize data-sets in R.

- Explore data-sets to create testable hypotheses and identify appropriate statistical tests.

- Perform appropriate statistical tests using R.

- Create and edit visualizations with R.

- Demonstrate how to install and configure RStudio.

Resource Person:
Mr. B. Phijik,
Assistant Professor,
Dept. of CSE,
VITAE, Hyderabad.

Co-ordinator:
Mr. S. Dilli Babu,
Assistant Professor,
Department of CSE.
Contact No: +91 8801179588

Last Date For Registration: 19th Jan, 2019

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```
stocks <- na.omit(stocks)
    (stocks$c'           '1) {
    status
  } else
    sta'
  } el
    s          lat"
  }
  st

  cc        ocks$close
  st        ifelse(con
  cor       cks$close
  sta       else(con
  head
  tablet

  x <- sample
  y <- sample(-1.
  all(x > 0)

  any(x == y)
```

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ADD-ON COURSE ON
"STATISTICS WITH R"

21ST TO 28TH JAN, 2019

Duration of the Course: 36 Hrs

Venue:
'A'-Block Seminar Hall, VMTW.

Organized By:
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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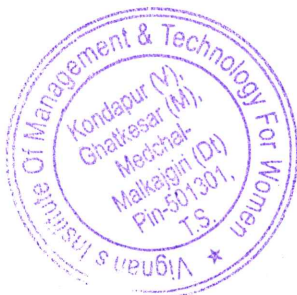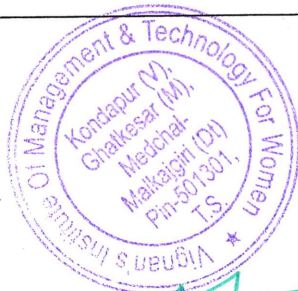
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

ADDON COURSE SYLLABUS

Addon course: Statistics with R

Course objectives:.

1. To Design the basics of statistical computing and data analysis
2. How to use R for analytical programming and to implement data structure in R
3. To Apply the R loop functions and debugging tools
4. To Develop simple Data visualization in R
5. To Understand How to perform error handling and Writing custom R functions

Module-1: Introduction, How to run R, R Sessions and Functions, Basic Math, Variables, Data Types, Vectors, Conclusion, Advanced Data Structures, Data Frames, Lists, Matrices, Arrays, Classes.

Module-2: R Programming Structures, Control Statements, Loops, - Looping Over Nonvector Sets,- If-Else, Arithmetic and Boolean Operators and values, Default Values for Argument, Return Values, Deciding Whether to explicitly call return- Returning Complex Objects.

Module-3: Doing Math and Simulation in R, Math Function, Extended Example Calculating Probability- Cumulative Sums and Products-Minima and Maxima- Calculus.

Module-4: Graphics, Creating Graphs, The Workhorse of R Base Graphics, the plot() Function – Customizing Graphs, Saving Graphs to Files.

Module-5: Probability Distributions, Normal Distribution- Binomial Distribution- Poisson Distributions Other Distribution, Basic Statistics, Correlation and Covariance.

Text Books

1. The Art of R Programming, Norman Matloff, Cengage Learning
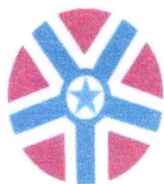
2. R for Everyone, Lander, Pearson

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DilliBabu
COORDINATOR

HOD

Head of the Department
Computer Science and Engineering
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VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DAY WISE SCHEDULE SHEET

Addon Course: Statistics with R

Room No: A Block Seminar Hall-ASF-5

DAY	Topic	Timings	Duration
21-01-2019	Introduction, How to run R, R Sessions and Functions, Basic Math, Variables, Data Types, Vectors, Conclusion, Advanced Data Structures, Data Frames.	09:00 am to 03:30 pm	6 hrs
22-01-2019	Lists, Matrices, Arrays, Classes, Programming Structures, Control Statements, Loops, - Looping Over Nonvector Sets,- If-Else, Arithmetic and Boolean Operators and values.	09:00 am to 03:30 pm	6 hrs
23-01-2019	Default Values for Argument, Return Values, Deciding Whether to explicitly call return- Returning Complex Objects,	09:00 am to 03:30 pm	6 hrs
24-01-2019	Doing Math and Simulation in R, Math Function, Extended Example Calculating Probability- Cumulative Sums and Products-Minima and Maxima- Calculus.	09:00 am to 03:30 pm	6 hrs
25-01-2019	Graphics, Creating Graphs, The Workhorse of R Base Graphics, the plot() Function – Customizing Graphs, Saving Graphs to Files.	09:00 am to 03:30 pm	6 hrs
28-01-2019	Probability Distributions, Normal Distribution- Binomial Distribution- Poisson Distributions Other Distribution, Basic Statistics, Correlation and Covariance.	09:00 am to 03:30 pm	6 hrs
Total 36 hours			

Alli Babu
COORDINATOR

N. Indira
HOD, CSE

Head of the Department
Computer Science and Engineering
Vignan Institute of Management and Technology for W...
Kondapur (V), Ghatkesar (M), R.R. Dist-50...0

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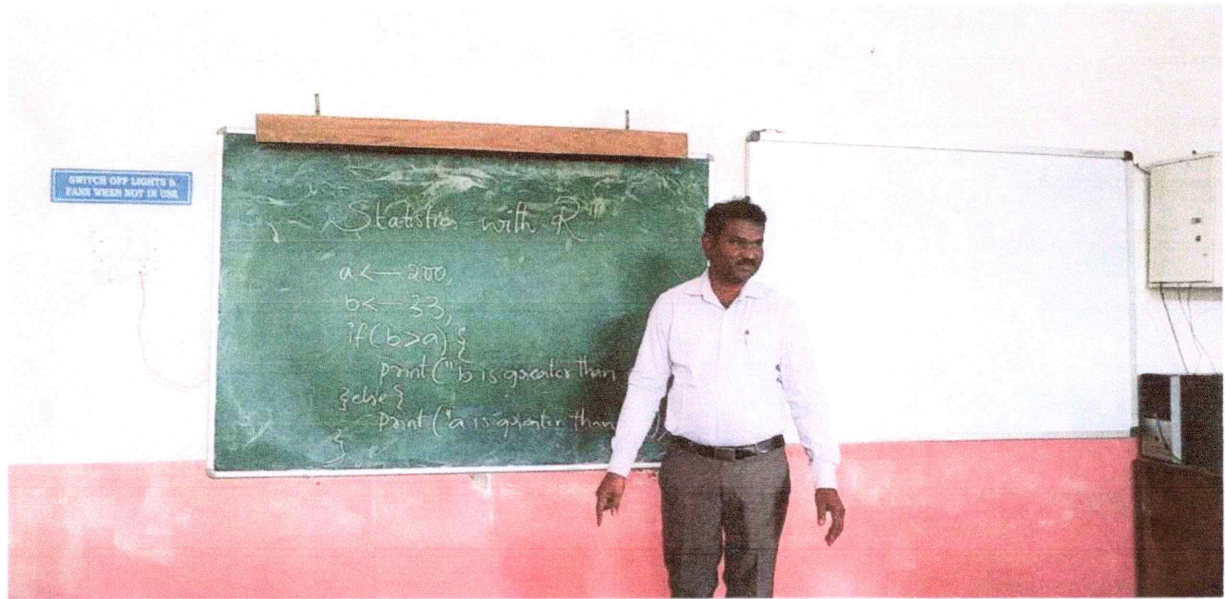# VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Date: 30-01-2019

Addon course Report

Name of the Add on Course: Statistics with R.

Day/Duration: 21-01-2019 to 28-01-2019 6 (Days) – 36 Hrs

Time: 09:00am to 03:30pm

Resource Person: Mr. B.Phijik, Assistant Professor, Dept of CSE.VITAE Hyd.,

Name of the Coordinator: Mr.S. Dilli Babu, Dept of CSE, VMTW.

Number of the Participants: 53

Topics covered: The following topics covered in this program:

- ❖ R Sessions and Functions, Basic Math, Variables, Data Types, Vectors, Conclusion, Advanced Data Structures.
- ❖ Programming Structures, Control Statements, Loops, - Looping Over Nonvector Sets,- If-Else.
- ❖ Simulation in R, Math Function, Extended Example Calculating Probability.
- ❖ Creating Graphs, The Workhorse of R Base Graphics, the plot() Function.
- ❖ Normal Distribution- Binomial Distribution- Poisson Distributions Other Distribution, Basic Statistics.

Course Outcomes

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Import, review, manipulate and summarize data-sets in R.	PO1	3
CO2	Explore data-sets to create testable hypotheses and identify appropriate statistical tests.	PO2, PO3	4
CO3	Perform appropriate statistical tests using R.	PO4	4
CO4	Create and edit visualizations with R.	PO2	3
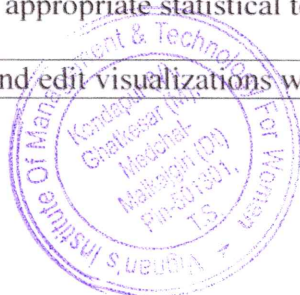
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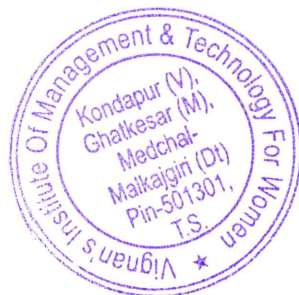| CO5	Demonstrate how to install and configure RStudio.	PO1	4

Addon course sessions by Mr. B.Phijik, Assistant Professor.

D.Illi Babu

COORDINATOR

v. Indira

HOD

Head of the Department
Computer Science and Engineering
Vignan's Institute of Management and Technology for Women
Kondapur (V), Ghatkesar (M), R.R. Dist-501 301.

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DEPARTMENT OF ELECRICAL AND ELECTRONICS ENGINEERING

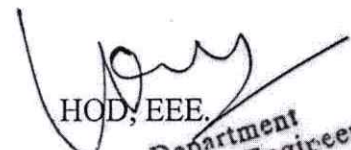VMTW/EEE/AOC/2018-19/I/01

Date: 27-08-2018.

CIRCULAR

This is to inform all the IV B. Tech EEE students that there will be an Add on course on **"ARTIFICIAL INTELLIGENCE"**. Department advisory committee has identified Mr. K. BHARATH REDDY, Assistant Professor in CSE Department as recourse person for the course. He has an efficient and fine knowledge about that add on course, hence he will avail 32 hours to complete the course, which will take place from 10-09-2018 to 29-10-2018. Hence the interested students are requested to register the course by having their confirmation signature in registration form.

HOD, EEE.
Head of the Department
Electrical and Electronics Engineering
Vignan's Institute of Management and Technology for Women
Kondapur (V), Ghatkesar (M), R.R. Dist-501 301

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ABOUT THE DEPARTMENT

Department of Electrical Engineering has been producing superlative graduates, since inception in 2008. The state of the art infrastructure and lab facilities are established with standards, which ensure good exposure and hands on experience for students during learning process, as we all know now a days Electrical Power plays a major role in development of nation, with huge demands of power, the requirement of Electrical Engineers in the Government and private sectors is also a must, considering the above reasons we the department of EEE are making best efforts to produce highly trained and capable engineers who can take up the challenges of the real world. The quality of any educational institution can be accessed based upon three important factors i.e. faculty, labs and placements.

ABOUT THE COURSE

Artificial Intelligence with Machine Learning and the internet of things are among the most trending skills related to. Artificial intelligence is of 4 types: namely purely reactive, limited memory, theory of mind, and self aware. Artificial Intelligence courses provide the knowledge and skills required to kickstart a career in the field of AI.

COURSE OBJECTIVES

- To impart knowledge about Artificial Intelligence.
- To give understanding of the main abstractions and reasoning intelligent systems.
- To enable the students to understand the basic principles of Artificial Intelligence in various applications

EXPECTED OUTCOMES

At the end of Workshop, participants will be able to

- Solve basic AI based problems
- Define the concept of Artificial Intelligence.
- Apply AI techniques to real-world problems to develop intelligent systems.

Resource Person:
Mr. K. Bharath Reddy,
Assistant Professor,
Department of CSE.

Co-ordinator:
Mr. S. Upendher Rao,
Assistant Professor,
Department of EEE,
Contact No: +91 99855 85663

LAST DATE FOR REGISTRATION: **30ᵀᴴ AUG, 2018**

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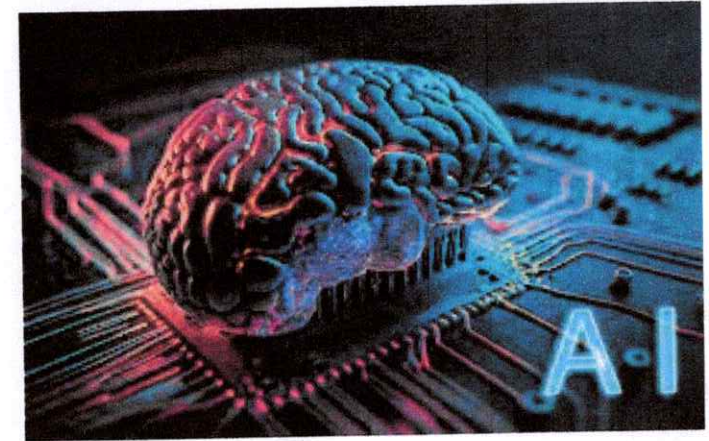
ADD-ON COURSE ON
"ARTIFICIAL INTELLIGENCE"
10ᵀᴴ SEP TO 29ᵀᴴ OCT, 2018

DURATION OF THE COURSE: **32 HRS**

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Venue:
'A'-BLOCK SEMINAR HALL, VMTW

Organized by:
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

ADD ON COURSE: ARTIFICIAL INTELLIGENCE

Academic year: 2018 – 19

B. Tech. IV-I SEM   **SYLLABUS**

COURSE OBJECTIVES:

1.   To learn the distinction between optimal reasoning Vs. human like reasoning.

2.   To understand the concepts of state space representation, exhaustive search, heuristic search together with the time and space complexities.

3.   To learn different knowledge representation techniques.

4.   To understand the applications of AI, namely game playing, theorem proving, and machine learning.

COURSE OUTCOMES:

1.   Learn the distinction between optimal reasoning Vs human like reasoning and formulate an efficient problem space for a problem expressed in natural language.

2.   Apply AI techniques to solve problems of game playing, theorem proving, and machine learning.

3.   Learn different knowledge representation techniques.

4.   Understand the concepts of state space representation, exhaustive search, heuristic search together with the time and space complexities.

5.   Comprehend the applications of Probabilistic Reasoning and Bayesian Networks.

6.   Analyze Supervised Learning Vs. Learning Decision Trees

UNIT – I - Introduction to AI - Intelligent Agents, Problem-Solving Agents, Searching for Solutions - Breadth-first search, Depth-first search, Hill-climbing search, Simulated annealing search, Local Search in Continuous Spaces.

UNIT – II - Games - Optimal Decisions in Games, Alpha–Beta Pruning, Defining Constraint Satisfaction Problems, Constraint Propagation, Backtracking Search for CSPs, Knowledge-Based Agents, Logic- Propositional Logic, Propositional Theorem Proving: Inference and proofs, Proof by resolution, Horn clauses and definite clauses.

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UNIT – III - First-Order Logic - Syntax and Semantics of First-Order Logic, Using First Order Logic, Knowledge Engineering in First-Order Logic. Inference in First-Order Logic: Propositional vs. First-Order Inference, Unification, Forward Chaining, Backward Chaining, Resolution. Knowledge Representation: Ontological Engineering, Categories and Objects, Events.

UNIT – IV -Planning - Definition of Classical Planning, Algorithms for Planning with State Space Search, Planning Graphs, other Classical Planning Approaches, Analysis of Planning approaches. Hierarchical Planning.

UNIT – V - Probabilistic Reasoning: Acting under Uncertainty, Basic Probability Notation Bayes' Rule and Its Use, Probabilistic Reasoning, Representing Knowledge in an Uncertain Domain, The Semantics of Bayesian Networks, Efficient Representation of Conditional Distributions, Approximate Inference in Bayesian Networks, Relational and First- Order Probability.

TEXT BOOK:

1. Artificial Intelligence: A Modern Approach, Third Edition, Stuart Russell and Peter Norvig, Pearson Education.

REFERENCE BOOKS:

1. Artificial Intelligence, 3rd Edn., E. Rich and K. Knight (TMH)

2. Artificial Intelligence, 3rd Edn., Patrick Henny Winston, Pearson Education.

3. Artificial Intelligence, Shivani Goel, Pearson Education. 4. Artificial Intelligence and Expert systems – Patterson, Pearson Education.

COORDINATOR

HOD

Head of the Department
Electrical and ~~Processors~~ Engineering
Vignan's Institute of Management and Technology for Women
Kondapur ~~Ghatkesar~~ (M), R.R. Dist-501 301

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DEPARTMENT OF ELECRICAL AND ELECTRONICS ENGINEERING

ADD ON COURSE NAME: ARTIFICIAL INTELLIGENCE

Academic Year: 2018-19   **Date: 28-08-2018**

B.Tech IV-I SEM   DAY WISE SCHEDULE   **Branch: EEE**

Venue: A – BLOCK Seminar Hall

S.No	DAY	Topic	Timings	Durati
1.	10-09-18	Artificial intelligence	04:00pm to 5:00pm	1 hour
2.	11-09-18	Present trends	04:00pm to 05:00pm	1 hour
3.	12-09-18	Future scope	04:00pm to 05:00pm	1 hour
4.	14-09-18	Artificial intelligence algorithms	04:00pm to 05:00pm	1 hour
5.	15-09-18	BPA	04:00pm to 05:00pm	1 hour
6.	17-09-18	Formalized symbolic logic	04:00pm to 05:00pm	1 hour
7.	18-09-18	Propositional logic-first order	04:00pm to 05:00pm	1 hour
8.	19-09-18	wff conversiontoclausalform	04:00pm to 05:00pm	1 hour
9.	20-09-18	Inferencerules	04:00pm to 05:00pm	1 hour
10.	22-09-18	Theresolutionprinciple	04:00pm to 05:00pm	1 hour
11.	24-09-18	Dealingwithinconsistenciesandun	04:00pm to 05:00pm	1 hour
12.	25-09-18	Fuzzylogic	04:00pm to 05:00pm	1 hour
13.	26-09-18	graphs, frames and related	04:00pm to 05:00pm	1 hour
14.	27-09-18	Graphs, frames and related	04:00pm to 05:00pm	1 hour
15.	28-09-18	Knowledge organization	04:00pm to 05:00pm	1 hour
16.	29-09-18	Manipulation.	04:00pm to 05:00pm	1 hour
17.	01-10-18	MatchingTechniques	04:00pm to 05:00pm	1 hour
18.	03-10-18	Knowledgeorganizations	04:00pm to 05:00pm	1 hour
19.	04-10-18	Management.	04:00pm to 05:00pm	1 hour
20.	05-10-18	NaturalLanguageprocessing	04:00pm to 05:00pm	1 hour
21.	06-10-18	Patternrecognition	04:00pm to 05:00pm	1 hour
22.	08-10-18	Patternrecognition	04:00pm to 05:00pm	1 hour
23.	10-10-18	Expertsystems	04:00pm to 05:00pm	1 hour
24.	11-10-18	Practice lab	04:00pm to 05:00pm	1 hour
25.	12-10-18	Practice lab	04:00pm to 05:00pm	1 hour
26.	22-10-18	Practice lab	04:00pm to 05:00pm	1 hour
27.	23-10-18	Patternrecognition	04:00pm to 05:00pm	1 hour
28.	24-10-18	Patternrecognition	04:00pm to 05:00pm	1 hour
29.	25-10-18	Expertsystems	04:00pm to 05:00pm	1 hour
30.	26-10-18	IndexedOrganization	04:00pm to 05:00pm	1 hour
31.	27-10-18	Lab Practice	04:00pm to 05:00pm	1 hour
32.	29-10-18	Feedback and Assessment test	04:00pm to 05:00pm	1 hour
		Total 32 hours		

Coordinator

HOD

Head of the Department
Electrical and Electronics Engineering

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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Date: 31-10-2018.

PROGRAM REPORT

Name of the Add on Course: ARTIFICIAL INTELLIGENCE

Day/Duration: 32 hours

Time: 04:00 PM to 05: 00 PM

Resource Person: Mr. K. Bharath Reddy, Assistant Professor, Dept. of CSE , VMTW.

Name of the Coordinator: Mr. S. Upendher Rao, Assistant Professor, VMTW.

Number of the Participants: 15

Topics covered: The following topics covered in this program

- Machine Learning
- IoT
- AI applications
- Intelligent systems

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Apply difficult real life problems in a state space representation so as to solve them using AI techniques	PO1,PO5	3
CO2	Formulate valid solutions for problems involving uncertain inputs or outcomes by using decision making techniques.	PO2	1
CO3	Demonstrate and enrich knowledge to select and apply AI tools to synthesize information and develop models within constraints of application area	PO3	2

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CO4	Understand the informed and uninformed problem types and apply search strategies to solve them	PO4	2

Assessment Procedure: The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode.

Coordinator

EEE, HOD

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VMTW/ECE/AOC/CIR/2018-19/I/01

18-07-2018.

CIRCULAR

This is to inform all the II B. Tech ECE students that there will be an Add on course on "**MATLAB for beginners**" in association with Elegant Embedded Solutions PVT LTD., Hyderabad, from **20-07-2018 to 30-08-2018.** All students are invited to take part in this course. The participating students will be honored with certificates of participation. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions. Hence those who are interested can sign in registration form.

HOD.

Head of the Department
Electronics and Communication Engineering
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About the Course:

The course provides a gentle introduction to the MATLAB computing environment and is intended for beginning users and those looking for a review. It is designed to give students a basic understanding of MATLAB, including popular toolboxes. The course consists of interactive lectures and sample MATLAB problems that generally they face. The hands-on session helped students with the syntaxes and functions for use in programming. The tool helps at various stages in studying subjects related to processing and analysis during regular subjects throughout courses. No prior programming experience or knowledge of MATLAB is assumed. Concepts covered include basic use, graphical representations and tips for designing and implementing MATLAB code.

Course Objectives:

- Understanding the MATLAB environment.
- Being able to do simple calculations using MATLAB.
- Being able to carry out simple numerical computations, analyses and graphics using MATLAB.

Expected Outcomes:

At the end of Workshop, participants will be able to

- Understand the main features of the MATLAB development environment.
- Use the MATLAB GUI effectively.
- Basic syntax, Looping, 2D & 3D Plotting.
- Design simple algorithms to solve problems.
- Write simple programs in MATLAB to solve scientific and mathematical problems.

  a) Solution of simultaneous linear equation.

  b) Solution of simultaneous non-linear equation.

  c) Regression, splines and polynomial interpolation.

  d) Solution of ordinary differential equation.

  e) Numerical Integration.

  f) Basics optimization procedure.

Resource Person:
Mr. K. Jagadeeshwar Reddy,
Technical Manager,
Elegant embedded solutions Pvt. Ltd,
Hyderabad.

Co-ordinator:
Mr. E. Nagaraju
Assistant Professor,
Department of ECE,
Contact No: +91 83747 17668

Last Date For Registration: 16th July, 2018

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ADD-ON COURSE ON
"MATLAB FOR BEGINNERS"
20TH JUL TO 30TH AUG, 2018

In collaboration with:

ELEGANT EMBEDDED SOLUTIONS PVT. LTD.

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Telangana State

Duration of the Course: 37 Hrs

Venue:
'B'-Block Seminar Hall, VMTW.

Organized By:
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE SYLLABUS: MATLAB for beginners

Academic Year: 2018-19

B.Tech II-I SEM   **SECTION**: ECE-A&B

COURSE OBJECTIVES

- To Understand the MATLAB environment.

- Design and simulate various electric circuits in MATLAB simulation tool.

- To understand the behavior of signal in time and frequency domain.

- To understand the characteristics of LTI systems using coding.

Module I: Introduction to MATLAB

The MATLAB Environment • MATLAB Basics – Variables, Numbers, Operators, Expressions, Input and output. • Vectors, Arrays – Matrices Environment setup- Syntax- M-files- Data- Operators

Module II: MATLAB Functions

Built-in Functions • User defined Functions- Decisions- Loops- Vectors- Matrix- Arrays- Colon notation

Module III Graphics with MATLAB

Files and File Management – Import/Export • Basic 2D, 3D plots • Graphic handling- Numbers- Strings

Module IV Programming with MATLAB

Conditional Statements, Loops • MATLAB Programs – Programming and Debugging. • Applications of MATLAB Programming- Data import- Data output- Plotting- Graphics

Module V Mathematical Computing with MATLAB

Algebraic equations • Basic Symbolic Calculus and Differential equations • Numerical Techniques and Transforms- GNU Octave- Simulink.

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Text Book

1. "A Guide to MATLAB - for Beginners and Experienced Users", 2nd Ed., Brian R. Hunt, Ronald L. Lipsman, Jonathan M. Rosenberg, Cambridge University Press, (2006).
2. "Essentials of MATLAB Programming", 2nd Ed., Stephen J. Chapman, Cengage Learning, (2009).

Coordinator

Resource Person

HOD

Head of the Department
Electronics and Communication Engineering
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE NAME: MATLAB for beginners

Academic Year: 2018-19   **Date: 18-08-2018**

B.Tech II-I SEM   **DAY WISE SCHEDULE SHEET**   **SECTION**: ECE-A&B

Room number: BFF6

S.no	DAY	Topic	Timings	Duration
1.	20-07-18	Environment setup	04:00pm to 05:00pm	1 hour
2.	21-07-18	Syntax	04:00pm to 05:00pm	1 hour
3.	23-07-18	Variables	04:00pm to 05:00pm	1 hour
4.	24-07-18	Commands	04:00pm to 05:00pm	1 hour
5.	25-07-18	Practice on Mat lab	04:00pm to 05:00pm	1 hour
6.	26-07-18	M-files	04:00pm to 05:00pm	1 hour
7.	27-07-18	Data	04:00pm to 05:00pm	1 hour
8.	28-07-18	Operators	04:00pm to 05:00pm	1 hour
9.	30-07-18	Decisions	04:00pm to 05:00pm	1 hour
10.	31-07-18	Practice on Mat lab	04:00pm to 05:00pm	1 hour
11.	01-08-18	Practice on Mat lab	04:00pm to 05:00pm	1 hour
12.	02-08-18	Loops	04:00pm to 05:00pm	1 hour
13.	03-08-18	Vectors	04:00pm to 05:00pm	1 hour
14.	04-08-18	Matrix	04:00pm to 05:00pm	1 hour
15.	06-08-18	Arrays	04:00pm to 05:00pm	1 hour
16.	07-08-18	Colon notation	04:00pm to 05:00pm	1 hour
17.	08-08-18	Numbers	04:00pm to 05:00pm	1 hour
18.	09-08-18	Strings	04:00pm to 05:00pm	1 hour
19.	10-08-18	Functions	04:00pm to 05:00pm	1 hour
20.	11-08-18	Data import	04:00pm to 05:00pm	1 hour
21.	13-08-18	Data output	04:00pm to 05:00pm	1 hour
22.	14-08-18	Plotting	04:00pm to 05:00pm	1 hour
23.	15-08-18	Graphics	04:00pm to 05:00pm	1 hour
24.	16-08-18	Algebra	04:00pm to 05:00pm	1 hour
25.	17-08-18	Calculus	04:00pm to 05:00pm	1 hour
26.	18-08-18	Differential	04:00pm to 05:00pm	1 hour
27.	20-08-18	Integration	04:00pm to 05:00pm	1 hour
28.	21-08-18	Polynomials	04:00pm to 05:00pm	1 hour
29.	22-08-18	Transforms	04:00pm to 05:00pm	1 hour
30.	23-08-18	Practice on Mat lab	04:00pm to 05:00pm	1 hour
31.	24-08-18	Practice on Mat lab	04:00pm to 05:00pm	1 hour
32.	25-08-18	GNU Octave	04:00pm to 05:00pm	1 hour

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33.	26-08-18	Simulink	04:00pm to 05:00pm	1 hour
34.	27-08-18	Practice on Mat lab	04:00pm to 05:00pm	1 hour
35.	28-08-18	Practice on Mat lab	04:00pm to 05:00pm	1 hour
36.	29-08-18	Practice on Mat lab	04:00pm to 05:00pm	1 hour
37.	30-08-18	Test conducted and	04:00pm to 05:00pm	1 hour
		Total 37 hours		

Coordinator

HOD, ECE

Head of the Department
Electronics and Communication Engineering
Vignan's Institute of Management and Technology for Women
Kondapur (V), Ghatkesar (M), R.R. Dist-501 301

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 01-09-2018.

PROGRAM REPORT

Name of the Add on Course: MATLAB for beginners

Day/Duration: 37 Hours

Time: 4:00pm to 5:00pm

Resource Person: K. Jagadeeswar Reddy, Technical Manager, Elegant Embedded Solutions,.Hyd.

Name of the Coordinator: Mr. E. Nagaraju, Assistant Professor, VMTW. Hyderabad.

Number of the Participants: 95

Topics covered: The following topics covered in this program

- Basic syntax.
- Looping.
- 2D & 3D Plotting.
- Design simple algorithms to solve problems.
- Basic Symbolic Calculus and Differential equations

Course Outcomes:

COs	At the end of the course, students will have the ability to:	PO s Mapped	Strength of mapping
CO1	Understand the main features of the MATLAB development environment	PO3,PO5	2
CO2	Understand the fundamentals of sampling including the implications of sampling theorem.	PO1,PO4	3
CO3	Analyze the signals with different transform technique	PO5,PO3	4
CO4	Apply the concepts of convolution and correlation for continuous time signals	PO1,PO4	4
CO5	Understand the fundamentals of Numerical Techniques and Transforms.	PO1,PO3	4

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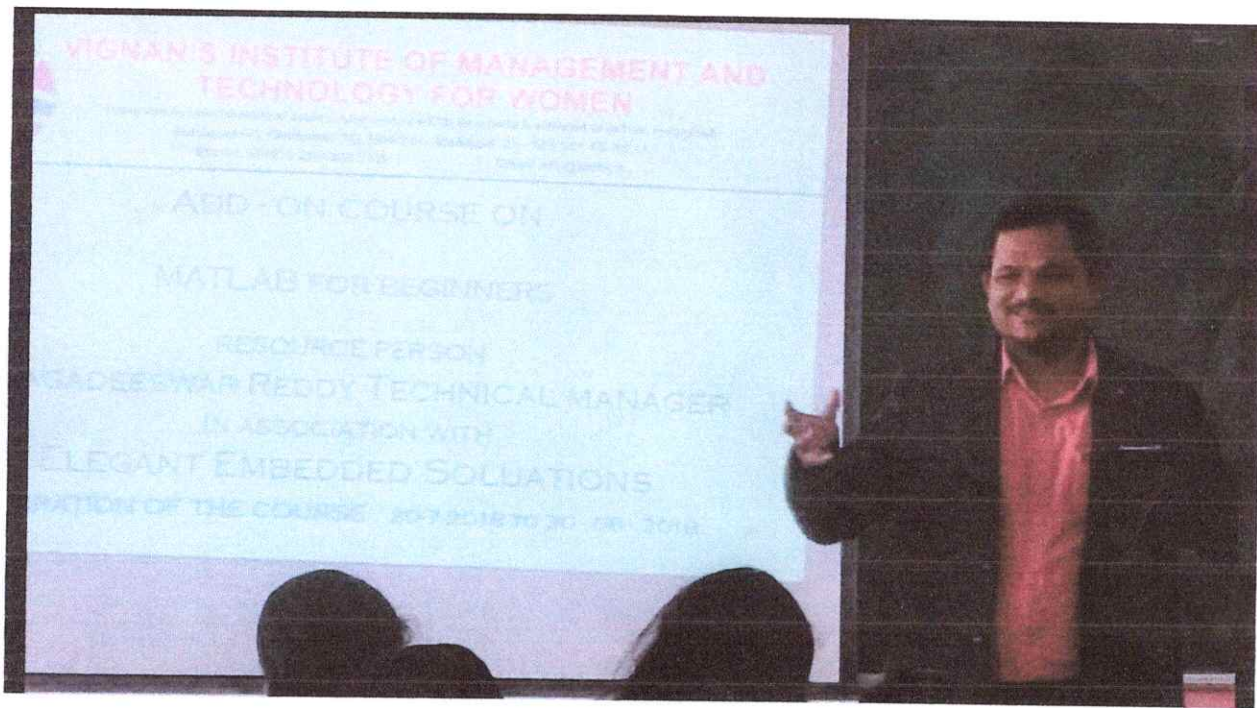
MATLAB for beginners: add on course explained by K. Jagadeeswar Reddy, Technical Manager, Elegant Embedded Solutions, .Hyderabad.

Hence, they have understood the concept on main features of the MATLAB development environment, fundamentals of sampling including the implications of sampling theorem, and also learnt how to analyze the signals with different transform technique and to apply the concepts of convolution and correlation for continuous time signals, understood the fundamentals of Numerical Techniques and Transforms. So, I am thankful for the department for conduct such type of informative events and Students also asking to conduct such types of events further.

Assessment Procedure: The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode.

Coordinator

ECE, HOD

Head of the Department
Electronics and Communication Engineering
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VMTW/ECE/AOC/CIR/2018-19/I/02

CIRCULAR   Date:16-08-2018.

This is to inform all the III B. Tech ECE students that, the Department organizing a Value added course on **"Embedded System Tools for IOT Applications"**. Department advisory committee has been identified Mr.Vijay Kumar R Urkude, Associate Professor in ECE Dept. for the course. He has an efficient and fine knowledge about that add on course, hence he will available 32 days to complete the course, which will take place from 20-08-2018 to 29-09-2018. The participating students will be honored with certificates of participation. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions. Hence those who are interested can sign in registration form.

HOD.

Head of the Department
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Vignan's Institute of Management & Technology For Women
Kondapur(V),Ghatkesar(M),Medchal-Malkajgiri(Dt)-501301
Telangana State

About the Course:

The Internet of things (IoT) is a term for the growing number of electronics that aren't traditional computing devices but are connected to the Internet to send data, receive instructions or both.

There's an incredibly broad range of things that fall under that umbrella: Internet-connected smart versions of traditional appliances like refrigerators and light bulbs, gadgets that could only exist in an internet-enabled world like Alexa-style digital assistants; internet-enabled sensors that are transforming factories, healthcare, transportation, distribution centres and farms.

The Internet of Things is transforming our physical world into a complex and dynamic system of connected devices on an unprecedented scale. The IoT brings the power of the internet, data processing and analytics to the real world of physical objects. For consumers, this means interacting with the global information network without the intermediary of a keyboard and screen; many of their everyday objects and appliances can take instructions from that network with minimal human intervention.

Course Objectives:

- Understand the basic concepts of IoT.
- Plan an IoT implementation.
- Construct and program an IoT device.
- Communicate with an IoT device using wired and wireless connections.
- Process sensor input and control an actuator on an IoT device.
- Manage security, privacy, and safety risks on IoT projects.

- Learn how to apply Machine Learning in IoT.
- Manage an IoT prototyping and development project throughout the development lifecycle.

Expected Outcomes:

At the end of Workshop, participants will be able to

- Describe the evolution of the Internet of Things.
- Identify the range of options for the way things can communicate.
- Select the standards most appropriate for building successful communications.
- Build addressing architectures that can scale to the sizes required.
- Analyze and record the interactions.
- Visualize the results of interactions.
- Deliver the security that modern services demand.
- Build new service networks that can support the future Internet of Things.

Resource Person:
Mr. Vijaykumar R. Urkude,
Associate Professor,
Department of ECE.

Co-ordinator:
Mr. E. Nagaraju,
Assistant Professor,
Dept. of ECE,
Contact No: +91 77308 92217

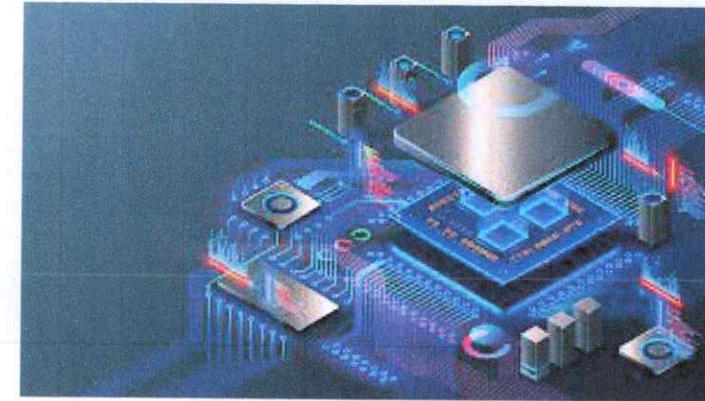*Last Date For Registration: 11th Aug, 2018*

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Telangana State

VALUE ADDED COURSE ON
"EMBEDDED SYSTEM TOOLS FOR IOT APPLICATIONS"
20TH AUG TO 29TH SEP, 2018

Duration of the Course: 35 Hrs

Venue:
'B'-Block Seminar Hall, VMTW.

Organized By:
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE SYLLABUS: Embedded System Tools for IOT Applications

Academic Year: 2018-19

B.Tech II-I SEM   SECTION: ECE-A&B

COURSE OBJECTIVES

- To design various controllers using the basic concepts of IoT.
- Design real time embedded systems using the concepts of RTOS.
- To demonstrate Functioning of various Routing protocols with an IoT device.
- To study the basic concepts of Advanced sensors
- Understanding the concepts of I/O and memory organization and operating systems

Module I: Embedded and Microcontroller Concepts

Introduction to embedded processors, Application Areas, Categories of embedded processors, Hardware architecture, Software architecture, Application software, Communication software, Introduction to Harvard & Neuman architectures, CISC & RISC Architectures.

Module II: PIC Microcontrollers:

Introduction to PIC microcontrollers, architecture and memory organization, registers, I/O ports, interrupts, timer, instruction sets, PIC programming in assembly and C, Sensor interfacing, motor control, SPI bus protocols.

Module III ARM

ARM design philosophy, data flow model and core architecture, registers, program status register instruction pipeline, interrupts and vector table, operating modes and ARM processor families. Instruction Sets: Data processing instructions, addressing modes, branch, load, store instructions, PSR instructions, and conditional instructions

Module IV Raspberry Pi: Raspberry Pi board and its processor, Programming the Raspberry Pi using Python, Communication facilities on Raspberry Pi (I2C,SPI, UART), Interfacing of sensors and actuators

Module V Intel Galileo or Edison microprocessors for Embedded System and IoT

Text Book

1. Muhammod Ali Mazidi, Rolin D. Mckinlay & Danny Sansey, "PIC Microcontroller and Embedded System SPI, UART using Assembly & C for PICI8," Pearson International Edition, 2008.
2. A. N. Sloss, D. Symes, and C. Wright, "ARM System Developer's Guide: Designing and Optimizing System Software", Elsevier, 2008
3. S. Monk, "Programming the Raspberry Pi" McGraw-Hill Education, 2013.

Coordinator   **Resource Person**   **HOD**

Head of the Department
Electronics and Communication Engine
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE NAME: Embedded System Tools for IOT Applications

Academic Year: 2018-19   **Date: 20-08-2018**

B.Tech III-I SEM   **DAY WISE SCHEDULE SHEET**   **SECTION:** ECE-A&B

Room number: BFF6

S.no	DAY	Topic	Timings	Duration
1.	20-08-18	Introduction to embedded processors	04:00pm to 05:00pm	1 hour
2.	21-08-18	Application Areas	04:00pm to 05:00pm	1 hour
3.	22-08-18	Categories of embedded processors	04:00pm to 05:00pm	1 hour
4.	23-08-18	Hardware architecture	04:00pm to 05:00pm	1 hour
5.	24-08-18	Software architecture	04:00pm to 05:00pm	1 hour
6.	25-08-18	Application software	04:00pm to 05:00pm	1 hour
7.	27-08-18	Communication software	04:00pm to 05:00pm	1 hour
8.	28-08-18	Introduction to Harvard	04:00pm to 05:00pm	1 hour
9.	29-08-18	Neuman architectures	04:00pm to 05:00pm	1 hour
10.	30-08-18	CISC & RISC Architectures	04:00pm to 05:00pm	1 hour
11.	31-08-18	CISC & RISC Architectures	04:00pm to 05:00pm	1 hour
12.	01-09-18	PIC Microcontrollers	04:00pm to 05:00pm	1 hour
13.	03-09-18	Introduction to PIC microcontrollers	04:00pm to 05:00pm	1 hour
14.	04-09-18	Architecture and memory Organization	04:00pm to 05:00pm	1 hour
15.	05-09-18	Registers, I/O ports	04:00pm to 05:00pm	1 hour
16.	06-09-18	Interrupts	04:00pm to 05:00pm	1 hour
17.	07-09-18	Timer, instruction sets	04:00pm to 05:00pm	1 hour
18.	08-09-18	PIC programming in assembly and C	04:00pm to 05:00pm	1 hour
19.	10-09-18	Sensor interfacing	04:00pm to 05:00pm	1 hour
20.	11-09-18	Motor control	04:00pm to 05:00pm	1 hour
21.	12-09-18	SPI bus protocols	04:00pm to 05:00pm	1 hour
22.	13-09-18	ARM design philosophy	04:00pm to 05:00pm	1 hour
23.	14-09-18	data flow model and core architecture	04:00pm to 05:00pm	1 hour
24.	15-09-18	ARM processor families	04:00pm to 05:00pm	1 hour
25.	17-09-18	Instruction Sets	04:00pm to 05:00pm	1 hour
26.	18-09-18	Data processing instructions	04:00pm to 05:00pm	1 hour
27.	19-09-18	Addressing modes	04:00pm to 05:00pm	1 hour
28.	20-09-18	conditional instructions	04:00pm to 05:00pm	1 hour
29.	21-09-18	Raspberry Pi	04:00pm to 05:00pm	1 hour
30.	22-09-18	Programming the Raspberry Pi	04:00pm to 05:00pm	1 hour
31.	24-09-18	Communication facilities on Raspberry	04:00pm to 05:00pm	1 hour
32.	25-09-18	Interfacing of sensors and actuators	04:00pm to 05:00pm	1 hour

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33.	27-09-18	Intel Galileo	04:00pm to 05:00pm	1 hour
34.	29-09-18	Embedded System and IoT	04:00pm to 05:00pm	1 hour
35.	29-09-18	Test conducted and feedback taken	04:00pm to 05:00pm	1 hour
		Total 35 hours		

Coordinator

HOD, ECE

Head of the Department
Electronics and Communication Engineeri
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 01-10-2018

PROGRAM REPORT

Name of the Add on Course: Embedded System Tools for IOT Applications

Day/Duration: 35 Hours

Time: 04:00pm TO 05:00pm

Resource Person: Mr. Vijay kumar R urkude, Associate Professor, VMTW.

Name of the Coordinator: Mr. E. Nagaraju, Assistant Professor, VMTW.

Number of the Participants: 80

Topics covered: The following topics covered in this program

1. Construct and program an IoT device.
2. Communicate with an IoT device using wired and wireless connections.
3. Process sensor input and controls an actuator on an IoT device.
4. Manage security, privacy, and safety risks on IoT projects.
5. Data processing instructions, addressing modes, branch

Course Outcomes:

Cos	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Able to understand Arduino programming	P02,P01	2
CO2	Able to apply various coding techniques of Arduino programing for different sensors	P02,P01	2
CO3	Improved Employability and entrepreneurship capacity due to knowledge	P01,P03	3
CO4	Ability to use GNU C to develop embedded software.	P02,P04	2
CO5	Understand the Data processing instructions, addressing modes, branch	P01,P02	2

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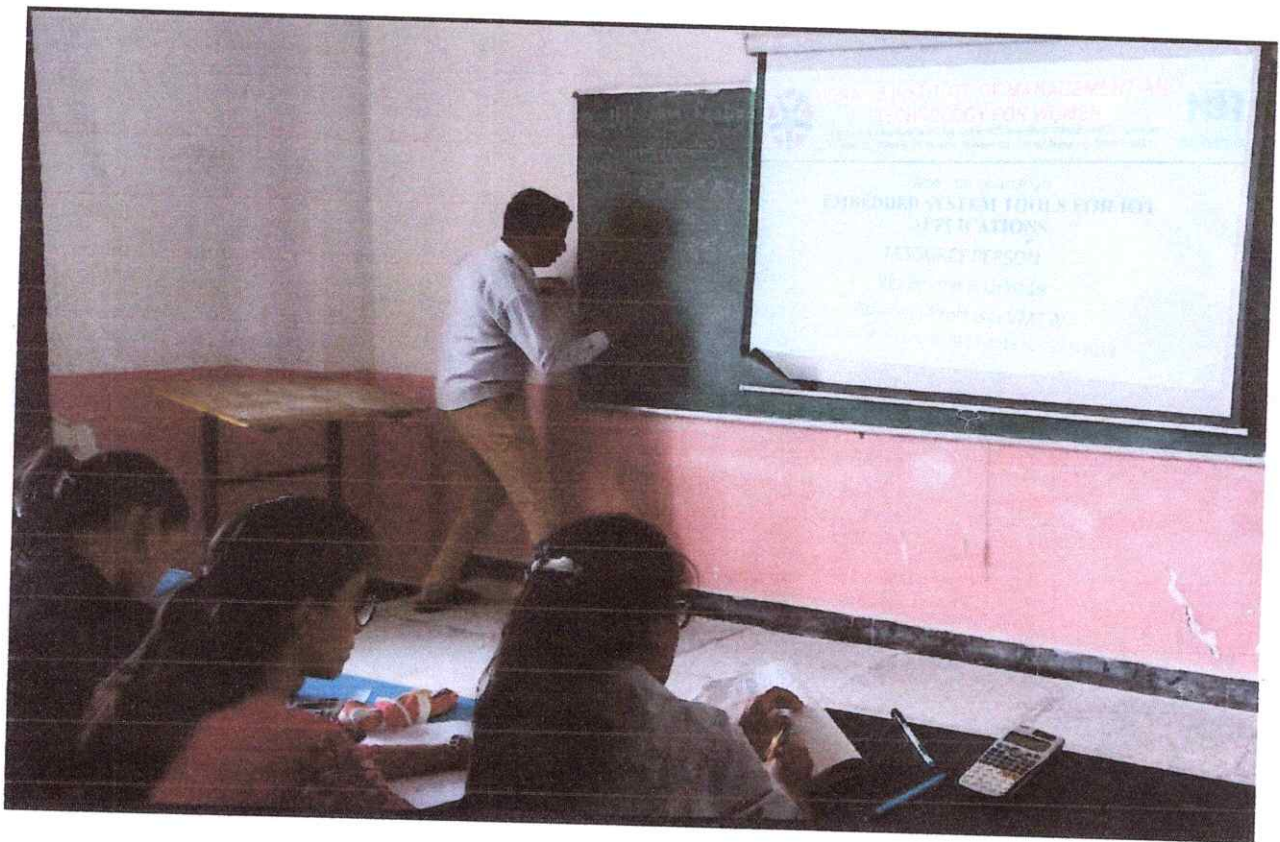
Embedded System Tools for IOT Applications: Add on course explaining by

Mr. Vijaykumar R Urkudey, Associate Professor in ECE Dept.VMTW.

.It was a wonderful event in this students are able to understand Arduino programming,and they have learnt how to apply various coding techniques of Arduino programing for different sensors, Improved Employability and entrepreneurship capacity due to knowledge and also had the ability to use GNU C to develop embedded software, Understood the Data processing instructions, addressing modes, branch. So, students need such kind of events to be held further and also thankful for the department for hosting such kind of events.

Assessment Procedure: The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode.

Coordinator

ECE, HOD

Head of the Department
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VMTW/ECE/AOC/CIR/2018-19/I/03

<div align="center"><u>**CIRCULAR**</u></div>

07-09-2018.

This is to inform all the IV B. Tech ECE students that there will be an Add on course on **"Design and FPGA Implementation of DSP Processor using Verilog"** in association with AMTRONICS PVT LTD., Hyderabad, on 11-09-2018 to 29-10-2018 All students are invited to take part in this course. The participating students will be honored with certificates of participation. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions. Hence those who are interested can sign in registration form.

HOD,ECE.

Head of the Department
Electronics and Communication Engineering
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1

About the Course:

The digital signal processing (DSP) applications are in numerous infotainments, Medical diagnostics and advanced communication and radar systems. Always the advancements in VLSI technology will have an impact on the performance of these systems. VLSI DSP deals with such archietectural and algorithmic optimization techniques to design high-performance DSP systems. As VLSI DSP needs inter-domain expertise of both DSP and VLSI domain, there is a huge necessity to impart thr necessary concepts and handson skills to research scholars and faculty.

Course Objectives:

- Introduction to DSP VLSI
- Architectural transformation techniques (Retiming, Folding)
- Architectural transformation techniques (Unfolding)
- Numerical Strength reduction techniques
- Hands-on MATLAB for VLSI DSP
- Introduction to INTEL FPGA Design flow
- Introduction to DSP Builder
- Design flow using DSP Builder
- Industry practice and trends in DSP VLSI system Design flow

Expected Outcomes:

At the end of Workshop, participants will be able to

- Simulate and verify a design.
- Work with real time projects using Architectural transformation techniques (Retiming, Folding).
- Work with real time projects using Architectural transformation techniques (Unfolding).
- Work with real time projects using Numerical Strength reduction techniques.
- Work with real time projects using DSP Builder.

Resource Person:
Mr. Arjun Modi,
Sr. Engineer,
Amtronics, Hyderabad.

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Co-ordinator:
Mr. J. Sunil Kumar,
Assistant Professor, Dept. of ECE,
Contact No: +91 83747 17668

Last Date For Registration: 10ᵗʰ Sep, 2018

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Verilog

ADD-ON COURSE ON
"DESIGN & FPGA IMPLEMENTATION OF DSP PROCESSOR USING VERILOG"
11ᵀᴴ SEP TO 29ᵀᴴ OCT, 2018

Duration of the Course: 35 Hrs

Vignan's Institute of Management & Technology For Women
Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (Dt.)-501301
Telangana State

Venue:
'B'-Block Computer Lab, VMTW.

Organized By:
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE SYLLABUS: Design and FPGA Implementation of DSP Processor Using Verilog

Academic Year: 2018-19

B.Tech II-I SEM   **SYLLABUS**   **SECTION:** ECE-A&B

COURSE OBJECTIVES

- To provide background and fundamental material for the analysis and processing of digital signals
- To assess the system performance using time domain analysis and methods for improving it.
- Study the security and privacy issues using Verilog.
- To acquaint in Multi-rate signal processing techniques and finite word length effects.
- Understand basic programmable logic devices and testing of CMOS circuits.
- Provide design concepts to design building blocks of data path of any system using gates.

Module I: Digital Signal Processors

Introduction to DSP, Concept of frequency in continuous time and discrete time signals –Sampling theorem – Discrete time signals. Discrete time systems –Analysis of Linear time invariant systems –Z transform –Convolution and correlation.

Module II: Introduction to DFT

Efficient computation of DFT Properties of DFT – FFT algorithms – Radix-2 and Radix-4 FFT algorithms – Decimation in Time – Decimation in Frequency algorithms – Use of FFT algorithms in Linear Filtering and correlation.

Module III Design Applications

General Design Issues, Counter Examples, A Fast Video Controller, and A Position Tracker for a Robot Manipulator, A Fast DMA Controller, Designing Counters with ACT devices, Designing Adders and Accumulators with the ACT Architecture.
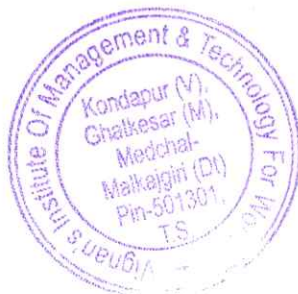
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Module IV Symmetric & Anti-symmetric FIR filters

Linear phase filter – Windowing techniques – rectangular, triangular, Blackman and Kaiser windows – Frequency sampling techniques – Structure for FIR systems

Module V Introduction to Programmable Logic Devices

Introduction, Simple Programmable Logic Devices – Read Only Memories, Programmable Logic Arrays, Programmable Array Logic, Programmable Logic Devices/Generic Array Logic; Complex Programmable Logic Devices – Architecture of Xilinx Cool Runner XCR3064XL CPLD, CPLD Implementation of a Parallel Adder with Accumulation

Text Book

1. 1.Oppenheim A V and Sehafer R W, "Discrete Time Signal Processing", Prentice Hall (1989).
2. Proakis J G and Manolakis D G, "Digital Signal Processing", Pearson Education India

Coordinator

Resource Person

HOD

Head of the Department
Electronics and Communication Engineering
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE NAME: Design and FPGA Implementation of DSP Processor using Verilog

Academic Year: 2018-19

Date: 11-09-2018

B.Tech IV-I SEM   **DAY WISE SCHEDULE SHEET**   **SECTION**: ECE-A&B

Room number: B BLOCK COMPUTER LAB

S.no	DAY	Topic	Timings	Duration
1.	11-09-18	Introduction to embedded	04:00pm to 05:00pm	1 hour
2.	12-09-18	Application Areas	04:00pm to 05:00pm	1 hour
3.	14-09-18	Categories of embedded processors	04:00pm to 05:00pm	1 hour
4.	15-09-18	Hardware architecture	04:00pm to 05:00pm	1 hour
5.	17-09-18	Software architecture	04:00pm to 05:00pm	1 hour
6.	18-09-18	Application software	04:00pm to 05:00pm	1 hour
7.	19-09-18	Communication software	04:00pm to 05:00pm	1 hour
8.	20-09-18	Introduction to Harvard	04:00pm to 05:00pm	1 hour
9.	22-09-18	Architectures	04:00pm to 05:00pm	1 hour
10.	24-09-18	CISC & RISC Architectures	04:00pm to 05:00pm	1 hour
11.	25-09-18	CISC & RISC Architectures	04:00pm to 05:00pm	1 hour
12.	26-09-18	PIC Microcontrollers	04:00pm to 05:00pm	1 hour
13.	27-09-18	Introduction to PIC	04:00pm to 05:00pm	1 hour
14.	28-09-18	Architecture and memory	04:00pm to 05:00pm	1 hour
15.	29-09-18	Registers, I/O ports	04:00pm to 05:00pm	1 hour
16.	01-10-18	Interrupts	04:00pm to 05:00pm	1 hour
17.	03-10-18	Timer, instruction sets	04:00pm to 05:00pm	1 hour
18.	04-10-18	PIC programming in assembly and	04:00pm to 05:00pm	1 hour
19.	05-10-18	Sensor interfacing	04:00pm to 05:00pm	1 hour
20.	06-10-18	Motor control	04:00pm to 05:00pm	1 hour
21.	08-10-18	SPI bus protocols	04:00pm to 05:00pm	1 hour
22.	09-10-18	ARM design philosophy	04:00pm to 05:00pm	1 hour
23.	10-10-18	data flow model and core	04:00pm to 05:00pm	1 hour
24.	11-10-18	ARM processor families	04:00pm to 05:00pm	1 hour
25.	12-10-18	Instruction Sets	04:00pm to 05:00pm	1 hour
26.	13-10-18	Data processing instructions	04:00pm to 05:00pm	1 hour
27.	15-10-18	Addressing modes	04:00pm to 05:00pm	1 hour
28.	16-10-18	conditional instructions	04:00pm to 05:00pm	1 hour
29.	20-10-18	Raspberry Pi	04:00pm to 05:00pm	1 hour
30.	22-10-18	Programming the Raspberry Pi	04:00pm to 05:00pm	1 hour
31.	24-10-18	Communication facilities on	04:00pm to 05:00pm	1 hour

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		Raspberry Pi		
32.	25-10-18	Interfacing of sensors and actuators	04:00pm to 05:00pm	1 hour
33.	27-10-18	Intel Galileo	04:00pm to 05:00pm	1 hour
34.	29-10-18	μ for Embedded System and IoT	03:00pm to 04:00pm	1 hour
35.	29-10-18	Test conducted & Feedback Taken	04:00pm to 05:00pm	1 hour
Total 35 hours				

Coordinator

HOD

Head of the Department
Electronics and Communication Engineering
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 31-10-2018.

PROGRAM REPORT

Name of the Add on Course: Design and FPGA Implementation of DSP Processor using Verilog

Day/Duration: 35 HOURS

Time: 4:00PM TO 5:00PM

Resource Person: Mr. ArjunModi, Sr.Engineer, Amtronics PVT, LTD, Hyderabad.

Name of the Coordinator: Mr. J. Sunil Kumar, Assistant Professor, VMTW

Number of the Participants: 79

Topics covered: The following topics covered in this program

- Digital Signal Processors
- Introduction to DFT
- Design Applications
- Symmetric & Anti-symmetric FIR filters
- Introduction to Programmable Logic Devices

Course Outcomes: Work with real time projects using Architectural transformation techniques

COs	At the end of the course, students will have the ability to:	POs Mapped	Strength of mapping
CO1	Understand the LTI system characteristics and Multi-rate signal processing.	PO1,PO3	3
CO2	Understand the significance of various filter structures and effects of round off errors	PO1,PO3	2
CO3	Understand the inter-relationship between DFT and various transforms	PO1,PO2	2
CO4	Understand different types of faults that can occur in a system and learn the concept of testing and adding extra hardware to improve testability of system	PO1,PO2	3
CO5	Design building blocks of data path systems, memories and simple logic circuits using PLA, PAL, FPGA and CPLD	PO1,PO2	2

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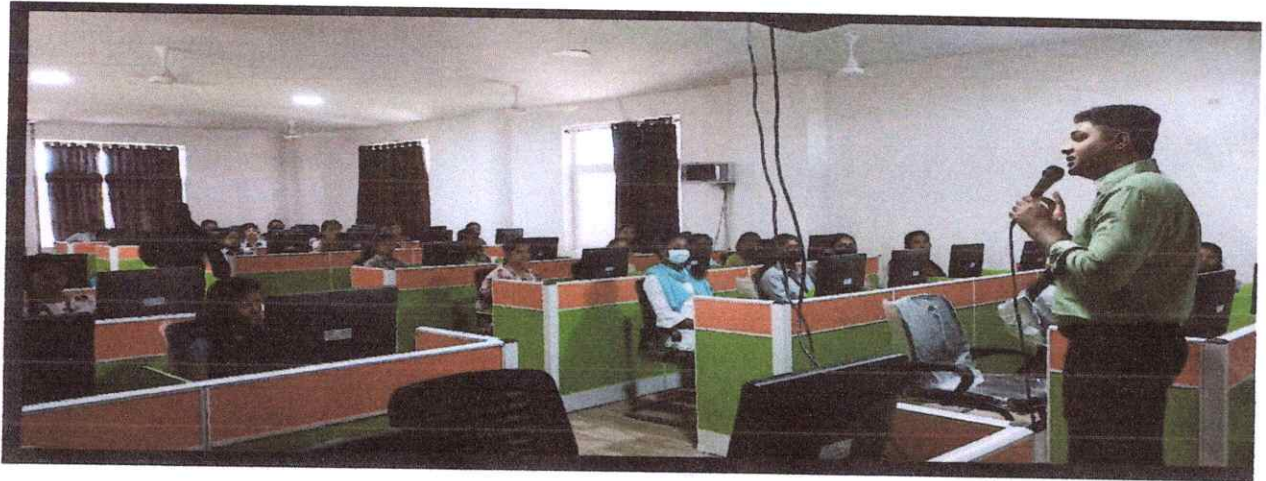
Students attended for the course: Design and FPGA Implementation of DSP Processor using Verilog

Hence, students have understood the LTI system characteristics and Multi-rate signal processing, the significance of various filter structures and effects of round off errors, the inter-relationship between DFT and various transforms, the different types of faults that can occur in a system and learn the concept of testing and adding extra hardware to improve testability of system and to design building blocks of data path systems, memories and simple logic circuits using PLA, PAL, FPGA and CPLD. We are thankful for conducting such type of events and students would like to have such a events further.

Assessment Procedure: The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode.

Coordinator

ECE, HOD

Head of the Department
Electronics and Communication Engineering
Vignan's Institute of Management & Technology For Women
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VMTW/ECE/AOC/CIR/2018-19/II/04

24-12-2018.

CIRCULAR

This is inform to the entire faculty that ECE department is going to organize a Value added course on **"Aspects of Micro Wave Antenna Design and its upcoming uses"** for the III-B.Tech students. Department advisory committee has been found Mr. Mr.Vijay Kumar R Urkude Associate Professor in ECE Dept. for the course. He has an efficient and fine knowledge about that add on course; hence he will avail 32 hours to complete the course, which will take place from 02-01-2019 to 08-03-2019. Hence those who are interested can sign in registration form.

HOD

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    III.B.Tech students

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Electronics and Communication Engineer
Vignan's Institute of Management and Technology for
Kondapur (V), Ghatkesar (M), R.R. Dist-501

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About the Course:

This course is designed to provide exposure to the fundamentals of RF and microwave engineering. During this course, the scientists, academicians, and industry persons in their respective areas of expertise will discuss the current and future scope on microwave industries in India. The participants of this short-term program are expected to acquire different design steps of antenna and microwave circuit and their applications in VLSI and communication field. This course provides an opportunity for participants to get cutting-edge research ideas and directions.

Course Objectives:

- Design and Analysis of Planar and 3-D Antenna Structures
- Antenna Array Analysis and Beam forming
- Microwave Filter Analysis
- PCB Fabrication and Analysis
- MEMS and NEMS
- MIMO/Antenna for WPAN
- Characterization of Silicon Devices
- Recent Trends in RF Energy Harvesting

Expected Outcomes:

At the end of Workshop, participants will be able to

- Work with Microwave Filter Analysis.
- Work with PCB Fabrication and Analysis.
- Work with MEMS and NEMS.
- Work with MIMO/Antenna for WPAN.
- Work with live projects in RF energy Harvesting.

Resource Person:
Mr. Vijaykumar R. Urkude,
Associate Professor,
Department of ECE.

Co-ordinator:
Mr. E. Nagaraju,
Assistant Professor,
Department of ECE,
Contact No: +91 83747 17668

Last Date For Registration: 28th Dec, 2018

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VALUE ADDED COURSE ON
"ASPECTS OF MICROWAVE ANTENNA DESIGN AND ITS UPCOMING USES"
2ND JAN TO 8TH MAR, 2019

PRINCIPAL
Vignan's Institute of Management & Technology For Women
Kondapur(V),Ghatkesar(M),Medchal-Malkajgiri(Dt)-501301
Telangana State

Duration of the Course: 35 Hrs

Venue:
'B'-Block Seminar Hall, VMTW.

Organized By:
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE SYLLABUS: Aspects of Micro Wave Antenna Design and its upcoming uses

Academic Year: 2018-19

B.Tech III-II SEM   **SECTION:** ECE-A&B

COURSE OBJECTIVES

- Design and Analysis of Planar and 3-D Antenna Structures
- To understand the concept of radiation, and significance of antenna parameters
- To analyze the characteristics MEMS
- To analyze the characteristics and design relations of UHF, VHF and Microwave Antennas
- To understand the concepts and set-up requirements for microwave measurements.

Module – I: Antenna Theory: Antennas, Radiation concept, Types of Antennas, Antenna parameters, Friis Transmission equation.

Module – II: Aperture Antenna: Introduction, Pyramidal Horns- Design Procedure, Conical and Corrugated Horns, Aperture Corrugated Horns, Reflected Antennas- Parameters, Analysis of front-fed parabolic reflector, Feed methods and feed types, Cassegrain Reflector Horns.

Module – III: Micro strip Radiators: Introduction, Rectangular Micro-strip Antenna analysis and Design, Circular Micro strip Antenna Analysis and Design,

Module – IV : Micro strip Slot Antennas: Wave guide fed slots, Radiation mechanism, Micro strip slot antennas, Introduction to rectangular slot antennas, narrow, wide, tapered and circularly polarized slot antennas, Annular slot antennas, Comparison of micro-strip slot antennas with patch antennas.

Module – V : Micro Strip Antenna Arrays: Introduction, Micro strip array antennas, Characteristics of fixed beam linear antenna arrays, Linear micro strip arrays, Characteristics of planar arrays, Micro-strip planar arrays, Microstrip scanned array antennas, Phase scanned micro-strip arrays, Time delay scanning,

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Electronic feed switching, Frequency scanned microstrip arrays, Advantage and disadvantages of phased array antennas.

REFERENCE BOOKS:

1. Ramesh Garg, Prakash Bhatia, Inder Bahl, Apisak Ittipiboon, "Microstrip Antenna Design Hand Book", Artech House Inc., 2001.
2. Samuel Silver, "Microwave Antenna – Theory and design", IEE Press, 1984.
3. James. J R. Hall, P S. Wood. C, "Micro strip Antenna-Theory and Design", Peter Peregrinus Ltd., 1981.

Coordinator   **Resource Person**   **HOD**

Head of the Department
Electronics and Communication Engine
Vignan's Institute of Management & Technology
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ADD ON COURSE NAME: Aspects of Micro Wave Antenna Design and its upcoming uses

Academic Year: 2018-19

Date: 28-12-2018

B.Tech III-II SEM   **DAY WISE SCHEDULE SHEET**   **SECTION: ECE-A&B**

Room number: BFF6

S.no	DAY	Topic	Timings	Duration
1.	02-01-19	Antenna Theory	04:00pm to 05:00pm	1 hour
2.	03-01-19	Antennas Introduction	04:00pm to 05:00pm	1 hour
3.	04-01-19	Radiation concept	04:00pm to 05:00pm	1 hour
4.	05-01-19	Types of Antennas	04:00pm to 05:00pm	1 hour
5.	07-01-19	Antenna parameters	04:00pm to 05:00pm	1 hour
6.	08-01-19	Friis Transmission equation	04:00pm to 05:00pm	1 hour
7.	09-01-19	Aperture Antenna	04:00pm to 05:00pm	1 hour
8.	10-01-19	Aperture Antenna : Introduction	04:00pm to 05:00pm	1 hour
9.	11-01-19	Pyramidal Horns	04:00pm to 05:00pm	1 hour
10.	12-01-19	Design Procedure	04:00pm to 05:00pm	1 hour
11.	21-01-19	Conical and Corrugated Horns	04:00pm to 05:00pm	1 hour
12.	22-01-19	Aperture Corrugated Horns	04:00pm to 05:00pm	1 hour
13.	23-01-19	Reflected Antennas	04:00pm to 05:00pm	1 hour
14.	24-01-19	Parameters	04:00pm to 05:00pm	1 hour
15.	25-01-19	front-fed parabolic reflector	04:00pm to 05:00pm	1 hour
16.	28-01-19	Feed methods and feed types	04:00pm to 05:00pm	1 hour
17.	29-01-19	Cassegrain Reflector Horns.	04:00pm to 05:00pm	1 hour
18.	30-01-19	Microstrip Radiators	04:00pm to 05:00pm	1 hour
19.	31-01-19	Introduction Rectangular Microstrip	04:00pm to 05:00pm	1 hour
20.	31-01-19	Antenna analysis and Design	04:00pm to 05:00pm	1 hour
21.	01-02-19	Circular Microstrip	04:00pm to 05:00pm	1 hour
22.	02-02-19	Microstrip Slot Antennas	04:00pm to 05:00pm	1 hour
23.	05-02-19	Wave guide fed slots	04:00pm to 05:00pm	1 hour
24.	06-02-19	Radiation mechanism	04:00pm to 05:00pm	1 hour
25.	07-02-19	Antenna Theory	04:00pm to 05:00pm	1 hour
26.	11-02-19	Antennas Introduction	04:00pm to 05:00pm	1 hour
27.	12-02-19	Radiation concept	04:00pm to 05:00pm	1 hour
28.	13-02-19	Types of Antennas	04:00pm to 05:00pm	1 hour
29.	14-02-19	Antenna parameters	04:00pm to 05:00pm	1 hour
30.	15-02-19	Micro strip slot antennas	04:00pm to 05:00pm	1 hour
31.	01-03-19	Rectangular slot antennas	04:00pm to 05:00pm	1 hour
32.	02-03-19	Narrow, wide	04:00pm to 05:00pm	1 hour

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33.	05-03-19	Polarized slot antennas	04:00pm to 05:00pm	1 hour
34.	08-03-19	Annular slot antennas	04:00pm to 05:00pm	1 hour
35.	08-03-19	Test conducted	04:00pm to 05:00pm	1 hour
	Total 35 hours			

Coordinator

HOD, ECE

Head of the Department
Electronics and Communication Engineering
Vignan's institute of Management and Technology for Women
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 11-03-19

PROGRAM REPORT

Name of the Add on Course: Aspects of Micro Wave Antenna Design and its upcoming uses

Day/Duration: 02-01-2019 TO 08-03-2019

Time: 4:00PM TO 5:00PM

Resource Person: Mr. Vijaykumar R Urkudey, Associate Professor in ECE Dept. VMTW.

Name of the Coordinator: Mr.E.Nagaraju, Assistant Professor in ECE Dept, VMTW.

Number of the Participants: 82

Topics covered: The following topics covered in this program

1. Design and Analysis of Planar and 3-D Antenna Structures
2. Antenna Array Analysis and Beam forming
3. Microwave Filter Analysis
4. PCB Fabrication and Analysis
5. MEMS and NEMS

Course Outcomes:

COs	At the end of the course, students will have the ability to:	POs Mapped	Strength of mapping
CO1	Able to understand Micro wave antenna characteristics	PO3,PO5	3
CO2	Able to explain the mechanism of radiation, definitions of different antenna characteristic parameters and establish their mathematical relations.	PO2,PO3	2
CO3	Able to understand to design antenna	PO3,PO5	3
CO4	Able to Design and Analysis of Planar and 3-D Antenna Structures	PO2,PO3	2
CO5	Understand MIMO/Antenna for WPAN	PO1,PO2	1

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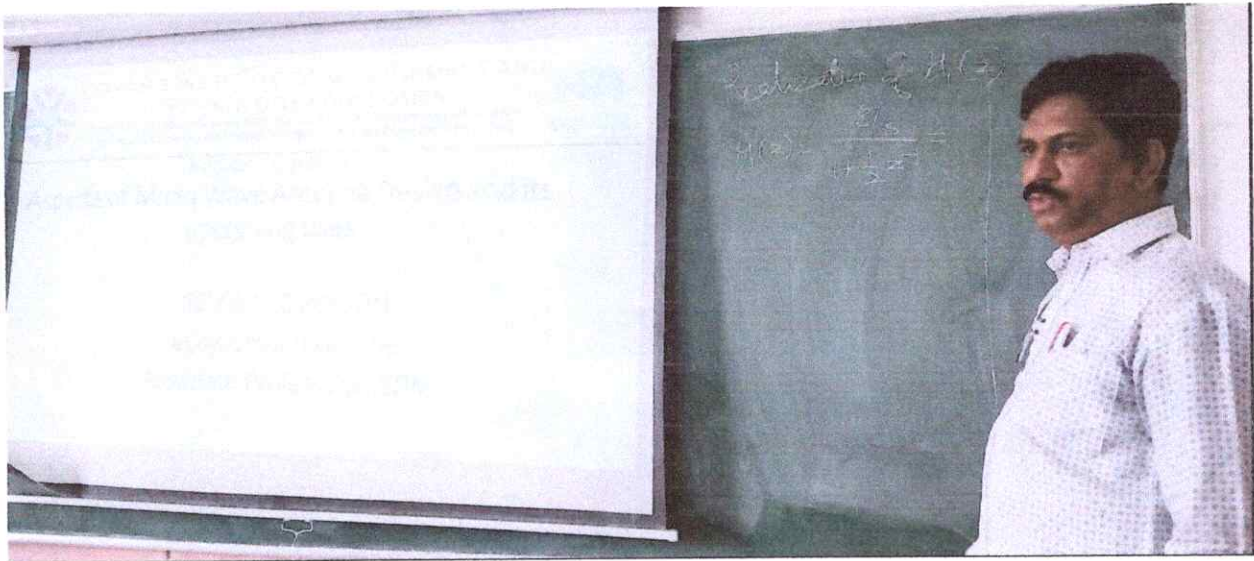
Aspects of Micro Wave Antenna Design and its upcoming uses: Add on course explaining by

Mr. Vijaykumar R Urkudey, Associate Professor in ECE Dept.VMTW.

By having such a wonderful event students are able to understand Micro wave antenna characteristics, and are ableto design antenna,and understood MIMO/Antenna for WPAN,and know how to Design and Analysis of Planar and 3-D Antenna Structures and they are also able to explain the mechanism of radiation, definitions of different antenna characteristic parameters and establish their mathematical relations.So,students are thankful to the department for conducting such type of event and they also want this event to be held further more.

Assessment Procedure: The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode

Coordinator

ECE, HOD

Head of the Department
Electronics and Communication Engineering
Vignan's Institute of Management & Technology For Women
Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (Dt.)-501301

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

VMTW/CSE/AOC/2018-2019/II/03

21-12-2018

CIRCULAR

This communication is intended to remind all IV-II B. Tech. CSE students that Mr. T.Srajan Kumar, Assistant Professor, Department of CSE, VMTW, will be teaching an Add-on course on "Android app development" from 2/1/2019 to 8/1/2019. Everyone is welcome to enroll in this course. Therefore, interested students can fill out registration form.

HOD

Head of The Department
Computer Science and Engineering
Vignan's Institute of Management & Technology For Women
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IV-II B.Tech Students

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About the Course:

This course covers the Fundamentals of Android App Development, starting with basics and gradually progressing to advanced topics like Project Structure, Activity in Android, Groups, Adapters, and more. You'll use Android Studio to build your own app and become an accomplished Android Developer. Android is the most popular mobile OS with a huge demand for skilled Android Developers worldwide. This course is for beginners and experienced programmers who want to create Android apps and includes chapter-wise quizzes and coding assignments to reinforce your understanding. Geosys offers step-by-step lessons, starting with Java Essentials for Android, to develop professional Android apps.

Course Objectives:

This course teaches how to set up Flutter SDK, Android Studio IDE, and Android Emulator on your machine, but it's not required to follow the exercises. You'll learn Flutter widgets and layouts, build a contact profile page, and use themes for app styling. The course covers Android development tools like Eclipse and DDMS, the importance of the activity lifecycle, and how to create list and grid views with data sources. You'll also learn to create apps with multiple activities, pass information between them, use audio and video files, and generate notifications in Android.

Expected Outcomes:

This module aims to educate individuals about Android OS, Gradle, and mobile app development using Android Studio. Android OS is an open-source operating system by Google specifically designed for mobile phones, which has gained immense popularity over the years resulting in high demand for android developers. The development process involves creating applications powered by the agile Android OS on Android Studio with the help of the Android Software Development Kit. Android development is a sought-after career path as it offers a high success ratio despite reduced resource utilization and lower costs. To become a successful Android developer, it is crucial to have a thorough understanding of the operating system and development tools such as those found in the Android Developer's Toolkit. Developers must also comprehend the key forces and constraints acting on handheld devices and how to accommodate these when building their own Android applications. This module provides additional sources of information to understand and solve Android-related problems, and teaches the organization, patterns, and programming mechanisms of the Android platform to effectively develop their own Android applications.

Resource Person:
Mr. T. Srajan Kumar
Assistant Professor,
Department of CSE.

Co-ordinator:
Mr. K. Bharath Reddy,
Assistant Professor,
Department of CSE.
Contact No: +91 8985 441 753

Last Date For Registration: *28th Dec, 2018*

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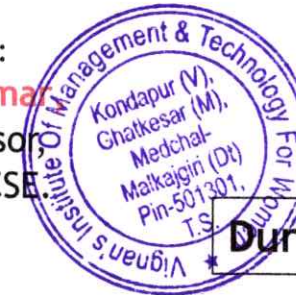Approved by AICTE, New Delhi & Affiliated to JNTUH, Hyderabad.
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ADD-ON COURSE ON
"ANDROID APP DEVELOPMENT"
2ND TO 8TH JAN, 2019

Duration of the Course: 32 Hrs

Venue:
'A'-Block Seminar Hall, VMTW.

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Addon Course Syllabus

Addon Course: Android app development

Course Outcomes:

1. To create robust mobile applications and learn how to integrate them with other services.
2. To create intuitive, reliable mobile apps using the android services and components.
3. To create a seamless user interface that works with different mobile screens.
4. To understand the concept of DDMS.
5. To understand the concepts of Form Widgets.

Module 1: Introduction to Android, Basic Building blocks - Activities,Services, Broadcast Receivers & Content providers, UI Components - Views & notifications.

Module 2: AndroidManifest.xml, uses-permission & uses-sdk, Resources & R.java o Assets, Layouts & Drawable Resources, Activities and Activity lifecycle, First sample Application

Module 3: Launching emulator, Editing emulator settings, Emulator shortcuts, Logcat usage, introduction to DDMS.

Module 4: Form widgets, Text Fields, Layouts, [dip, dp, sip, sp] versus px, Examples.

Module 5: Option menu, Context menu, Sub menu, menu from xml, menu via code, Examples.

Text Books:

1. Android Programming for Beginners, Author Name: John Horton, Publisher: Packt Publishing, Latest Edition: 2nd edition.

2. Android App Development For Dummies, Author Name: Michael Burton, Publisher: Wiley, Latest Edition: 3rd edition.

COORDINATOR

HOD
Head of The Department
Computer Science and Engineering
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Addon Course Name: Android app development

DAY WISE SCHEDULE

DAY	Topic	Timings	Duration
02-01-2019	Introduction to Android, Basic Building blocks - Activities, Services, Broadcast Receivers & Content providers, UI Components - Views & notifications.	09:00 am to 03:30 pm	6 hrs
03-01-2019	Android Manifest.xml, uses-permission & uses-sdk, Resources & R.java o Assets, Layouts & Draw able Resources, Activities and Activity lifecycle, First sample Application.	09:00 am to 03:30 pm	6 hrs
04-01-2019	Launching emulator, Editing emulator settings, Emulator shortcuts, Logcat usage, Introduction to DDMS.	09:00 am to 03:30 pm	6 hrs
07-01-2019	Form widgets, Text Fields, Layouts, [dip, dp, sip, sp] versus px, Examples.	09:00 am to 04:30 pm	7 hrs
08-01-2019	Option menu, Context menu, Sub menu, menu from xml, menu via code, Examples.	09:00 am to 04:30 pm	7 hrs
Total 32 hours			

COORDINATOR

HOD

Head of The Department
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Addon Course Report

Name of the Add on Course: Android app development

Day/Duration: 02-01-2019 to 08-01-2019 5(Days) - 32Hrs

Time: 09:00am to 04:30pm

Name of the Coordinator: Mr.K.Bharath Reddy, Assistant Professor, Dept of CSE, VMTW.

Number of the Participants: 76

Topics covered: The following topics covered in this program

- Introduction to Android, Basic Building blocks - Activities,Services,Broadcast Receivers & Content providers.
- AndroidManifest.xml, uses-permission & uses-sdk, Resources & R.java Assets, Layouts & Drawable Resources.
- Launching emulator, Editing emulator settings, Emulator shortcuts, Logcat usage,
- Form widgets, Text Fields, Layouts, [dip, dp, sip, sp] versus px, Examples.
- Option menu, Context menu, Sub menu, menu from xml, menu via code, Examples.

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Apply essential Android Programming concepts.	PO1,PO2	3
CO2	Develop various Android applications related to layouts & rich uses interactive interfaces	PO3	4
CO3	Develop Android applications related to mobile related server-less database like SQLITE	PO3.PO5	2
CO4	Install and configure Android application development tools.	PO4	4
CO5	Design and develop user Interfaces for the Android platform	PO3	3

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Resource Person: Mr. T.Srajan Kumar, Assistant Professor, Dept of CSE, VMTW.

COORDINATOR

HOD

Head of The Department
Computer Science and Engineering
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

VMTW/CSE/AOC/2018-2019/II/01

Date: 02-07-2018

CIRCULAR

This is to inform all the III-II B. Tech CSE students that there will be an Add on course on "Deep learning using Spyder IDE Environment" from 09-07-2018 to 14-07-2018 by Mrs.B.Geetha, Assistant Professor, Dept of CSE, VMTW. All students are invited to take part in this course. The participating students will be honored with certificates of completion. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions. Hence those who are interested can sign in registration form.

HOD
Head of The Department
Computer Science and Engineering
Vignan's Institute of Management & Technology For Women
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About the Course:

Deep Learning is a foundational program that will help you understand the capabilities, challenges, and consequences of deep learning and prepare you to participate in the development of leading-edge AI technology. It has necessary to have interactive environments to create software applications and this fact becomes very important when you work in the fields of Data Science, engineering, and scientific research. The Python Spyder IDE has been created for the same purpose. In this, you will be learning how to install and make use of Spyder or the Scientific Python and Development IDE. It provides real-time code introspection (The ability to examine what functions, keywords, and classes are, what they are doing and what information they contain). Spark makes use of the concept of RDD to achieve faster and more efficient MapReduce operations. Here you will learn how MapReduce operations take place and why they are not so efficient.

Course Objectives:

This course introduces major deep learning algorithms, the problem settings, and their applications to solve real world problems. Identify the deep learning algorithms which are more appropriate for various types of learning tasks in various domains. Implement deep learning algorithms and solve real-world problems. There are a few reasons for this, It is versatile and easy to learn and use it allows for multiple ways to handle inputs and outputs. This makes it especially easy and fast to explore data it has built-in support for scientific computing tasks: SciPy, NumPy, Pandas, and a vast number of other libraries created by and for scientists. It is the most widely used programming language for Machine Learning (including Keras and Tensor Flow) It has excellent libraries for plotting It can be integrated with Jupyter Notebooks and implementations of these in the cloud. Various aspects of code writing can be implemented through IDEs like compiling, debugging, building executables, editing source code, etc. Python is a widely used language by coders, and Python IDEs help in coding & compiling easily.

Expected Outcomes:

Understand and analyze Applications of Deep Learning to NLP. Spyder is a Scientific Integrated Development Environment written in Python, and designed by and for scientists, engineers, and data analysts. It features a unique combination of the advanced editing, analysis, debugging, and profiling functionality of a comprehensive development tool with the data exploration, interactive execution, deep inspection, and beautiful visualization capabilities of a scientific package. Together with Python, it provides a very complete set of tools for scientific computing. Implement deep learning algorithms, understand neural networks and traverse the layers of data   Understand the topics such as convolutional neural networks, recurrent neural networks, training deep networks and high-level interfaces. You can find the redundant variables, errors, and syntax issues in your code without even compiling it in Spyder via the static code analysis feature. It is also integrated with many DS packages like NumPy, SciPy, Pandas, IPython, etc. to help you in doing data analytics.

Resource Person:
Mrs. B. Geetha,
Assistant Professor,
Department of CSE.

Co-ordinator:
Mrs. P. Prathima,
Assistant Professor,
Department of CSE.
Contact No: +91 9703 598 700

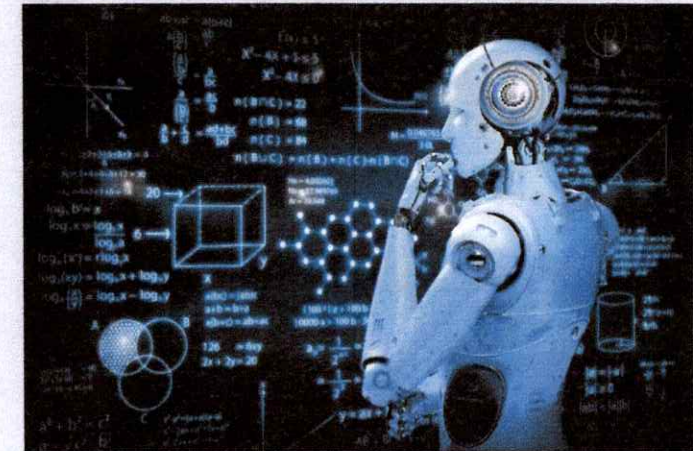**Last Date For Registration: 7th July, 2018**

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Approved by AICTE, New Delhi & Affiliated to JNTUH, Hyderabad.
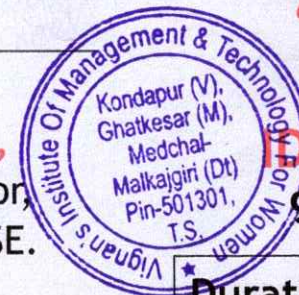**Kondapur (V), Ghatkesar (M), Medchal - Malkajgiri (D) - 501 301**

ADD-ON COURSE ON
"DEEP LEARNING USING SPYEDER IDE ENVIRONMENT"
9TH TO 14TH JUL, 2018

Duration of the Course: 30 Hrs

Venue:
'A'-Block Seminar Hall, VMTW.

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Addon course syllabus

Addon course: Deep learning using Spyder IDE Environment

Course objectives:

1. To Understand the context of neural networks and deep learning
2. To Explore the parameters for neural networks
3. To Have a working knowledge of neural networks and deep learning
4. To Know how to use a neural network
5. To build the training Loop, Overfitting and Underfitting

Module 1: Introduction to Neural Network, Gradient descent, Stochastic Gradient descent Perceptron, Multilayer Perceptron, BackPropagation, Overview of deep learning, DL Environment setup locally, Installing Tensorflow Installing Keras.

Module 2: Placeholders in Tensorflow, Defining placeholders, Feeding placeholders with data, Variables, Constant, Computation graph, Visualize graph with Tensor Board.

Module 3: Sigmoid function, Hyperbolic Tangent function, ReLu -Rectified Linear units, Softmax function.

Module 4: Exploring the MNIST dataset, Defining the hyperparameters, Model definition.

Building the training loop, Overfitting and Underfitting, Building Inference.

Text Books

1. Deep Learning (Adaptive Computation and Machine Learning series) Hardcover by Aaron Courville (Author), Ian Goodfellow (Author), Yoshua Bengio (Author).
2. Deep Learning and Applications by Shaik Mohammad Rafi (Author), Naveena (Author), Malathi Eswaran (Author).

Prathima
COORDINATOR

v. Indira
HOD
Head of The Department
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ADDON COURSE NAME: Deep learning using Spyder IDE Environment

DAY WISE SCHEDULE SHEET

Room No: SEMINAR HALL-ASF-5

DAY	Topic	Timings	Duration
09-07-2018	Introduction to Neural Network, Gradient descent, Stochastic Gradient descent Perception, Multilayer Perception, Back Propagation, Overview of deep learning, DL Environment setup locally, Installing Tensor flow Installing Keras.	09:00 am to 03:30 pm	6 hrs
10-07-2018	Placeholders in Tensor flow, Defining placeholders, Feeding placeholders with data.	09:00 am to 03:30 pm	6 hrs
11-07-2018	Sigmoid function, Hyperbolic Tangent function, ReLu - Rectified Linear units, Softmax function.	09:00 am to 03:30 pm	6 hrs
12-07-2018	Exploring the MNIST dataset, Defining the hyper parameters	09:00 am to 03:30 pm	6 hrs
13-07-2018	Model definition. Building the training loop, Over fitting and Under fitting.	09:00 am to 03:30 pm	6hrs
14-07-2018	Variables, Constant, Computation graph, Visualize graph with Tensor Board, Building Inference.	09:00 am to 03:30 pm	6hrs
	Total 36 hours		

Prathima
COORDINATOR

V. Indira
HOD
Head of The Department
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Date: 17-07-2018

ADDON COURSE REPORT

Name of the Add on Course: Deep learning using Spyder IDE Environment

Day/Duration: 09-07-2018 to 14-07-2018 6(Days) - 36Hrs

Time: 09:00am to 03:30pm

Resource Person: Mrs.B.Geetha Assistant Professor, Dept of CSE, VMTW.

Name of the Coordinator: Mrs. P.Prathima, Assistant Professor, Dept of CSE, VMTW.

Number of the Participants: 74

Topics covered: The following topics covered in this program

- Overview of deep learning, DL Environment setup locally, Installing Tensor flow Installing Keras.
- Defining placeholders, Feeding placeholders with data,Variables, Constant, Computation graph, Visualize graph with Tensor Board.
- Sigmoid function, Hyperbolic Tangent function, ReLu -Rectified Linear units, Softmax function.
- Defining the hyperparameters, Model definition, Building the training loop, Overfitting and Underfitting, Building Inference.

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Explain the basic principles behind neural networks and deep learning	PO1	4
CO2	Compare modeling aspects of various neural network architectures	PO2,PO5	5
CO3	Apply and evaluate deep learning on real data sets	PO1,PO3	3
CO4	Implement simple neural network algorithms	PO5	3
CO5	Illustrate the application of the algorithms	PO2	4

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Value added course by Mrs.B.Geetha, Assistant Professor, Department of CSE.

Assessment Procedure: The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode.

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

VMTW/CSE/AOC/2018-2019/II/05

01-03-2019

CIRCULAR

This is to inform all the II-II B.Tech CSE students that there will be an Add on course on **"AI Automation and Testing"** from 5/3/2019 to 11/3/2019 by Mrs.Indrani Vasireddy, Associate Professor, Department CSE, VMTW. All students are invited to take part in this course. The participating students will be honored with certificates of participation. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions. So those who are interested can sign in registration form.

HOD

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About the Course:

In this course, the focus is designed to provide you with a complete knowledge of software testing technologies such as Selenium Web Driver, TestNG, Maven, AutoIT, Selenium Grid, Appium, and Docker to help you build a robust testing framework. This introduces state-of-the-art techniques for automated analysis. The automated analysis encompasses both approaches to automatically generate a very large number of tests to check whether programs meet requirements and also means by which it is possible to prove that software meets requirements and that it is free from certain commonly-occurring defects, such as divide-by-zero, overflow/underflow, deadlock, race-condition freedom, buffer/array overflow, uncaught exceptions, and several other commonly-occurring bugs that can lead to program failures or security problems. They will become familiar with the fundamental theory and applications of such approaches, and apply a variety of automated analysis techniques on example programs.

Course Objectives:

The objective of Automation testing course has enabled with a better understanding of software quality testing tools and technologies along with test entry and test exit criteria based on real live project scenarios. Automation testing has widespread usage in the space of Artificial intelligence, automotive embedded systems, API testing, and so on. It will confer you with knowledge of automation frameworks, test suits, scripting, test cycles, when and how of automation testing, ways to improve productivity and efficiency with test automation, best practices, etc. The topics shall include understanding test design, basic and advanced scripting, programming techniques, database fundamentals, RDBMS basics, an overview of automation testing, automation architecture and so forth. These will further help you to obtain professional automation testing certifications like CAST -Certified Associate in Software Testing, Certified Automation Functional Testing professional, Certified Software Test Automation Specialist, Certified Software Test Automation Architect, etc.

Expected Outcomes:

Understand the foundations of automated verification: randomization and symbolic representation. Distinguish the strengths and weaknesses of random testing, symbolic analysis, static analysis, and model checking. Use a variety of state-of-the-art static analysis and automated testing tools for automated verification. Create executable requirements as an oracle suitable for automated testing and symbolic analysis. Understand how the choice of oracle affects fault-finding for automated analysis strategies. Use automated testing to achieve full mutation coverage. Create a test plan that utilizes both manually-written tests and automated tests towards maximizing rigor, minimizing effort and time, and minimizing test costs. It is beneficial for automation test engineers to have coding knowledge. It helps them communicate better with developers and participate in unit testing.

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ADD-ON COURSE ON
"AI AUTOMATION AND TESTING"
5TH TO 11TH MAR, 2019

Duration of the Course: 30 Hrs

Resource Person:
Mrs. V. Indrani,
Associate Professor,
Department of CSE.

Co-ordinator:
Mr. V. Jagadeeshwar Reddy,
Assistant Professor,
Department of CSE.
Contact No: +91 9676 891 489

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Venue:
'A'-Block Seminar Hall, VMTW.

Organized By:
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Last Date For Registration: 3rd March, 2019

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Addon Course Syllabus

Addon Course: AI Automation and Testing

Course objectives:

1. To impart knowledge about Artificial Intelligence.
2. To give understanding of the main abstractions and reasoning for intelligent systems.
3. To enable the students to understand the basic principles of Artificial Intelligence in various applications.
4. To introduce method and Techniques of Model Testing in ML
5. To give understanding of method and Techniques of Testing AI-based Systems

Module 1: Introduction to AI, Definition of AI and AI Effect., Narrow, General and Super A, AI-Based and Conventional Systems , AI Technologies, AI Development Frameworks.

Module 2: Testing AI-Based Systems Overview, Specification of AI-Based Systems, Test Levels for AI-Based Systems, Input Data Testing, ML Model Testing.

Module 3: Component Testing, Component Integration Testing, System Testing, Acceptance Testing, Test Data for Testing AI-based Systems. Testing for Automation Bias in AI-Based Systems .

Module 4: Testing for Concept Drift, Selecting a Test Approach for an ML System, Challenges Testing Self-Learning Systems, Testing Autonomous AI-Based Systems, Testing for Algorithmic, Sample and Inappropriate Bias, Methods and Techniques for the Testing of AI-Based Systems.

Text Book

1. Artificial Intelligence and Software Testing: Building systems by Adam Leon Smith (Author, Editor), James Davenport (Author).

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ADD ON COURSE NAME: AI Automation and Testing

DAY WISE SCHEDULE SHEET

Room No: A Block Seminar Hall-ASF-5

DAY	Topic	Timings	Duration
05-03-2019	**Topic 1:** Introduction to AI, Definition of AI and AI Effect., Narrow, General and Super A, AI-Based and Conventional Systems, AI Technologies, AI Development Frameworks.	09:00 am to 03:30 pm	6 hrs
06-03-2019	**Topic 2:** Testing AI-Based Systems Overview, Specification of AI-Based Systems, Test Levels for AI-Based Systems, Input Data Testing, ML Model Testing.	09:00 am to 03:30 pm	6 hrs
07-03-2019	**Topic 3:** Component Testing, Component Integration Testing, System Testing, Acceptance Testing, Test Data for Testing AI-based Systems, Testing for Automation Bias in AI-Based Systems.	09:00 am to 03:30 pm	6 hrs
08-03-2019	**Topic 4:** Testing for Concept Drift, Selecting a Test Approach for an ML System, Challenges Testing Self-Learning Systems, Testing Autonomous AI-Based Systems.	09:00 am to 03:30 pm	6 hrs
11-03-2019	**Topic 5:** Testing for Algorithmic, Sample and Inappropriate Bias, Methods and Techniques for the Testing of AI-Based Systems.	09:00 am to 03:30 pm	6 hrs
Total 30 hours			

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Date: 12-03-2019

ADDON COURSE REPORT

Name of the Add on Course: AI Automation and Testing

Day/Duration: 05-03-2019 to 11-03-2019 5(Days) - 30Hrs

Time: 09:00am to 03:30pm

Name of the Coordinator: Mr.Jagadeeshwar Reddy, Assistant Professor, Dept of CSE, VMTW.

Number of the Participants: 78

Topics covered: The following topics covered in this program

- AI-Based and Conventional Systems, AI Technologies, AI Development Frameworks.
- Testing AI-Based Systems Overview, Specification of AI-Based Systems, Test Levels for AI-Based Systems.
- Component Integration Testing, System Testing, Acceptance Testing, Test Data for Testing AI-based Systems, Testing for Automation Bias in AI-Based Systems.
- Selecting a Test Approach for an ML System, Challenges Testing Self-Learning Systems, Testing Autonomous AI-Based Systems, Testing for Algorithmic.
- Sample and Inappropriate Bias, Methods and Techniques for the Testing of AI-Based Systems.

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Understand the informed and uninformed problem types and apply search strategies to solve them	PO1	3
CO2	Apply difficult real life problems in a state space representation so as to solve them using AI techniques like searching and game playing	PO1,PO5	2
CO3	Design and evaluate intelligent expert models for perception and prediction from intelligent environment.	PO3,PO4	2
CO4	Examine the issues involved in knowledge bases,	PO2	3

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	reasoning systems and planning		
CO5	Solve basic AI based problems	PO4	4

Resource Person: Mrs. Indrani Vasireddy, Associate Professor, Department CSE, VMTW.

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VMTW/CSE/AOC/2018-2019/I/02   **Date:10-09-2018**

CIRCULAR

This is to inform all IV-I B. Tech CSE students that Add-On Course on "Internet of Things Using Rasberry Pi" is planned from 17-09-2018 to 24-09-2018.Therefore, interested students can fill out the registration form. The participating students will be honored with certificates of completion. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions.

HOD

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About the Course:

This course is all about IOT using Raspberry Pi. Currently, IOT is made up of a loose collection of different, purpose-built networks. Today's cars, intended for example, have multiple networks to control engine function, safety features, communication systems, and so on. Commercial and residential buildings also have various control systems for heating, venting, and air condition (HVAC), telephone service, security, and lighting. As IOT evolves, these networks, and a lot of others will be connected with additional security, analytics, and management capabilities. This will allow IOT to become even more powerful in what it can help people achieve. The Raspberry Pi is a popular choice when developing IoT products. It offers a complete Linux server with a tiny platform at an incredibly low price. The Internet of Things (IoT) is a system of interrelated computing devices, mechanical and digital machines, objects, animals or people that are provided with unique identifiers and the ability to transfer data over a network without requiring human-to human or human-to-computer interaction.

Course Objectives:

This Course focuses on hands-on IoT concepts such as sensing, actuation and communication. It covers the hands On Experience with Raspberry pi and development of Internet of Things (IoT) prototypes—including devices for sensing, actuation, processing, and communication—to help you develop skills and experiences. Raspberry Pi is equipped with numerous interfaces to interact with small electronic devices. This little wonder device can help learners of all age group to explore computing and even learn programming in languages like Python and Scratch. And if you thought if they were any less than the usual desktops, it's time to get facts cleared. It can do everything that a normal computer can- play song, play videos, make spreadsheets, word processing, browsing and much more. That's not all, it can also interact with the outside world which it has proved by its utility in music machines, weather stations, parent detectors and more. One of the major perks of Raspberry in the IoT space is its network connectivity. Majority of Raspberry Pi boards come with an in-built ethernet connector. Furthermore, the USB ports on it make is easy to connect WiFi dongle. And the Internet connection and power supply continuous required to work proper functioning and

periodically monitoring physiological parameter of body avoid the risk. Because sensor is sensing or collecting information after every 10 secs and sends it to database server.

Expected Outcomes:

It presents the basic level of IOT implementation, it involves remote control and remote sensing, which requires connectivity between the host and the remote devices. The "things" in IOT are connected via a Control Board, which acts as an interface, to the Raspberry Pi. The host (a computer or a mobile phone) and the remote Pi are connected via a network. The weaved services are used to interact with Raspberry Pi wirelessly. The application software on the host helps us to initiate a command. The application software on the Pi is designed to respond to these commands. It generates meaningful output that drives the Control Board, which in-turn drives the "things" that we intend to control. Operations like adjustment control, ON/OFF control and remote sensor's output acquisition are observed and controlled by application software and commands. IOT is aiming to take hold on the future, Implementing it involves bringing the "things" of our interest into the computer world and giving them power to communicate with us. Able to understand the application areas of IOT, to realize the revolution of Internet in Mobile Devices, Cloud & Sensor Networks and able to understand building blocks of Internet of Things and characteristics

Resource Person:
Mr. D.Anil Srikanth,
Design Engineer,
Texas Instruments.

Co-ordinator:
Mr. P. Vinay Bhushan,
Assistant Professor,
Department of CSE.
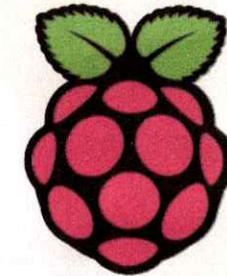Contact No: +91 7095 707 096

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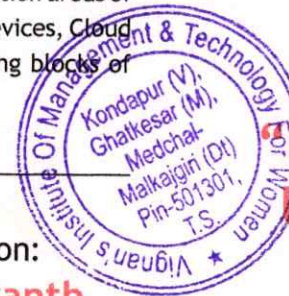Kondapur (V), Ghatkesar (M), Medchal - Malkajgiri (D) - 501 301

RaspberryPi

ADD-ON COURSE ON
"INTERNET OF THINGS USING RASBERRY PI"

17TH TO 24TH SEP, 2018

Duration of the Course: 36 Hrs

PRINCIPAL
Vignan's Institute of Management & Technology For Women
Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (Dt.)-501301
Telangana State

Venue:
'A'-Block Seminar Hall, VMTW.

Organized By:
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Last Date For Registration: 14th Sep, 2019

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Phone: +08615 200006/07/08   Email: info@vmtw.in

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Addon Course Syllabus: Internet of Things using Rasberry Pi

Course objectives:

1. To Understand the core components and technology behind the Internet of Things (IoT).
2. To Explore the basics of using a Raspberry Pi Operating System.
3. To Gain practical experience with installing, configuring and programming the Raspberry Pi board.
4. To Develop the skill to integrate Raspberry Pi components with networking and other external device.
5. To introduce various Raspberry Pi Models

Module 1: Overview of IoT Understanding IoT fundamentals Definition of the term Internet of Things (IoT) Technological trends leading to IoT IoT and its impact on society IoT Architecture and Protocols Real time examples of IoT.

Module 2: Definition of an embedded system in terms of its interface Components of an embedded system How embedded systems interact with the physical world.

Module 3: The core hardware components commonly used in IoT devices Sensors and Actuators.

Module 4: Introduction to Raspberry Pi Comparison of various Raspberry Pi Models Understanding SoC architecture and SoCs used in Raspberry Pi Pin description of Raspberry Pi On-board components of Raspberry Pi and their functions Projects using Raspberry Pi.

Text Books

1. Internet of Things with Raspberry Pi by Maneesh Rao, Publisher(s): Packt Publishing, ISBN: 9781788627405.
2. Internet of Things with Raspberry Pi and Arduino By Rajesh Singh, Anita Gehlot, Lovi Raj Gupta, Bhupendra Singh, Mahendra Swain.

COORDINATOR

HOD
Head of The Department
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

DAY WISE SCHEDULE SHEET

Addon Course Name: Internet of Things Using Raspberry pi

Room No: SEMINAR HALL-ASF-5

DAY	Topic	Timings	Duration
17-09-2018	Overview of IoT Understanding IoT fundamentals Definition of the term Internet of Things (IoT) Technological trends leading to IoT. IoT and its impact on society IoT Architecture and Protocols Real time examples of IoT.	09:00 am to 03:30 pm	6 hrs
18-09-2018	Definition of an embedded system in terms of its interface Components of an embedded system.	09:00 am to 03:30 pm	6 hrs
19-09-2018	The core hardware components commonly used in IoT devices Sensors and Actuators.	09:00 am to 03:30 pm	6 hrs
20-09-2018	Introduction to Raspberry Pi Comparison of various Raspberry Pi Models.	09:00 am to 03:30 pm	6 hrs
22-09-2018	Pin description of Raspberry Pi On-board components of Raspberry Pi and their functions Projects using Raspberry Pi.	09:00 am to 03:30 pm	6 hrs
24-09-2018	Understanding SoC architecture and SoCs used in Raspberry Pi. How embedded systems interact with the physical world.	09:00 am to 03:30 pm	6 hrs
Total 36 hours			

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Date: 26-09-2018

Addon course Report

Name of the Add on Course: Internet of Things Using Raspberry pi

Day/Duration: 17-09-2018 to 24-09-2018 6(Days) - 36Hrs

Resource Person: D.Anil Srikanth, Design Engineer, Texas Instruments.

Time: 09:00am to 03:30pm

Name of the Coordinator: Mr.P.Vinay Bhushan, Assistant Professor, Dept of CSE, VMTW.

Number of the Participants: 82

Topics covered: The following topics covered in this program

- Overview of IoT Understanding IoT fundamentals Definition of the term Internet of Things (IoT) Technological trends leading to IoT.
- interface Components of an embedded system How embedded systems interact with the physical world.
- The core hardware components commonly used in IoT devices Sensors and Actuators.
- Understanding SoC architecture and SoCs used in Raspberry Pi.

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Learn the basics of Internet of Things and its applications	PO1	3
CO2	Develop and test an IoT weather monitoring station	PO3	2
CO3	Understand Raspbian OS, Python programming, SMTP and API	PO1	4
CO4	Build your computer using Raspberry Pi platform	PO1,PO3	3
CO5	Develop the knowledge to set up the Raspberry Pi Board and access via network	PO3,PO4	5

COORDINATOR

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

VMTW/CSE/AOC/2018-2019/II/04

26-02-2019

CIRCULAR

This is to inform all the III-II B. Tech CSE students that there will be an Add on course on **"Advanced Computing with Python programming Language"** from 4/3/2019 **to** 8/3/2019 by T.Karthik Chandra, RPA Developer, Ericom Software Solutions. All students are invited to take part in this course. The participating students will be honored with certificates of participation. During the course you will be encouraged to interact with the speaker and the other participants through the case study discussions. So those who are interested can sign in registration form.

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The Principal, for kind information

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Notice board

III-II B.Tech Students

Dept. file

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Telangana State

About the Course:

Python is one of the most popular and widely used programming languages in the world. In fact, Python has surpassed Java to become the first choice for developers in the data science, AI and machine learning market. It is easy to use and versatile. Tech giants like Google, Facebook, and Netflix use Python to generate insights from data and build ground-breaking products. It is a comprehensive training program that introduces learners to the Python development language, the essentials of object-oriented programming, and web development. The purpose of this program is to teach the student how to program in Python and prepare for a career in data science, AI, machine learning, and deep learning. Advanced concepts like connecting to databases, interacting with networks and handling concurrency are also discussed to give the learners a holistic view of Python development capabilities.

Course Objectives:

In this course, you will learn about Programming in Python, implementing data types, statements, operators and strings, and Implementing the OOPs concept in Python. Creating GUI applications using Python Connecting with databases Implementing TCP socket connectivity. This course is tailored for students who have completed the introductory course of Python and want to advance further in it. Students will learn about complex data structures, and functions and be introduced to Object Oriented Programming. Students learn to design, build, and debug programs – skills that any programmer needs to solve real-world problems. When students complete Intro to Programming with Python, they will be able to Build basic programs using fundamental programming constructs like variables, conditional logic, looping, and functions. Work with user input to create fun and interactive programs. Create simple games with images, animations, and audio using our custom beginner-friendly programming library, Wizardlib.

Expected Outcomes:

The basic fundamentals to Learn about file handling to Learn advanced concepts like OOPs (Object Oriented Programming), Inheritance, Polymorphism, etc. Learn python libraries like NumPy, Matplotlib, Pandas, etc. In this module, students practice adding and positioning images and text on a webpage using Python and our custom Wizard lib library. They can start making small custom projects that use pictures and text. The able to learn some built-in Python functions for printing text and getting user input, along with two ways to combine variables and strings. They use these skills to create an interactive quiz app that tracks correct answers and displays unique messages if you win or lose. The students spend some more time defining and using functions that they authored. They also learn to update text, modify global variables, and make comparisons between two values. Students can get technology-oriented knowledge and the ability to develop creative solutions, and better understand the effects of future developments of computer systems and technology on people and society as a whole. They prepare for placement by developing personality and soft skills. Gain the ability to apply knowledge of Computer Science to real-world issues.

Resource Person:
Mr. T. Karthik Chandra,
RPA Developer.

Co-ordinator:
Ms. Deepthisri Dakuri,
Assistant Professor,
Department of CSE.
Contact No: +91 9059 640 840
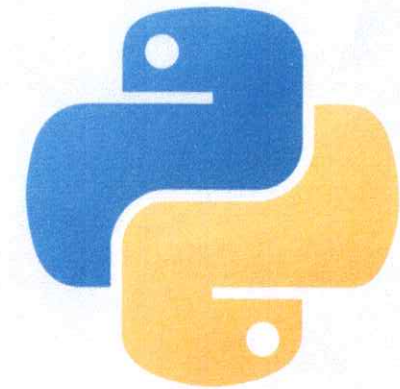
Last Date For Registration: 2nd March, 2019

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ADD-ON COURSE ON
ADVANCE COMPUTING WITH PYTHON PROGRAMMING LANGUAGE'
4TH TO 8TH MAR, 2019

Duration of the Course: 32 Hrs

Venue:
PRINCIPAL
Vignan's Institute of Management & Technology For Women
Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (Dt)-501301
Telangana State
'A'-Block Seminar Hall, VIMTW.

Organized By:
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Addon Course Syllabus

Addon Course: Advanced Computing with Python programming Language

Course objectives:

1. To learn about reading, writing and implementing other operation on files in Python.
2. To implement threading concept and multithreading on Python.
3. To design GUI Programs and implement database interaction using Python.
4. To know about use of regular expression and handling exceptions for writing robust python programs
5. To Understand the Concept of Server-Client Architecture

Module 1: Working with files: Files, opening and closing a file, working with text files containing strings, knowing whether a file exists or not, working with binary files, the „with" statement, the seek() and tell() methods, random accessing of binary files.

Module 2: Database in python: Using SQL with python, retrieving rows from a table, inserting rows into a table, deleting rows from a table, updating rows in a table, creating database tables through python, Exception handling in databases.

Module 3: Exceptions in python: Errors in a python program, compile & run-time errors, logical error, exceptions-exception handling,

Module 4: Types of exceptions, the except block, the assert statement, user-defined exceptions, logging the exceptions

Module 5: Networking: Protocols, server-client architecture, tcp/ip and udp communication.

TEXT BOOKS

1. Advanced Python Programming: Build high performance, concurrent, and multi-threaded apps with Python using proven design patterns by Sakis Kasampalis Quan Nguyen Dr Gabriele Lanaro Dr. Gabriele Lanaro (Author).

COORDINATOR

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Telangana State

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Addon Course Name: Advance Computing With Python Programming Language

DAY WISE SCHEDULE SHEET

Room No: A Block Seminar Hall-ASF-5

DAY	Topic	Timings	Duration
04-03-2019	Files, opening and closing a file, working with text files containing strings, knowing whether a file exists or not, working with binary files, random accessing of binary files, zipping and unzipping files, working with directories.	09:00 am to 03:30 pm	6 hrs
05-03-2019	Using SQL with python, retrieving rows from a table, inserting rows into a table, deleting rows from a table.	09:00 am to 03:30 pm	6 hrs
06-03-2019	Updating rows in a table, creating database tables through python, Exception handling in databases.	09:00 am to 03:30 pm	6 hrs
07-03-2019	Errors in a python program, compile & run-time errors, logical error, exceptions-exception handling, types of exceptions, the except block, the assert statement, user-defined exceptions, logging the exceptions.	09:00 am to 04:30 pm	7 hrs
08-03-2019	Networking Protocols, server-client architecture, tcp/ip and udp communication.	09:00 am to 04:30 pm	7 hrs
Total 32 hours			

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Date: 13-03-2019

PROGRAM REPORT

Name of the Add on Course: Advance Computing With Python Programming Language

Day/Duration: 04-03-2019 to 08-03-2019 5(Days) - 32Hrs.

Resource Person: T.Karthik Chandra , RPA Developer, Ericom Software Solutions.

Time: 09:00am to 04:30pm

Name of the Coordinator: Mrs. Deepthi Sri Dakuri, Assistant Professor, Dept of CSE.

Number of the Participants: 81

Topics covered: The following topics covered in this program

- Files, opening and closing a file, working with text files containing strings, knowing whether a file exists or not, working with binary files.
- Using SQL with python, retrieving rows from a table, inserting rows into a table, deleting rows from a table, updating rows in a table, creating database tables through python, Exception handling in databases.
- Errors in a python program, compile & run-time errors, logical error, exceptions-exception handling, types of exceptions, the except block
- Protocols, server-client architecture, tcp/ip and udp communication.

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	Ability to work with files and perform operations on it using Python.	PO2	4
CO2	Ability to implement regular expression and concept of threads for developing efficient program.	PO2,PO3	3
CO3	Knowledge of working with databases, designing GUI in Python and implement networking in Python	PO1,PO3	2
CO4	Illustrate the Object-oriented Programming concepts in Python.	PO1	4

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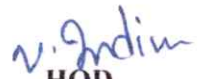Telangana State

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CO5	Demonstrate the basic database design for storing data as part of a multi-step data gathering, analysis, and processing	PO2	4

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