VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

BROCHURES and REPORT

OF

FDP/PROFESSIONAL DEVELOPMENT / ADMINISTRATIVE TRAINING PROGRAMMES ORGANIZED BY INSTITUTE

(2018-2019)

Principal
Vignan's Institute of Management & Technology For Women
Kondapur (V), Ghatkesar (M), Medchal-Malkajgiri (Dt)-501301,
Telangana State

VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

Sponsored by Lavu Educational Society, Approved by AICTE, New Delhi & Affiliated to JNTUH, Hyderabad.
Kondapur (V), Ghatkesar (M), Medchal - Malkajgiri (D) - 501 301 TS INDIA
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ACADEMIC YEAR 2018-19

Sl. No	Title of the FDP /MDP/ professional development / administrative training program	Dates (from-to) (DD-MM-YYYY)	No. of days
1	ONLINE FDP ON "MACHINE LEARNING AND DEEP LEARNING CONCEPTS USING PYTHON"	18-12-2018 to 23-12-2018	One Week
2	ONLINE FDP ON "CMOS MIXED SIGNAL DESIGN".	07-01-2019 to 11-01-2019	One Week
3	A FIVE DAY WORKSHOP ON EFFECTIVE COMMUNICATION SKILLS	16-07-2018 to 20-07-2018	5 Days

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Registration Form

Name: ....

Date of Birth & Age: ....

Gender (M/F): ....

Qualifications: ....

A). Institution: ....

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B). Affiliating University: ....

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Phone No (2): ....

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Email: ....

Resource Person:
Dr. P.S. Murthy,
Professor in CSE,
University College of Science, ANU, Guntur.

Ch. Aparna,
Professor in CSE,
RVR & JC College of Engineering, Guntur.

Chief-patrons:
Dr. L. Rathaiah,
Chairman, Vignan Group of Institutions.

Mr. B. Shravan,
CEO, Vignan Group of Institutions

Patron:
Dr. P. Sudhakara Rao,
Principal, VMTW

Mrs. Indrani Vasireddy,
Associate Professor & HOD, CSE.

Co-ordinator:
Mrs. D. Swaroopa,
Assistant Professor, CSE.

Faculty Co-ordinator:
Mrs. A. Amara Jyothi, Assistant Professor, CSE.
Mr. Ravi Surarapu, Assistant Professor, CSE.
Mr. Nagaraju Narapogu, Assistant Professor, CSE.
Mrs. Veena Chenigaram, Assistant Professor, CSE.

For Registration:
Mr. Nitesh Vengala, Assistant Professor, CSE.
Contact No: +91 8801179588

Last Date For Registration: 14th Dec, 2018

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FACULTY DEVELOPMENT PROGRAM
"MACHINE LEARNING AND DEEP LEARNING CONCEPTS USING PYTHON"
18TH TO 23RD DEC, 2018

Venue:
'A'-Block Seminar Hall, VMTW.

Organized By:
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

About the Institution:
Vignan's Institute of Management and Technology for Women (VMTW), is the brainchild of Dr. L. Rathaiah, Chairman, Vignan Group of Institutions, which was founded in August 2008, with four branches of Engineering.

VMTW is an exemplary institution of higher learning with a mission of pursuing excellence in education and research. The institution, with its diverse and dynamic community of about 2000 students offers a distinctive combination of some of the finest facilities for 7 different graduate, and undergraduate programs, accomplished faculty, world-class facilities with a hostel set on a sprawling area of 22 acres sylvan surroundings of mango groves and greenery.

While students at VMTW immerse themselves in academics, the college has a lot in store for them outside the classroom. Student life includes participation in sports, recreational & co-curricular, and cultural activities. In short, at VMTW students will find an academic and social environment where everyone from faculty members to peers helps shape their future.

VMTW is a home to aesthetically designed buildings with state-of-the-art computer and internet facilities, modern laboratories, workshops, seminar halls, auditoriums, and well-stocked libraries, sports, and games fields.

The Institution boasts of a strong alumni network with alumni events held every year serving as a platform for past students to give back to VMTW and share their experiences with its present fellow students. With so much to offer, it is only natural that students of VMTW get a unique opportunity to carve a niche for themselves in their chosen field of study that enables them to become well-rounded and discerning citizens, fully qualified for their chosen professions in the workplace. Institute also brings out the creativity in students by imparting engineering knowledge imbibing interpersonal skills to promote innovation, research, and entrepreneurship.

About the Department:
Department of CSE, VMTW has started B.Tech in Computer Science and Engineering in the year 2008 with an intake of 90. The scholarly ambiance in CSE Department is encouraging to the complete development of the students with well-equipped labs. Department has dedicated and professionally well competent faculty and the many of the faculty are pursuing Ph.D. in emerging areas like Artificial Intelligence, Machine Learning, Cyber Security, Data Science and Block Chain. The main objective is to prepare the students to be successful in Computer Science Engineer to meet the industry needs, encourage lifelong skills and strengthen research.

About the Program:
The goal of this Programme is to educate and enhance knowledge on Python programming along with current research studies and challenges as well as practical experiences in the field of Deep Learning. All participants will gain understanding on enhancement from machine learning to Deep Learning. Exposure to some most practical and applicable Deep learning techniques. Hands on Python programming in conjunction with deep learning .Faculties will learn concepts of neural networks. They will be educated to implement Multilayer Perceptron, Covolutional Neural networks using python.

Objectives:
Learn the concept, syntax, semantics and gain introductory knowledge in Machine Learning principles through examples that provide useful exposure to the various concepts of Machine Learning.

- Machine Learning-Introduction
- Machine Learning-Fundamental
- Python-Basics
- Core Concepts of Python (Required for ML Practical Implementation)
- Steps of Machine Learning Implementations
- Data Collection for Machine Learning
- Concept of Supervise & Unsupervised Machine Learning
- Data Visualization for Machine Learning using Matplotlib
- Practical implementation of Supervised Machine Learning Algorithm
- Practical implementation of Unsupervised Machine Learning Algorithm

Expected Outcomes:
At the end, participants will be able to:

- To provide foundation of Python scripting and its principles.
- To design and implement real-world inspired Machine Learning algorithm applications.
- To teach high-value forecasts that can direct smarter decisions and smart behavior in real-time without human interference.
- To provide insights about the roles of a data scientist and enable you to analyse big data.
- To teach techniques and tools for transformation of data, understand data mining and data visualization and optimization.
- To familiarize you with different formats and make you figure out Machine Learning algorithms

Focus Areas:
At the end, participants will be able to:

- Introduction to packages in Python
- Tuples, Set, Dictionaries & Functions
- Numpy, Scipy, Pandas, EDA
- Supervised and Unsupervised Learning
- Statistics, Hypothesis Testing, Distributions
- Association Rule Mining and Recommendation System
- Linear Regression, Logistic Regression
- Decision Tree, Random Forest
- Naive Bayes Classifier, Support Vector Machines
- Basics of Neural Network and Deep Learning
- Server Deployment using Flask

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Resource Person:
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Professor, VFSTR,

Dr. T. Pitchaiah,
Professor, VFSTR,

Dr. Shaik Jakeer Hussain,
Professor, VFSTR,

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CEO,
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Last Date For Registration: 4ᵗʰ Jan, 2019

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FACULTY DEVELOPMENT PROGRAM
"CMOS MIXED SIGNAL DESIGN"
7ᵀᴴ TO 11ᵀᴴ JAN, 2019

Venue:
**'A'-Block Seminar Hall,
VMTW.**

Organized By:
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

About the Institution:

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About the Department:

Department of Electronics and Communication Engineering was started since the inception of Vignan's Institute of Management and Technology for Women during 2008 with an initial intake of 90. The strength was enhanced to 120 during 2010. The Department had added Post graduate programme in VLSI during the year 2011 and Embedded Systems during 2014 with an intake of 18 each.

The Department has state-of-art laboratories equipped with advanced and well maintained equipment, continuously updated application software packages, more than adequate computing systems with 24x7, 30 MBPS internet facility.

About the Program:

5G network is the fifth-generation cellular network technology. It is more efficient compared to the first, second, third and fourth generation technology networks for they easily get congested with information and slow down the desired speed by the users. It suffices to say that the 5G network surmounts all the challenges faced by the networks before it since the beginning of the new millennium in 2000. The 5G technology has both its opportunities and attendant challenges.

Artificial intelligence is the trending technology of the future. Machine learning is an application of Artificial intelligence that provides systems the ability to automatically learn and improve from experience without being explicitly programmed. With the expansion of volumes and complexity of data AI and machine learning are used for its processing and analyzing. Industries apply AI and ML to improve performance and propel product development.

This program focuses on exploring various research Opportunities and challenges in the field of mixed signal IC design. Mixed-signal ICs are integrated circuits that contain both analog and digital circuitry on one chip. An analog signal is a continuous time-varying signal, and a digital signal is a noncontinuous signal that takes on only a finite number of values. Mixed signal ICs make use of both types of signals. Mixed-signal ICs are used in a wide variety of applications and application-specific integrated circuits (ASICs). The primary goal of the FDP is to bring awareness on CADENCE for mixed signal IC designing and to promote research and development activities and learn the advanced topics used in designing of VLSI.

Course Objectives:
- Overview of IC Design
- Aspects of IC Design
- Introduction to device characteristics
- Biasing styles Single-Stage Amplifiers
- Cascode Amplifier Gain Boosting Techniques
- Differential amplifiers and Current Mirrors
- Frequency Response of amplifiers and Noise
- Op-Amp Design and Compensation
- Mixed Signal Design / RF IC Design

Expected Outcomes:
- Mixed-Signal and RF Design Platforms
- Mixed Signal and RF Verification
- CMOS Data Converters – Analog-to-Digital Converter (ADC)/Digital-to-Analog Converter (DAC)
- Analog Layout Generation EDA Tool
- Analog Front-End Circuit (AFE)
- Analog/Mixed-Signal IC Design

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Resource Person:
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Assistant Professor,
Department of BS&H.

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Mr. B. Shravan,
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Vignan Group of Institutions

Patron:
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Principal, VMTW

Mrs. Indrani Vasireddy,
Associate Professor &
Head of the Department,
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Last Date For Registration: 14ᵗʰ July, 2018

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A FIVE-DAY WORKSHOP ON
"EFFECTIVE COMMUNICATION SKILLS"
16ᵀᴴ TO 20ᵀᴴ JULY, 2018

Venue:
**'A'-Block Seminar Hall,
VMTW.**

Organized By:
**DEPARTMENT OF
COMPUTER SCIENCE AND ENGINEERING**

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About the Program:

The aim of technical education is human resource development in specific professional areas of which communication is an important part. The acquisition of this facility would accelerate the process and undoubtedly enrich the language education. Grammar is to a language what management is to an organization.
In writing a composition or an oral presentation, the primary requirement is to ensure that the sentences you generate are acceptable from the grammatical and usage point of view. Grammar and usage together constitute a body of rules and practices. It is necessary to learn these rules and practices for acquiring a command over its spoken and written forms. This program illustrates the application of grammar at various levels through examples. The linguistic proficiency of the learner is bound to improve, if he goes through them carefully.

Objectives:

* To develop a clear perception on the basic concepts of grammatical accuracy.

* To prompt the learner to acquire proficiency in writing various forms of communication in flawless and acceptable English.

* Understand the basic difficulty in writing clearly, simply and correctly.

* Identify the different purposes of writing.

* Know the essential principles of effective written communication.

* Plan your written communication by paying special attention to the needs and expectations of your readers.

Expected Outcomes:

At the end, participants will be able to:

* To analyze the theoretical skills and strategies to be an effective communicator.

* To reflect with greater confidence and create positive impression in the classroom.

* To develop a learning culture to enrich knowledge where communication is both receptive and expressive.

* To improve the art of communication by precision in thought and expression.

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Report of Events Organized

1. Online FDP on "Machine Learning and Deep Learning Concepts Using Python"

One Week FDP on "Machine Learning and Deep Learning Concepts Using Python" conducted by Department of Computer Science and Engineering from 18-12-2018 to 23-12-2018. The resource persons for this program are Dr. P.S. Murthy, Professor in CSE, University College of Science, ANU, Guntur and Dr. Ch. Aparna, Professor in CSE, RVR & JC College of Engineering, Guntur. They have been explained the detail about the Introduction-Data Science, ML, DL & AI, Concept of Supervise & Unsupervised Machine Learning, Practical implementation of Supervised Machine Learning Algorithm, Practical implementation of Unsupervised Machine Learning Algorithm, Introduction To Deep Learning and Neural Networks and Deep dive into Neural Networks. Total 50 certificates were issued for this FDP.

2. Online FDP on "CMOS Mixed Signal Design".

Five days FDP on "CMOS Mixed Signal Design" was organised by Department of Electronics and Communication Engineering from 07-01-2019 to 11-01-2019. DR B. Seetha Ramanjaneyulu, Professor, VFSTR, Dr T. Pitchaiah, Professor, VFSTR, Dr. Shaik Jakeer Hussain, Professor, VFSTR, Guntur Delivered the talk and conducted hands on sessions. The resource person has explained in detail about the recent trends on CMOS Mixed Signal Design and its concepts. The same concepts practiced with real time problems Mentor Graphics tool. Total 54 certificates were issued for this FDP.

3. A Five-Day Workshop On Effective Communication Skills

Department of Basic Science & Humanities conducted workshop **on – "Effective Communication Skills"**, during 16-07-2018 to 20-07-2018 for Non – Teaching Faculty,

The main objective of this program is to provide deep knowledge in Communication Skills.

- To analyze the theoretical skills and strategies to be an effective communicator.
- To reflect with greater confidence and create positive impression in the classroom.
- To develop a learning culture to enrich knowledge where communication is both receptive and expressive

Total 30 certificates were issued for this workshop. **Mrs.SuzhanShalini,** acted as the resource person for this five day workshop session.

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