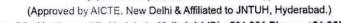
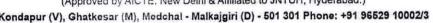
VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

[Sponsored by Lavu Educational Society]







Department of Electronics and Communication Engineering

VMTW/ ECE/ ICT/CIR/2019-20/ 11/09

Dt. 30-12-2019

CIRCULAR

All the B.Tech 4th year ECE students here by requested that they have Training program "Low Power VLSI Design Using Mentor Graphic EDA Tools" from 10:00 AM to 4:00 PM.

Date: 03-01-2020 to 04-01-2020

Venue: Seminar Hall

Coordinator: Dr. Samiran Chatterjee

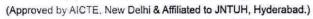
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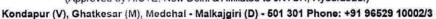
- 1. The Principal, for his kind information.
- 2. All Department HOD's
- 3. IQAC.
- 4. Circulate to all Class Rooms
- 5. All Notice Boards
- 6. Office File

PRINCIPAL Vignan's Institute of Management & Technology For Women Kondapur (V), Ghatkesar (M), Medchal Malkajgiri (Dt)-501301 Telangana State

VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

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Department of Electronics and Communication Engineering

Training program "Low Power VLSI Design using Mentor Graphic EDA Tools"

Schedule of the Program

DATE	TIME SCHEDULE	PROGRAMME
03-01-2020	10:00 AM to 10:30 AM	Guest introduction by HOD
	10:30 AM to 11:15 AM	Introduction
	11:15 AM to 11:30AM	Break
	11:30 AM to 1:00 PM	VLSI Circuit Design,
	01:00 PM to 01:30 PM	Lunch Break
	01:30PM to 04:00PM	EDA tools,
04-01-2020	10:00 AM to 10:30 AM	Cadence Virtuoso, Synopsys,
	10:30 AM to 11:15 AM	Mentor Graphics,
	11:15 AM to 11:30AM	Break
	11:30 AM to 01:00 PM	Xilinx, Tanner,
	01:00 PM to 01:30 PM	Lunch Break
	1:30PM to 4:00PM	Mentor Graphics, Electric, Silvaco, Glade, Alliance

Coordinator

Kondapur (V) Ohalkasar (M) Medahal Makaigin (DI) Fin-504301, T.S. 1840

PRINCIPAL

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Telangaga State