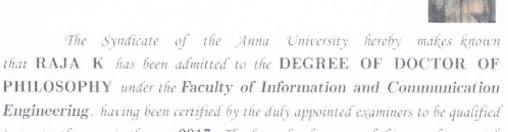


Reg.No.11260531055/RG



PHILOSOPHY under the Faculty of Information and Communication Engineering, having been certified by the duly appointed examiners to be qualified to receive the same in the year 2017. The degree has been awarded in compliance with the "University Grants Commission, Regulations 2009".

Title of the Thesis:

A NOVEL FLIP-FLOP BASED ERROR FREE AREA EFFICIENT AND LOW POWER PIPELINE ARCHITECTURE FOR FINITE IMPULSE RECURSIVE SYSTEM



Given under the seal of the University

Chennai 600025

Controller of Examinations



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