



VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

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Kondapur (V), Ghatkesar (M), Medchal - Malkajiri (D) - 501 301 Phone: +91 96529 10002/3



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 02 -11- 2021

PROGRAM REPORT

Name of the Add on Course: Modern Chip Design and Its Applications

Day/Duration: 33 hours

Time: 4:00pm to 5:00pm

Resource Person: Mr. I.Shankar Rao, Elegant Embedded Solutions Pvt. Ltd. Hyderabad

Name of the Coordinator: Mr.T.Pullaiiah, Associate Professor, VMTW, Hyderabad

Number of the Participants:100

Topics covered: The following topics covered in this program

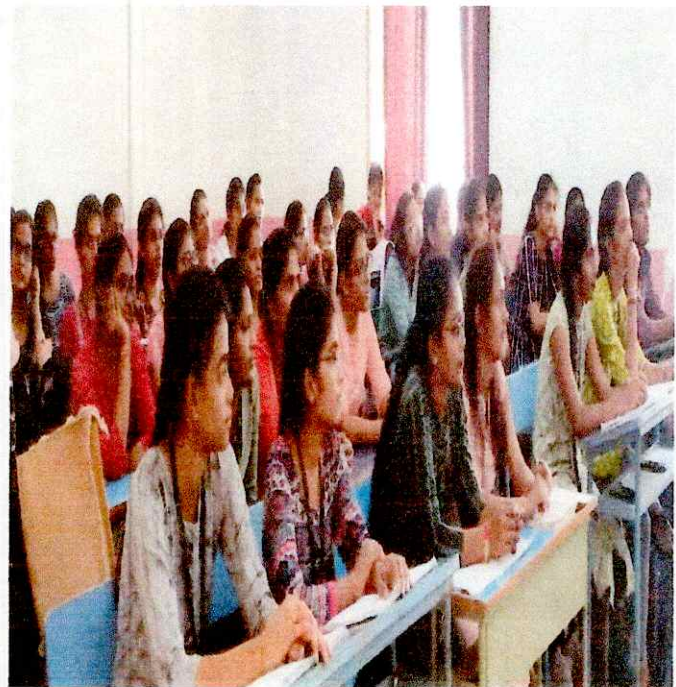
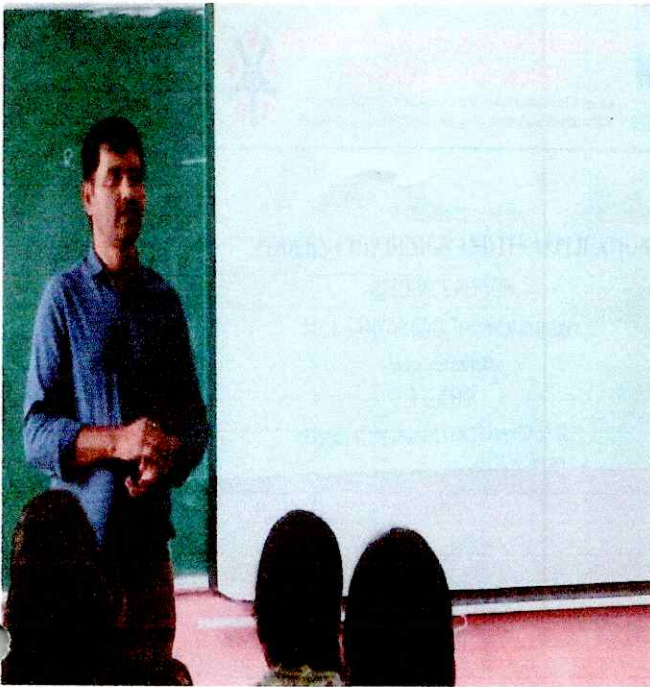
- ❖ Fabrications of ICs
- ❖ MOS/CMOS/BiCMOS
- ❖ Counters
- ❖ Programmable Logic Devices
- ❖ CMOS Testing

Course Outcomes:

COs	At the end of the course, students will have the ability to:	Pos Mapped	Strength of mapping
CO1	To Learn basic programmable logic devices and testing of CMOS circuits	PO2,PO4	2
CO2	To Understand the electrical properties of MOS and BiCMOS devices	PO3,PO5	3
CO3	Give exposure to different steps involved in the fabrication of ICs	PO2,PO4,PO7	2
CO4	Understand basic programmable logic devices and testing of CMOS circuits	PO3,PO5	3
CO5	Learn chip level Test Techniques	PO4,PO7	1




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Telangana State



Modern Chip Design and Its Applications: Add on course explaining by Mr. I.Shankar Rao, Elegant Embedded Solutions Pvt.Ltd, Hyderabad

By having such a wonderful event students are able to understand chip designs, and are able to design chips ,and understood VLSI basic concepts,and know how to Design and Analysis of modern chips . So, students are thankful to the department for conducting such type of event and they also want this event to be held further more.

Assessment Procedure: The assessment of the Add on course is conducted in Multiple Choice Questions through offline Mode.

COORDINATOR

HOD

Head of the Department
Electronics and Communication Engineering
Vignan's Institute of Management & Technology For Women
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