



List of External Grievances

S.No.	Description	Page No.
1	Sample Copy M.Tech Panel Uploading Error	1-5



Examination Branch VMTW <exams@vmtw.in>

UP_VMTW_M.TECH PANEL UPLOADING ERROR

3 messages

Examination Branch VMTW <exams@vmtw.in>

9 October 2023 at 12:56

To: support.registrations@jntuh.ac.in

Jawaharlal Nehru Technological University Hyderabad
Kukatpally, Hyderabad - 500 085, Telangana, India
Project Panels Services

VignanIC™ Institute of Management AND Technology for Women(UP) - (M.Tech_September_2023)

Registration Reports View DE Approved Panel Account

- **ERROR IN INSERTION ...:COULD NOT EXECUTE JOBC BATCH UPDATE**
- **DATA SAVED SUCCESSFULLY**

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Kukatpally, Hyderabad - 500 085, Telangana, India
Project Panels Services

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- **ERROR IN INSERTION ...:NULL**
- **DATA SAVED SUCCESSFULLY**

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support.registrations support.registrations <support.registrations@jntuh.ac.in>

9 October 2023 at 14:44

To: Examination Branch VMTW <exams@vmtw.in>

SEND ME HTNO?

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Examination Branch VMTW <exams@vmtw.in>

9 October 2023 at 16:26

To: "support.registrations support.registrations" <support.registrations@jntuh.ac.in>

Jawaharlal Nehru Technological University Hyderabad
Kukatpally, Hyderabad - 500 085, Telangana, India
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Uploaded Plagiarism Details

*If NotAccepted / GrammarCheck_Disqualified Please Click On Haltticket No and Download Report

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 - * Replacement of 1(one) with l (letter l)
 - * Replacement of 0(zero) with o(letter o)
- Some of the text matter was saved in image format Hence the dissertations of PG students are failed in plagiarism check.

BRANCH CODE	HTNO	PROJECT THESIS TITLE	EMAIL ID	PLAGIARISM STATUS	% OF SIMILARITY	THESIS UPLOAD DATE
57	21UP197011	DESIGN AND VERIFICATION OF LOW POWER HIGH SPEED VOLTAGE LEVEL SHIFTER BASED ON PAISS TRANSISTOR METHODOLOGY	principal@vmtw.in	Notifying	0%	9/25/23 2:44:35 PM 0.000
57	21UP19702	DESIGN CMOS LOWPOWER HIGH SPEED DIGITAL 4 BIT COUNTER AT 450NM TECHNOLOGY	principal@vmtw.in	Notifying	0%	9/25/23 4:10:17 PM 0.000
57	21UP19703	High speed low power analysis of mixed logic line driver using 450NM technology	principal@vmtw.in	Notifying	0%	9/25/23 2:45:50 PM 0.000
57	21UP19705	DESIGN AND VALIDATION OF HIGH PERFORMANCE 10T SRAM WITH HIGH RECOVERABILITY OF MULTINODE SOFT ERROR	principal@vmtw.in	Notifying	0%	9/25/23 2:45:40 PM 0.000
57	21UP19708	Design and Validation of Low Power High Speed area effective comparator using CMOS technology	principal@vmtw.in	Notifying	0%	9/25/23 2:46:19 PM 0.000

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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD-500 085

PROCEEDINGS OF THE DIRECTOR OF UNIVERSITY EXAMINATIONS

Procs.No.JNTUH/EB/B.Tech/VV/2023, Dt:25.6.2023

Sub: JNTUH - Examinaton Branch-Appointment of External Examiner for conduct of B.Tech Project Viva-Voce Exam -Orders-Issued.

ORDER

The Principal, Vignan Inst. Of Management & Tech for Women, has sent the panel of external examiners for conducting stage-II-project viva-voce examination for B.Tech. IV - II(R18 Regulation) Regular/supply and (R16,R15,R13 Regulation) major project viva-voce supply students. IV - II Minor Degree Program Mini Project, and IV-I(R18,R16 Regulation) Mini-project viva-voce supply students. They have requested the University to issue necessary orders for appointing the external examiners. The following is the list of examiners appointed to conduct the above mentioned project viva-voce examinations.

Name Of The Course	Approved External Examiner
B.Tech(ECE) (SECTION : 1) Panel_Id : UP_A_04_1_36	KANAGALA SATEESH Associate Professor ECE Sree Nidhi Inst. of Science & Tech sateeshkumarkanagala@gmail.com 8500242799
B.Tech(ECE) (SECTION : 2) Panel_Id : UP_A_04_2_36	Dr K Anitha Sheela Professor ECE JNTUHUCEH kanithasheela@jntuh.ac.in 9441189219
B.Tech(CSE) (SECTION : 1) Panel_Id : UP_A_05_1_36	MADHUKAR GUNDA Assistant Professor CSE CMR Technical Campus madhu.mani536@gmail.com 9246456780
B.Tech(CSE) (SECTION : 2) Panel_Id : UP_A_05_2_36	Dr. B. Sateesh Kumar Professor CSE JNTUHCE JAGTIAL sateeshbkumar@gmail.com 9494668833

B.Tech(CSE) (SECTION : 3) Panel_Id : UP_A_05_3_36	GAUTAMI LATHA ANGA Assistant Professor CSE Sridevi Womens Engg College gauthamilatha@gmail.com 9490228309
B.Tech(IT) Panel_Id : UP_A_12_1_36	Dr. B. Vishnu Vardhan Professor CSE JNTUHCE JAGTIAL mailvishnu@jntuh.ac.in 9848435073

The Principal is requested to arrange for conduct of Project viva-voce examination before the due date mentioned in the notification and upload the project viva-voce awarded marks to the JNTUH University Examination Registrations Portal on the same day. Take the printout of system generated marks report and get it signed with internal & external examiners and submit the same to DE/JNTUH.

TO
THE PRINCIPAL
Vignan Inst. Of Management & Tech for
Women

K. Venkateswara Rao
DIRECTOR OF UNIVERSITY EXAMINATIONS



Examination Branch VMTW <exams@vmtw.in>

UP_VMTW-REPLACEMENT OF External Examiner for conduct of B.Tech Project Viva-Voce Exam

6 messages

Examination Branch VMTW <exams@vmtw.in>

26 June 2023 at 15:44

To: support.registrations@jntuh.ac.in

Dear Sir/Madam,

we are here by inform that the below faculty not coming to 4-2 B.tech Project panel so kindly request you to replace the external examiner.the details are given below.

College code-UP

B.Tech(ECE) (SECTION : 2)

Panel_Id : UP_A_04_2_36

Dr K Anitha Sheela

Professor

ECE

JNTUHUCEH

kanithasheela@jntuh.ac.in

9441189219

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Examination Branch VMTW <exams@vmtw.in>

27 June 2023 at 10:05

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Examination Branch VMTW <exams@vmtw.in>

27 June 2023 at 17:31

To: support.registrations@jntuh.ac.in

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Examination Branch VMTW <exams@vmtw.in>

28 June 2023 at 12:13

To: support.registrations@jntuh.ac.in

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Examination Branch VMTW <exams@vmtw.in>

29 June 2023 at 13:47

To: support.registrations@jntuh.ac.in

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From: **Examination Branch VMTW** <exams@vmtw.in>

Date: Mon, Jun 26, 2023 at 3:44 PM

Subject: UP_VMTW-REPLACEMENT OF External Examiner for conduct of B.Tech Project Viva-Voce Exam

To: <support.registrations@jntuh.ac.in>

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 **UP_B.Tech_June_2023_Projectpanels_Reports.pdf**
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Examination Branch VMTW <exams@vmtw.in>

30 June 2023 at 10:51

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