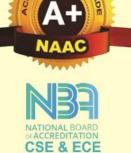
VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

(An Autonomous Institution) (Affiliated to JNTU, Hyderabad)

[Sonsored by Lavu Educational Society, Affiliated to JNTUH & Aproved by AICTE, New Delhi] Kondapur (V), Ghatkesar (M), Medchal - Malkajgiri (D) - 201 301, Phone: 9652910002/3



Department of Electronics and Communication Engineering Organizes

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About the Conference

This International Conference on Advancement in VLSI and Signal Processing (VMTW-ICAVSP-2K24) is being organized by Department of Electronics and Communication Engineering. VMTW-ICAVSP-2K24 will provide an international forum for researches, academicians, industry professional and UG/PG Students to explore and contribute their research by presenting and discussing the various aspects of developments in the field of Advancements in VLSI and Signal Processing.

Topics of the Conference

VLSI

- Advanced Semiconductor Technologies
- Low-Power Design
- High-Performance Computing
 Analog and Mixed-Signal Design
- Design Automation and Tools
- > Reliability and Testing
- System-on-Chip (SoC) Design
- Quantum Computing
- Neuromorphic Computing
 Photonics and Optical
- Computing > Design for Manufacturing

Conference Advisory Committee

Dr. Xiao-zhiGao, Professor,Univ. of Eastern Finland Dr. D. Praveen Kumar, Prof., Stockholm Univ., Sweden Dr. P.V. Naganjaneyulu, Prof., Mizoram Central Univ. Dr. P. Chandrasekhar Reddy, Prof., ECE, JNTU, Hyd. Dr. B. Vishnu Vardhan, Prof., CSE, JNTU, Hyd. Dr. N. Ramakrishnaiah, Prof., CSE, JNTU, Kakinada Dr. K.ShahuChatrapati, Prof., CSE, JNTU, Kakinada Dr. A. Chandra Sekhara Rao, Assoc Prof, IIT Dhanbad Dr. K. Naga Jyothi, Pro-VC, BESTIU, AP Dr. B. S Nagakishor, Registrar, MangalayatanUniv, MP Dr. P. Koteswara Rao, Assoc Prof, YV University, AP Dr. MangeshNkose, AssocProf.SandipUniv, MH

Signal Processing

W-ICAVSP-2K24

& 26th November, 2024

- Machine Learning and AI in Signal Processing
- Audio and Speech Processing
- ► Image and Video Processing
- Biomedical Signal Processing
 Communications and N/Ws
- Radar and Sonar Systems
- Adap+tive Filtering and
- Estimation
- Time-Frequency Analysis
 Security and Privacy in Signal Processing
- ➢ Quantum Signal Processing
- > Multirate Signal Processing

Registration & Account Details

Categories	Registrati	on Fees	5	
Student/ Faculty	Rs 500			
R&D/ Industry	Rs 1000			
Foreign Delegates	US \$ 50			
MANAGEMENT & T Account No (Current Bank & Branch: SBI IFSC Code: SBIN00	Account): 42 , Ghatkesar 20450	27842087		
Name of the Jou	rnal	Туре	(Rs)*	(US \$)*
Journal of Interdiscip Multidisciplinary Res		UGC Care	2000	50
Care 2 Journal, Sl No	52)	(Grp 2)		50

Important Dates

International Conference

on

Advancement in VLSI and Signal Processing

: 14 th Sept 2024
13 th Oct 2024
11 th Nov 2024
ight form submission and
18 th Nov 2024
25 th & 26 th Nov 2024

Submission Guidelines

Authors intending to present papers are requested to send soft copy of full length paper in IEEE format not exceeding 7 pages to mail id **conference.ece@vmtw.in** along with the application form. The selected papers will be notified for registration.

- > Upload the details through paper submission link
- No. of Authors per paper is limited to 4 only.
- Certificate will be provided to Authors who are present during their paper presentation.
- ISBN International Conference Proceeding will be provided with an additional charge of INR 1000 (Cost of one book).
- All submissions must include title, author(s) and affiliation(s). Full address for correspondence including phone no. and email id.
- Selected papers will be published in respective Journals as per the interest of author.

Dr. HathiramNenavath, AsstProf.,ECE,IIT Bhilai Dr. Banoth Ravi, AsstProf., ECE, IIIT Trichy Dr. T. Srinath Reddy, AsstProf., IIITDM, Kanchipuram Dr. VadthiyaNarendar, AsstProf., ECE, NIT Warangal Dr. SumitSaha, AsstProf., ECE, NIT Rourkela Dr. G. Appala Naidu, AsstProf., JNTU, Vizianagaram

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*Indian Author ** For	eign Author			Ì

Conveners

Dr. Sk. Masthan Basha Associate Professor, ECE

Contact No: +91 94416 98765 Email: masthanbasha@vmtw.in

Dr. Vijaykumar R. Urkude Professor, ECE Contact No: +91 92937 93986 Email: vijay@vmtw.in



Paper Submission Link

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Organizing committee

Mr. ThanamPullaiah, Associate Professor Mr. SunilkumarJangili, Associate Professor Mr. NagarajuEduru, Associate Professor Mrs. GangulaSwathi, Associate Professor Ms. K. Padmaleela, AssociateProfessor Mrs. G. Indira Priya Darshini, Associate Professor Mr. S. Sandeep Babu, Assistant Professor Mr. G. Ganesh Reddy, Assistant Professor Ms. SusmithaGalipelly, Assistant Professor Mr. Ashok Reddy Katta, Assistant Professor Mrs. D.Shirisha, Assistant Professor Mr. D.Naresh, Assistant Professor Mrs. K.Pravallika, AssistantProfessor

For more details visit https://vmtw.edu.in/VMTW-ICAVSP-2K24.php

















About the Institution

Vignan's Institute of Management and Technology for Women (VMTW), is the brainchild of **Dr. L Rathaiah**, **Chairman, Vignan Group of Institutions**, was founded during August 2008, with four branches of Engineering.

VMTW is an exemplary institution of higher learning with a mission of pursuing excellence in education and research. The institution, with their diverse and dynamic community of about 2500 students offers a distinctive combination of some of the finest facilities for 8 different graduate, and undergraduate programs, accomplished faculty, world class facilities with hostel set on a sprawling area of 22 acres sylvan surroundings of mango groves and greenery.

At **VMTW** students will find an academic and social environment where everyone from faculty members to peers help shape their future. **VMTW** is a home to aesthetically designed buildings with state of the-art computer and internet facilities, modern laboratories, workshops, seminar halls, auditoriums and libraries, sports and games fields.

The Institution boasts of a strong alumni network with alumni events held every year serving as a platform for past students to give back to **VMTW** and share their experiences with its present fellow students. Institute also brings out creativity in students by imparting engineering knowledge imbibing interpersonal skills to promote innovation, research and entrepreneurship.

About the Department

Department of Electronics and Communication Engineering was started since the inception of Vignan's Institute of Management and Technology for Women during 2008 with an initial intake of 90. The strength was enhanced to 120 during 2010. The Department had added Post graduate programme in VLSI during the year 2011 with intake of 18.

The Department has state-of-art laboratories equipped with advanced and well maintained equipment, continuously updated application software packages, more than adequate computing systems with 24x7, 100 MBPS internet facility. The department is run by well qualified and experienced faculty & supporting staff. *Chief-Patrons* **Dr. L. Rathaiah,** Chairman, Vignan Group of Institutions.

Sri. B. Shravan, CEO, Vignan Group of Institutions, Hyd

> Patron Dr. G. Apparao Naidu, Principal

Co-Patron Mr. P. Harikrishna, HoD, ECE

Conveners

Dr. Sk. Masthan Basha Associate Professor, ECE Contact No: +91 94416 98765 Email: masthanbasha@vmtw.in

Dr. Vijaykumar R. Urkude Professor, ECE Contact No: +91 92937 93986 Email: <u>vijay@vmtw.in</u>

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An Autonomous Institution (Affiliated to JNTU, Hyderabad)

1st International Conference

on

Advancement in VLSI and Signal Processing (VMTW-ICAVSP-2K24) (Hybrid Mode)

25th & 26th November, 2024

Organized by

DEPARTMENT OF

ELECTRONICS & COMMUNICATION ENGINEERING

VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

(Sponsored by Lavu Educational Society) Approved by AICTE, New Delhi & Affilated to JNTU, Hyderabad. Kondapur(V), Ghatkesar (M), Medchal – Malkajgiri(D) – 501 301, Phone: +9196529 10002/3 Website: http://www.vmtw.in



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- Dr. Sumit Saha, Asst Prof., ECE, NIT Rourkela

Dr. G. Appala Naidu, Asst Prof., JNTU, Vizianagaram

Registration Details

Categories	Registratio	n Fees		
Student/ Faculty	Rs 500			
R&D/ Industry	Rs 1000			
Foreign Delegates US \$ 50				
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Name of the Journal		Туре	Amount	
			(Rs)*	(US \$)**
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- ✤ Selected papers will be published in respective Journals as per the interest of author.

Who Should Attend?

The programme is open to all Faculty/ Research Scholars/ Industry Professionals/ and all eligible students.



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1st International Conference

on

Advancement in VLSI and Signal Processing (VMTW-ICAVSP-2K24)

(Hybrid Mode)

Name (in BLOCK LETERS):

Application Form

Name (in DEOCK EETERS).
Designation:
College/ Organization:
Address for Communication:
Phone No (with STD Code):
Mobile No.:
Email Id:
Title of the Paper:

Important Dates

Paper Submission opens from:	14th Sept 2024
Paper Submission closes on:	13th Oct 2024
Acceptance Intimation:	11th Nov 2024
Camera Ready Paper, Copy Right for	m submission and
Author Registration:	18th Nov 2024
Conference Dates:	25 th & 26 th Nov 2024

Declaration:

The information provided by me is true to the best of my knowledge. If selected, I agree to abide by rules and regulation of the organization and shall attend the International Conference for the entire duration. I also undertake the responsibility to inform the Convener(s), in case I am unable to attend the International Conference.

Place:

Date:

Signature of the Participant

*Indian Author

Journal of Polymer and

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Paper Submission Link

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- Estimation ≻ Time-Frequency Analysis
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> Multirate Signal Processing

- Processing > Quantum Signal Processing